SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

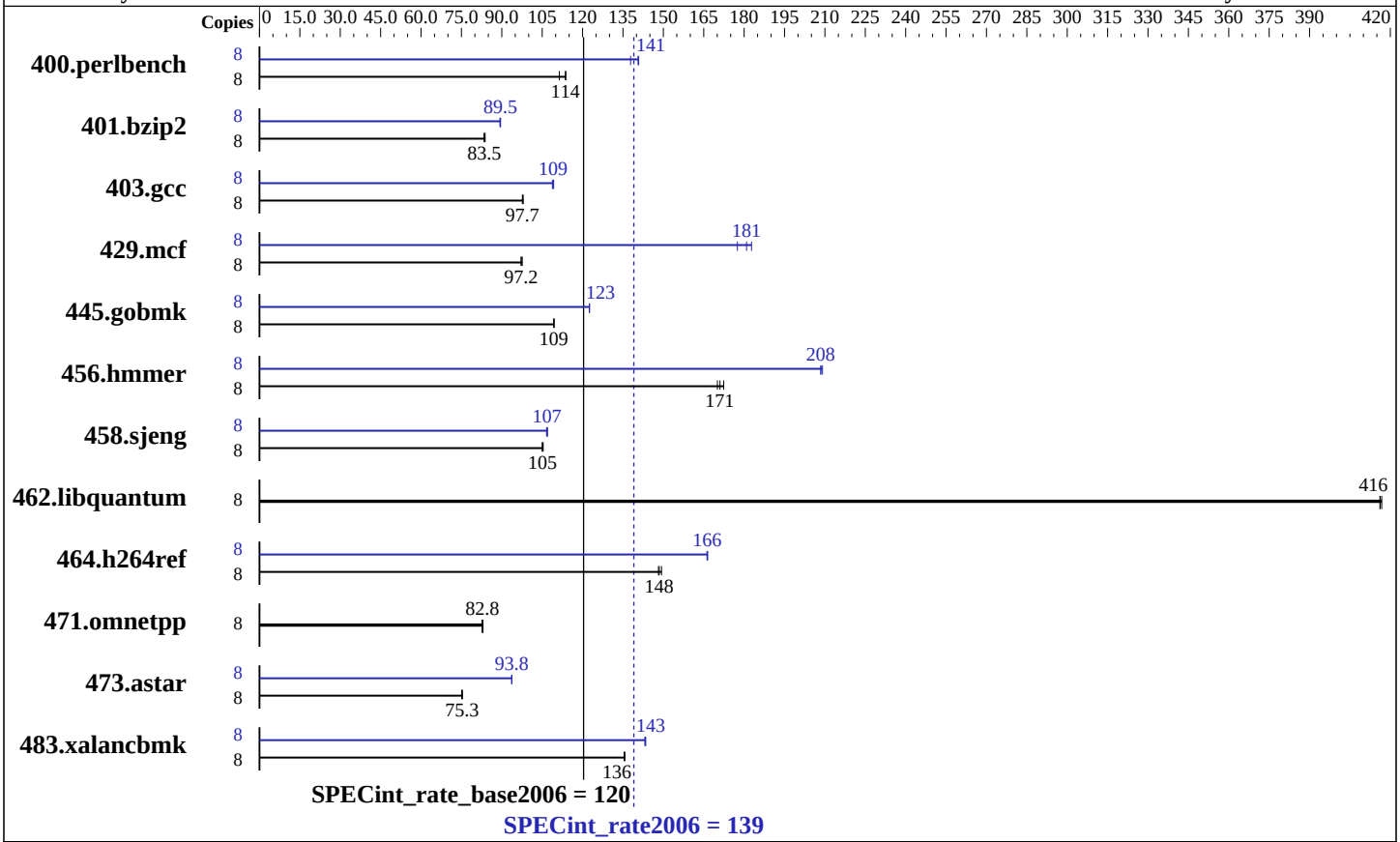
PowerEdge R515 (AMD Opteron 4122, 2.20 GHz)

SPECint®_rate2006 = 139

SPECint_rate_base2006 = 120

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2010
Hardware Availability: Jul-2010
Software Availability: Jul-2010



Hardware

CPU Name:	AMD Opteron 4122
CPU Characteristics:	
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	6 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (8 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 146 GB 10000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default
Compiler:	x86 Open64 4.2.4 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap 8.1 32-bit Library for Linux

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R515 (AMD Opteron 4122, 2.20 GHz)

SPECint_rate2006 = 139

SPECint_rate_base2006 = 120

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2010
Hardware Availability: Jul-2010
Software Availability: Jul-2010

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	702	111	687	114	**688**	**114**	8	567	138	**556**	**141**	555	141
401.bzip2	8	925	83.4	921	83.8	**924**	**83.5**	8	865	89.3	**863**	**89.5**	862	89.5
403.gcc	8	659	97.7	657	98.0	**659**	**97.7**	8	589	109	592	109	**590**	**109**
429.mcf	8	748	97.6	**750**	**97.2**	751	97.2	8	**403**	**181**	399	183	411	177
445.gobmk	8	767	109	**767**	**109**	768	109	8	685	123	**685**	**123**	685	123
456.hmmer	8	**436**	**171**	439	170	433	172	8	**358**	**208**	357	209	358	208
458.sjeng	8	922	105	**921**	**105**	919	105	8	905	107	908	107	**907**	**107**
462.libquantum	8	**398**	**416**	398	417	398	416	8	**398**	**416**	398	417	398	416
464.h264ref	8	1186	149	**1192**	**148**	1195	148	8	1064	166	**1064**	**166**	1065	166
471.omnetpp	8	605	82.7	**604**	**82.8**	602	83.0	8	605	82.7	**604**	**82.8**	602	83.0
473.astar	8	748	75.1	745	75.3	**746**	**75.3**	8	600	93.6	599	93.8	**599**	**93.8**
483.xalancbmk	8	**407**	**136**	407	136	408	135	8	386	143	**385**	**143**	385	143

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=7168 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd1002-rate-libs-revC/64:/root/cpu2006-1.1/amd1002-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64

Binaries were compiled on SLES10 SP2 with binutils 2.18
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-march=barcelona -mso -Ofast -CG:local_sched_alg=1
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small_pu=100
-HP:bdt=2m:heap=2m

C++ benchmarks:
-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on
-CG:cmp_peep=on -L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

Continued on next page

Peak Portability Flags (Continued)

401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-CG:local_sched_alg=1 -CG:unroll_fb_req=on
-HP:bdt=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

403.gcc: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
-LNO:prefetch_ahead=10 -CG:cmp_peep=on -m32
-HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on
-CG:gcm=off -GRA:prioritize_by_density=on -m32
-HP:bdt=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -ipa -IPA:plimit=750
-IPA:min_hotness=300 -IPA:pu_reorder=1 -LNO:prefetch=1
-LNO:ignore_feedback=off -CG:p2align=on
-CG:unroll_fb_req=on -HP:bdt=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=0
-OPT:alias=disjoint -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
-CG:local_sched_alg=1 -CG:cflow=0
-CG:push_pop_int_saved_regs=off -CG:cmp_peep=on
-HP:bdt=2m:heap=2m

Continued on next page

Peak Optimization Flags (Continued)

458.sjeng: `-march=barcelona -mso -fb_create fbdata`(pass 1)
`-fb_opt fbdata`(pass 2) `-O3 -ipa -LNO:ignore_feedback=off`
`-LNO:full_unroll=10 -LNO:fusion=0 -LNO:fission=2`
`-IPA:pu_reorder=2 -CG:ptr_load_use=0`
`-OPT:unroll_times_max=8 -INLINE:aggressive=on`

462.libquantum: `basepeak = yes`

464.h264ref: `-march=barcelona -mso -fb_create fbdata`(pass 1)
`-fb_opt fbdata`(pass 2) `-O3 -IPA:plimit=20000`
`-OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0`
`-CG:push_pop_int_saved_regs=off`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `-march=barcelona -mso -fb_create fbdata`(pass 1)
`-fb_opt fbdata`(pass 2) `-Ofast -TENV:frame_pointer=off`
`-WOPT:if_conv=0 -GRA:optimize_boundary=on`
`-OPT:alias=disjoint -INLINE:aggressive=on`
`-IPA:small_pu=3000 -IPA:plimit=3000 -m32`
`-HP:bdt=2m:heap=2m`

483.xalancbmk: `-march=barcelona -mso -Ofast -INLINE:aggressive=on -m32`
`-CG:cmp_peep=on -GRA:unspill=on -TENV:frame_pointer=off`
`-fno-emit-exceptions`
`-L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html
http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml
http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 14:32:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 October 2010.