SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

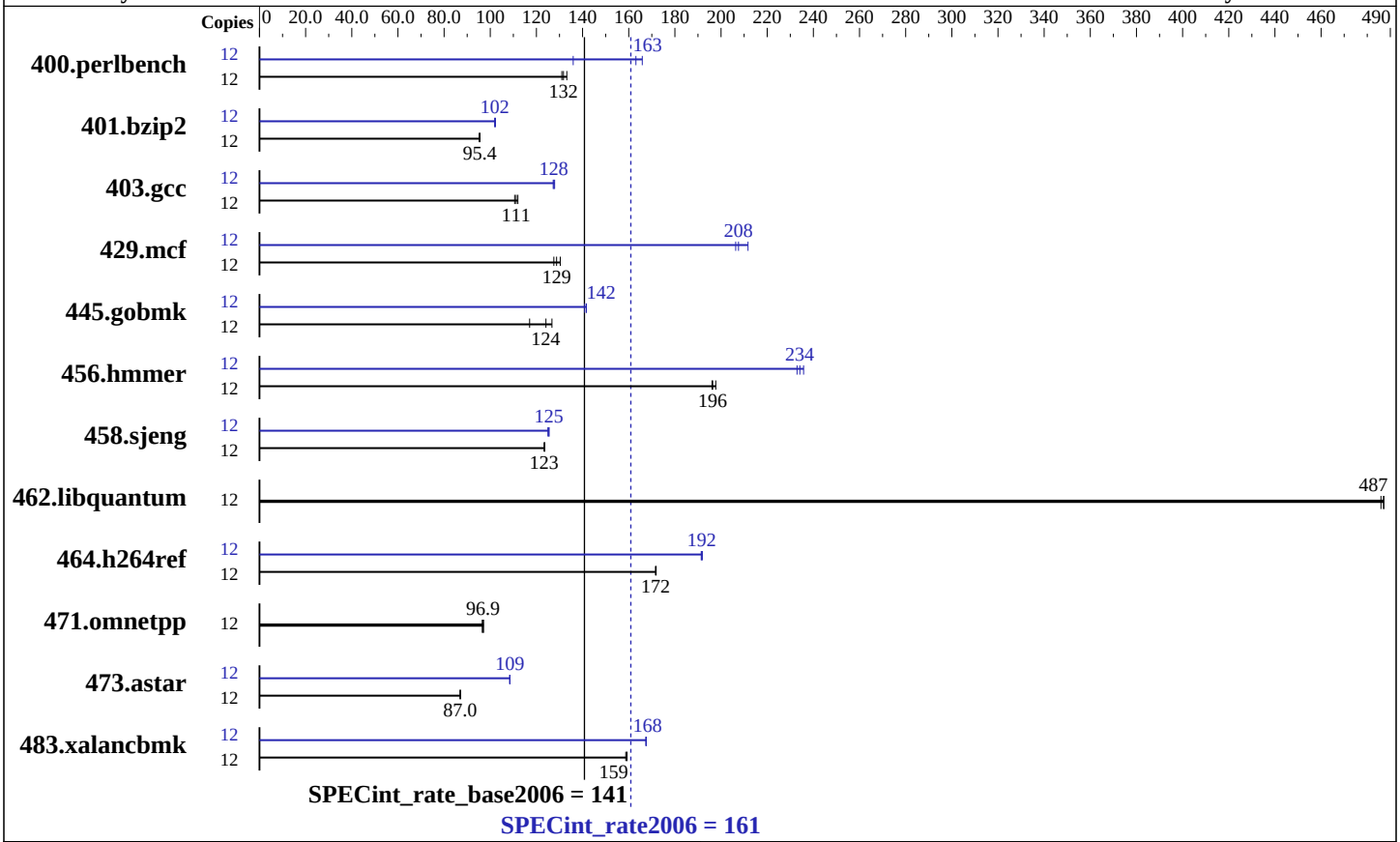
PowerEdge R415 (AMD Opteron 4162 EE, 1.70 GHz)

SPECint®_rate2006 = 161

SPECint_rate_base2006 = 141

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2010
Hardware Availability: Jul-2010
Software Availability: Jul-2010



Hardware

CPU Name:	AMD Opteron 4162 EE
CPU Characteristics:	
CPU MHz:	1700
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	6 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (8 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 146 GB 10000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default
Compiler:	x86 Open64 4.2.4 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap 8.1 32-bit Library for Linux

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R415 (AMD Opteron 4162 EE, 1.70 GHz)

SPECint_rate2006 = 161

SPECint_rate_base2006 = 141

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2010
Hardware Availability: Jul-2010
Software Availability: Jul-2010

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	880	133	895	131	**890**	**132**	12	**719**	**163**	862	136	707	166
401.bzip2	12	1217	95.2	1212	95.6	**1213**	**95.4**	12	1136	102	1132	102	**1136**	**102**
403.gcc	12	874	111	**868**	**111**	863	112	12	**757**	**128**	755	128	759	127
429.mcf	12	**850**	**129**	840	130	858	128	12	517	212	**527**	**208**	530	206
445.gobmk	12	993	127	1075	117	**1014**	**124**	12	**889**	**142**	893	141	889	142
456.hmmer	12	566	198	571	196	**570**	**196**	12	475	236	481	233	**478**	**234**
458.sjeng	12	1177	123	1175	124	**1176**	**123**	12	1162	125	1157	125	**1158**	**125**
462.libquantum	12	510	487	**510**	**487**	512	486	12	510	487	**510**	**487**	512	486
464.h264ref	12	1547	172	**1547**	**172**	1545	172	12	1384	192	**1386**	**192**	1387	191
471.omnetpp	12	**774**	**96.9**	777	96.5	773	97.1	12	**774**	**96.9**	777	96.5	773	97.1
473.astar	12	968	87.0	**968**	**87.0**	969	87.0	12	776	109	**776**	**109**	777	108
483.xalancbmk	12	522	159	**521**	**159**	520	159	12	**494**	**168**	495	167	494	168

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=5400 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "450"
LD_LIBRARY_PATH = "/cpu2006/amd1002-rate-libs-revC/64:/cpu2006/amd1002-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64

Binaries were compiled on SLES10 SP2 with binutils 2.18
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-march=barcelona -mso -Ofast -CG:local_sched_alg=1
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small_pu=100
-HP:bdt=2m:heap=2m

C++ benchmarks:
-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on
-CG:cmp_peep=on -L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

Continued on next page

Peak Portability Flags (Continued)

401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-CG:local_sched_alg=1 -CG:unroll_fb_req=on
-HP:bdt=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

403.gcc: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
-LNO:prefetch_ahead=10 -CG:cmp_peep=on -m32
-HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on
-CG:gcm=off -GRA:prioritize_by_density=on -m32
-HP:bdt=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -ipa -IPA:plimit=750
-IPA:min_hotness=300 -IPA:pu_reorder=1 -LNO:prefetch=1
-LNO:ignore_feedback=off -CG:p2align=on
-CG:unroll_fb_req=on -HP:bdt=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=0
-OPT:alias=disjoint -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
-CG:local_sched_alg=1 -CG:cflow=0
-CG:push_pop_int_saved_regs=off -CG:cmp_peep=on
-HP:bdt=2m:heap=2m

Continued on next page

Peak Optimization Flags (Continued)

458.sjeng: `-march=barcelona -mso -fb_create fbdata`(pass 1)
`-fb_opt fbdata`(pass 2) `-O3 -ipa -LNO:ignore_feedback=off`
`-LNO:full_unroll=10 -LNO:fusion=0 -LNO:fission=2`
`-IPA:pu_reorder=2 -CG:ptr_load_use=0`
`-OPT:unroll_times_max=8 -INLINE:aggressive=on`

462.libquantum: `basepeak = yes`

464.h264ref: `-march=barcelona -mso -fb_create fbdata`(pass 1)
`-fb_opt fbdata`(pass 2) `-O3 -IPA:plimit=20000`
`-OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0`
`-CG:push_pop_int_saved_regs=off`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `-march=barcelona -mso -fb_create fbdata`(pass 1)
`-fb_opt fbdata`(pass 2) `-Ofast -TENV:frame_pointer=off`
`-WOPT:if_conv=0 -GRA:optimize_boundary=on`
`-OPT:alias=disjoint -INLINE:aggressive=on`
`-IPA:small_pu=3000 -IPA:plimit=3000 -m32`
`-HP:bdt=2m:heap=2m`

483.xalancbmk: `-march=barcelona -mso -Ofast -INLINE:aggressive=on -m32`
`-CG:cmp_peep=on -GRA:unspill=on -TENV:frame_pointer=off`
`-fno-emit-exceptions`
`-L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html`
`http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml`
`http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 14:49:10 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 October 2010.