SPEC® CFP2006 Result

IBM Corporation

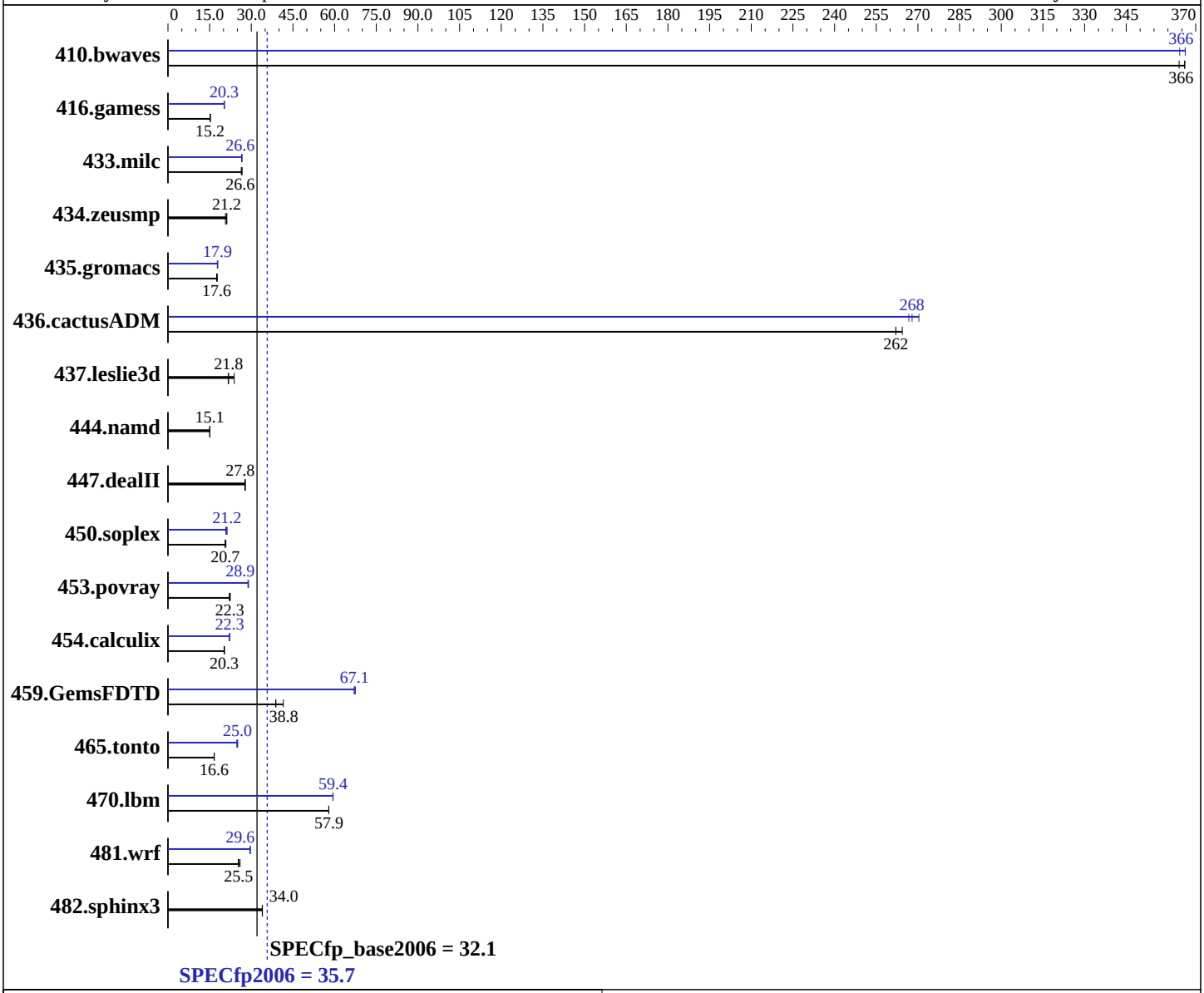
IBM BladeCenter HX5 (Intel Xeon L7545)

SPECfp®2006 = 35.7

SPECfp_base2006 = 32.1

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Aug-2010
Hardware Availability: Jun-2010
Software Availability: Jan-2010



Hardware

CPU Name:	Intel Xeon L7545
CPU Characteristics:	Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache: 18 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (32 x 8 GB PC3-8500R CL7, Quad Rank, running at 978 MHz)
Disk Subsystem: 2 x 50 GB SATA, SSD, RAID 0
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	37.1	366	**37.1**	**366**	37.3	364	37.3	364	**37.1**	**366**	37.1	366
416.gamess	1290	15.2	1284	15.3	**1288**	**15.2**	965	20.3	964	20.3	**965**	**20.3**
433.milc	348	26.4	344	26.7	**345**	**26.6**	347	26.5	344	26.7	**345**	**26.6**
434.zeusmp	440	20.7	430	21.2	**430**	**21.2**	440	20.7	430	21.2	**430**	**21.2**
435.gromacs	407	17.5	**406**	**17.6**	402	17.8	402	17.8	**399**	**17.9**	399	17.9
436.cactusADM	45.6	262	45.2	264	**45.6**	**262**	**44.6**	**268**	44.8	267	44.2	270
437.leslie3d	433	21.7	394	23.9	**431**	**21.8**	433	21.7	394	23.9	**431**	**21.8**
444.namd	534	15.0	**531**	**15.1**	531	15.1	534	15.0	**531**	**15.1**	531	15.1
447.dealII	411	27.8	413	27.7	**411**	**27.8**	411	27.8	413	27.7	**411**	**27.8**
450.soplex	**404**	**20.7**	405	20.6	400	20.9	401	20.8	392	21.3	**393**	**21.2**
453.povray	237	22.4	241	22.1	**239**	**22.3**	183	29.0	185	28.8	**184**	**28.9**
454.calculix	406	20.3	407	20.3	**406**	**20.3**	**370**	**22.3**	370	22.3	374	22.1
459.GemsFDTD	**273**	**38.8**	255	41.6	273	38.8	158	67.1	**158**	**67.1**	157	67.5
465.tonto	592	16.6	590	16.7	**591**	**16.6**	**393**	**25.0**	392	25.1	397	24.8
470.lbm	237	57.9	237	57.9	**237**	**57.9**	231	59.4	**231**	**59.4**	231	59.4
481.wrf	442	25.3	430	26.0	**438**	**25.5**	**377**	**29.6**	377	29.6	378	29.6
482.sphinx3	**573**	**34.0**	574	33.9	573	34.0	**573**	**34.0**	574	33.9	573	34.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Platform Notes

```
Turbo Boost set to Traditional
```

General Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
```

Continued on next page

General Notes (Continued)

KMP_STACKSIZE set to 200M

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Continued on next page

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-ansi-alias`

470.lbm: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `basepeak = yes`

450.soplex: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-opt-malloc-options=3 -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 12:53:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 September 2010.