# SPEC® CINT2006 Result

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## Fujitsu

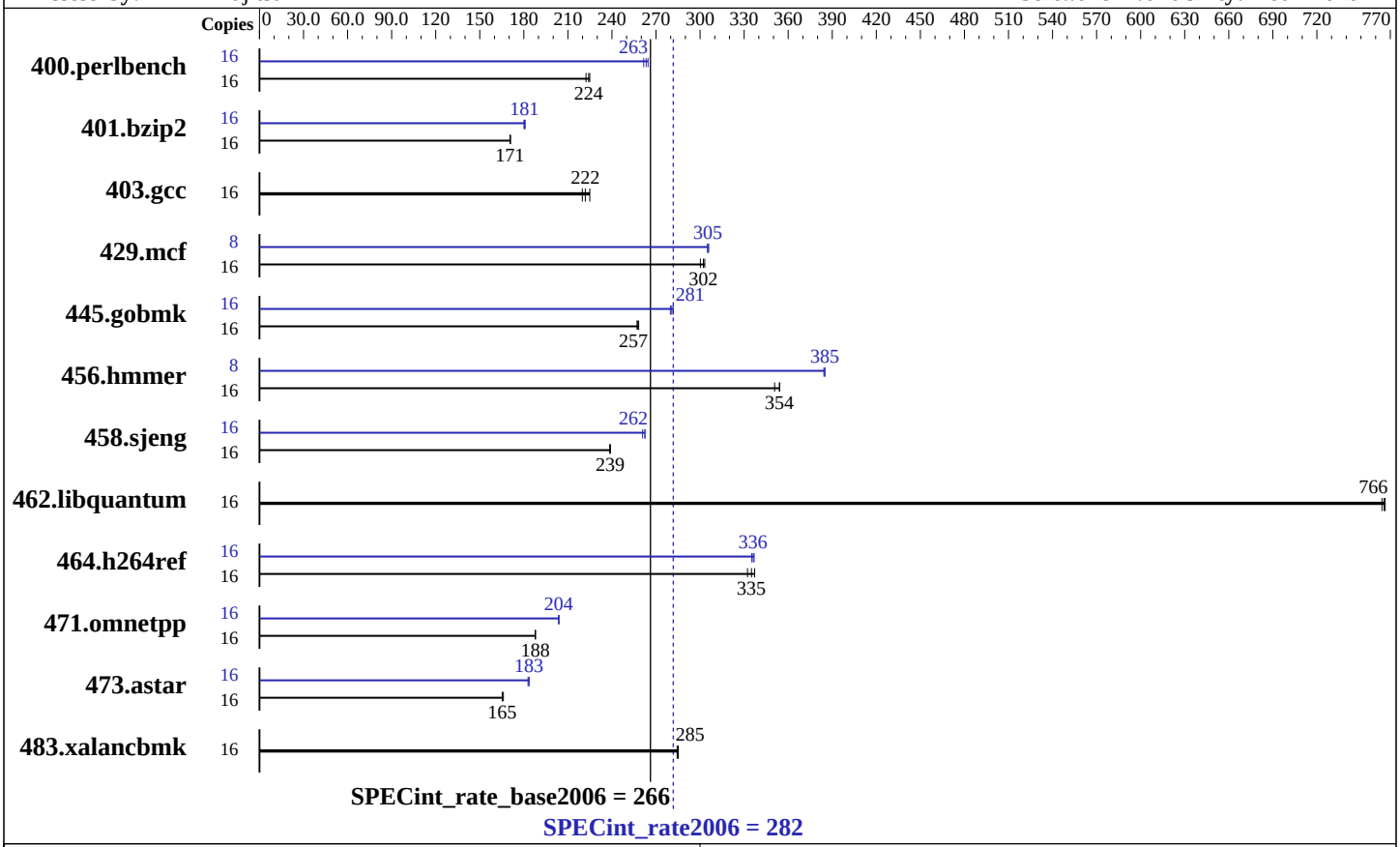
PRIMERGY BX920 S2, Intel Xeon X5667, 3.06 GHz

SPECint®_rate2006 = 282

SPECint_rate_base2006 = 266

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Aug-2010
**Hardware Availability:** Aug-2010
**Software Availability:** Jan-2010




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5667 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.46 GHz |
| CPU MHz: | 3067 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 12 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC) |
| Disk Subsystem: | 1 x SAS, 300 GB, 10000 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default |
| Compiler: | Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | **697** | **224** | 703 | 222 | 695 | 225 | 16 | **594** | **263** | 591 | 265 | 598 | 262 |
| 401.bzip2 | 16 | 903 | 171 | 905 | 171 | **905** | **171** | 16 | **855** | **181** | 857 | 180 | 854 | 181 |
| 403.gcc | 16 | **581** | **222** | 586 | 220 | 573 | 225 | 16 | **581** | **222** | 586 | 220 | 573 | 225 |
| 429.mcf | 16 | 486 | 300 | **483** | **302** | 481 | 303 | 8 | **239** | **305** | 239 | 306 | 239 | 305 |
| 445.gobmk | 16 | 653 | 257 | 650 | 258 | **652** | **257** | 16 | 596 | 282 | **598** | **281** | 600 | 280 |
| 456.hmmer | 16 | 421 | 354 | **422** | **354** | 426 | 351 | 8 | 194 | 384 | 194 | 385 | **194** | **385** |
| 458.sjeng | 16 | 812 | 239 | **811** | **239** | 811 | 239 | 16 | 738 | 262 | **738** | **262** | 742 | 261 |
| 462.libquantum | 16 | 434 | 764 | **433** | **766** | 433 | 766 | 16 | 434 | 764 | **433** | **766** | 433 | 766 |
| 464.h264ref | 16 | **1057** | **335** | 1066 | 332 | 1050 | 337 | 16 | 1057 | 335 | **1053** | **336** | 1051 | 337 |
| 471.omnetpp | 16 | 532 | 188 | 532 | 188 | **532** | **188** | 16 | 491 | 204 | **491** | **204** | 491 | 204 |
| 473.astar | 16 | 680 | 165 | 676 | 166 | **679** | **165** | 16 | 614 | 183 | 612 | 184 | **612** | **183** |
| 483.xalancbmk | 16 | **387** | **285** | 387 | 285 | 388 | 284 | 16 | **387** | **285** | 387 | 285 | 388 | 284 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

## Platform Notes

```
BIOS configuration:
Data Reuse Optimization = Disable
Performance/Power Setting = Traditional
```

## General Notes

```
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

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## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

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## Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml`


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