



SPEC[®] CINT2006 Result

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Fujitsu

SPECint[®]_rate2006 = 1010

PRIMERGY RX900 S1, Intel Xeon X7542, 2.66 GHz

SPECint_rate_base2006 = 953

CPU2006 license: 19

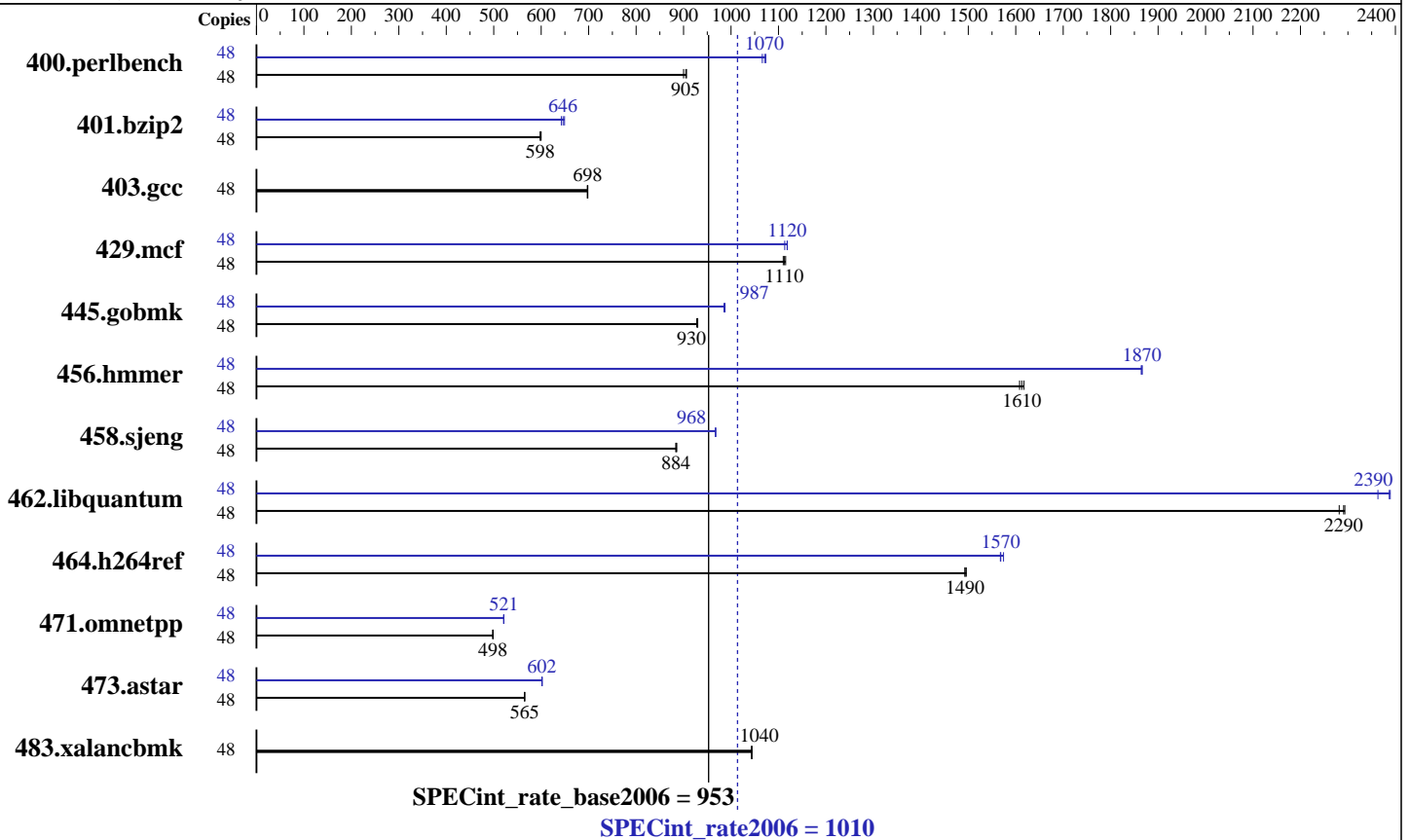
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2010

Hardware Availability: Aug-2010

Software Availability: Feb-2010



Hardware

CPU Name: Intel Xeon X7542
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 48 cores, 8 chips, 6 cores/chip
 CPU(s) orderable: 4,6,8 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 18 MB I+D on chip per chip
 Other Cache: None
 Memory: 1 TB (128 x 8 GB DDR3-1066 DIMMs)
 Disk Subsystem: 2 x 147 GB (SAS, 15000 RPM, RAID0)
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20100203 Package ID: l_cproc_p_11.1.069
 Auto Parallel: No
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	517	906	<u>518</u>	<u>905</u>	521	900	48	440	1070	<u>438</u>	<u>1070</u>	437	1070
401.bzip2	48	775	598	772	600	<u>774</u>	<u>598</u>	48	721	642	714	649	<u>717</u>	<u>646</u>
403.gcc	48	553	698	<u>554</u>	<u>698</u>	554	697	48	553	698	<u>554</u>	<u>698</u>	554	697
429.mcf	48	<u>393</u>	<u>1110</u>	393	1120	394	1110	48	393	1110	391	1120	<u>391</u>	<u>1120</u>
445.gobmk	48	541	930	543	928	<u>542</u>	<u>930</u>	48	510	987	511	985	<u>510</u>	<u>987</u>
456.hammer	48	<u>278</u>	<u>1610</u>	277	1620	278	1610	48	240	1870	<u>240</u>	<u>1870</u>	240	1860
458.sjeng	48	657	884	656	886	<u>657</u>	<u>884</u>	48	<u>600</u>	<u>968</u>	600	968	601	967
462.libquantum	48	436	2280	<u>434</u>	<u>2290</u>	434	2290	48	416	2390	<u>417</u>	<u>2390</u>	421	2360
464.h264ref	48	<u>711</u>	<u>1490</u>	710	1500	712	1490	48	<u>677</u>	<u>1570</u>	675	1570	678	1570
471.omnetpp	48	<u>602</u>	<u>498</u>	602	499	602	498	48	576	521	575	521	<u>576</u>	<u>521</u>
473.astar	48	596	565	596	565	<u>596</u>	<u>565</u>	48	<u>560</u>	<u>602</u>	559	602	560	601
483.xalancbmk	48	317	1040	<u>317</u>	<u>1040</u>	317	1040	48	317	1040	<u>317</u>	<u>1040</u>	317	1040

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

The following command was used prior to run

```
ulimit -s unlimited
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_9/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/opt/SmartHeap_9/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/opt/SmartHeap_9_64/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu.RX900.ic11.1-linux64.20100901.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Fujitsu.RX900.ic11.1-linux64.20100901.xml>

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For other inquiries, please contact webmaster@spec.org.

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