SPEC® CFP2006 Result

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IBM Corporation

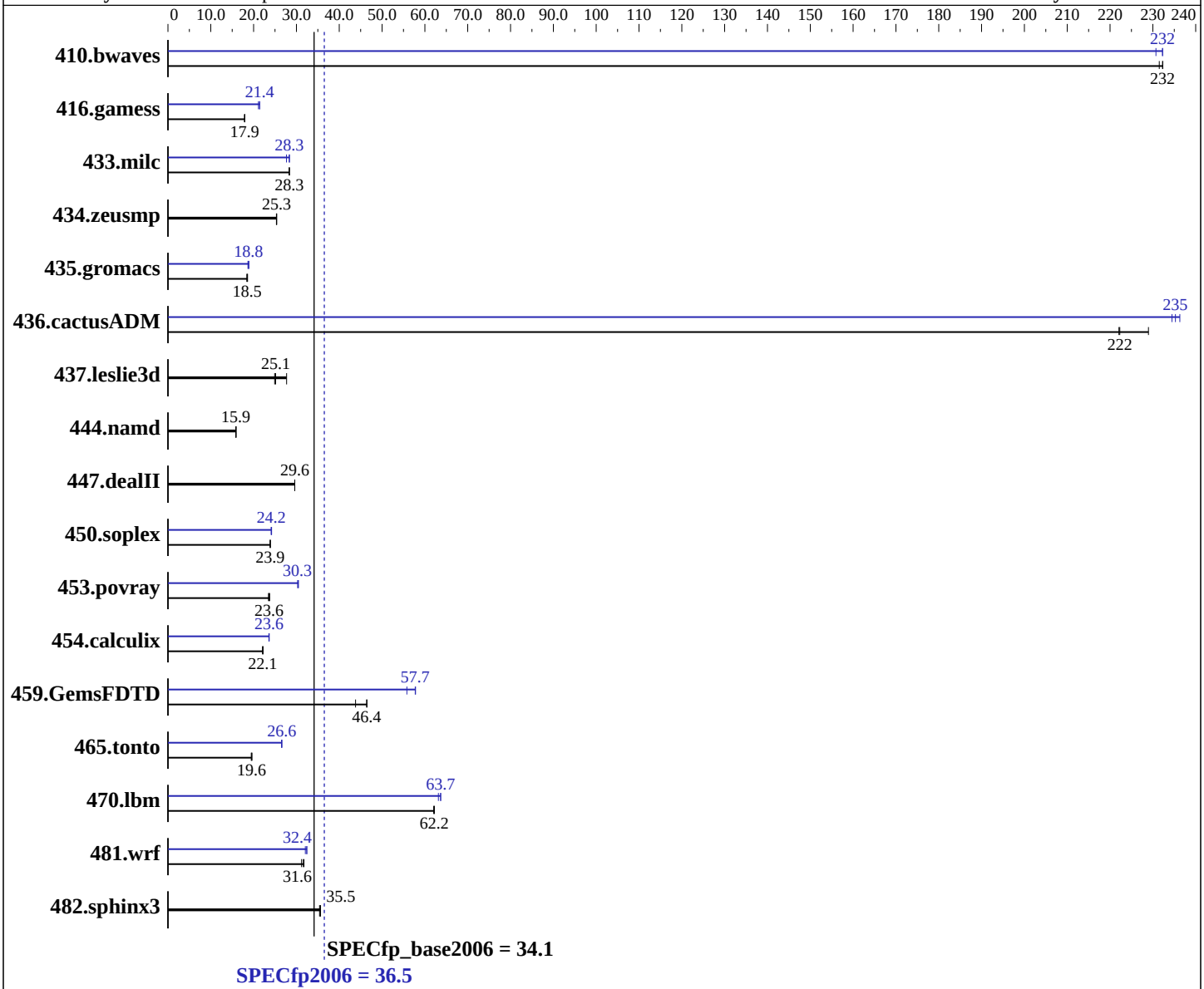
IBM BladeCenter HX5 (Intel Xeon X7560)

SPECfp®2006 = 36.5

SPECfp_base2006 = 34.1

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jul-2010
Hardware Availability: Oct-2010
Software Availability: Jan-2010



Hardware

CPU Name:	Intel Xeon X7560
CPU Characteristics:	Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB PC3-8500R CL7, Quad Rank, running at 978 MHz)
Disk Subsystem: 2 x 50 GB SATA, SSD, RAID 0
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	58.7	231	**58.5**	**232**	58.5	232	58.9	231	**58.5**	**232**	58.5	232
416.gamess	**1091**	**17.9**	1097	17.8	1090	18.0	**915**	**21.4**	915	21.4	927	21.1
433.milc	324	28.4	**324**	**28.3**	324	28.3	**324**	**28.3**	332	27.7	323	28.4
434.zeusmp	**359**	**25.3**	359	25.3	358	25.4	**359**	**25.3**	359	25.3	358	25.4
435.gromacs	388	18.4	**386**	**18.5**	384	18.6	382	18.7	378	18.9	**380**	**18.8**
436.cactusADM	52.2	229	53.8	222	**53.8**	**222**	51.0	234	50.6	236	**50.8**	**235**
437.leslie3d	377	24.9	**374**	**25.1**	339	27.7	377	24.9	**374**	**25.1**	339	27.7
444.namd	**505**	**15.9**	506	15.9	505	15.9	**505**	**15.9**	506	15.9	505	15.9
447.dealII	386	29.6	386	29.6	**386**	**29.6**	386	29.6	386	29.6	**386**	**29.6**
450.soplex	349	23.9	349	23.9	**349**	**23.9**	346	24.1	**345**	**24.2**	345	24.2
453.povray	227	23.5	224	23.8	**225**	**23.6**	175	30.5	**175**	**30.3**	175	30.3
454.calculix	371	22.2	373	22.1	**373**	**22.1**	350	23.6	**349**	**23.6**	349	23.6
459.GemsFDTD	242	43.8	**229**	**46.4**	228	46.5	**184**	**57.7**	184	57.8	190	55.8
465.tonto	506	19.5	502	19.6	**503**	**19.6**	**370**	**26.6**	370	26.6	371	26.5
470.lbm	221	62.2	**221**	**62.2**	221	62.1	216	63.7	218	63.2	**216**	**63.7**
481.wrf	352	31.8	**353**	**31.6**	358	31.2	345	32.4	348	32.1	**345**	**32.4**
482.sphinx3	551	35.4	547	35.6	**548**	**35.5**	551	35.4	547	35.6	**548**	**35.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Turbo Boost set to Traditional
```

General Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -parallel -ansi-alias -auto-ilp32

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100603.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100603.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 11:51:19 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 August 2010.