SPEC® CINT2006 Result
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NEC Corporation

Express5800/GT120b
(Intel Xeon E5506)

SPECint®_rate2006 =   75.9

SPECint_rate_base2006 = 70.9

Benchmark	Copies	base	peak
400.perlbench	4	61.9	74.1
401.bzip2	4	38.1	40.3
403.gcc	4	57.1	
429.mcf	4	89.3	89.3
445.gobmk	4	61.8	65.7
456.hmmer	4	104	119
458.sjeng	4	62.6	67.9
462.libquantum	4	240	250
464.h264ref	4	96.2	101
471.omnetpp	4	48.1	52.7
473.astar	4	39.3	43.9
483.xalancbmk	4	76.9	

SPECint_rate_base2006 = 70.9
SPECint_rate2006 = 75.9

Hardware

CPU Name:	Intel Xeon E5506
CPU Characteristics:	
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (6 x 8 GB PC3L-10600R, 2 rank, CL9, ECC, running at 800 MHz)
Disk Subsystem:	1x160 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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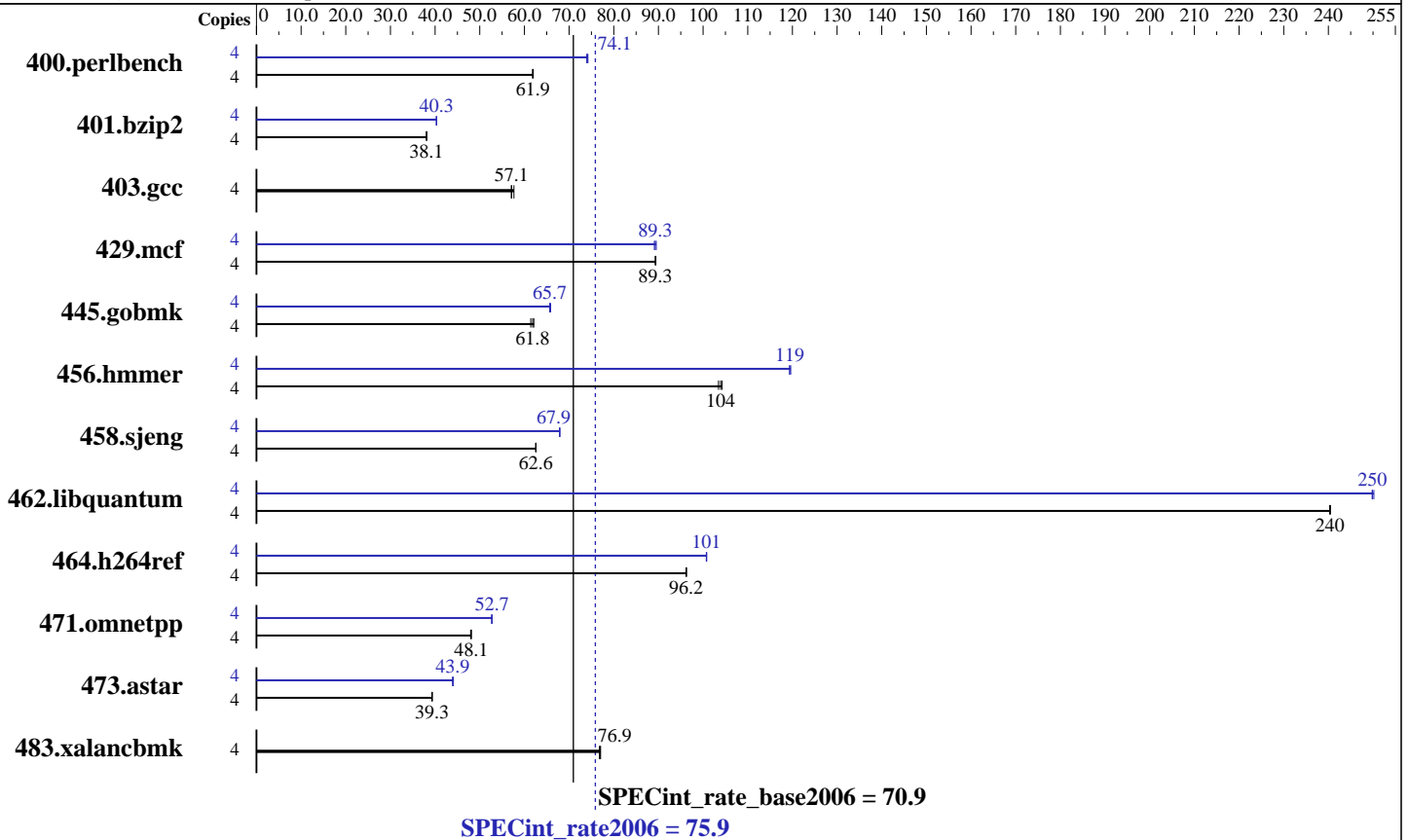
SPECint_rate_base2006 = 70.9

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Jul-2010
Hardware Availability: Jun-2010
Software Availability: Dec-2009

Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	4	631	62.0	**631**	**61.9**	632	61.8	4	528	73.9	527	74.2	**527**	**74.1**
401.bzip2	4	1013	38.1	**1014**	**38.1**	1014	38.1	4	957	40.3	958	40.3	**958**	**40.3**
403.gcc	4	564	57.1	559	57.7	**564**	**57.1**	4	564	57.1	559	57.7	**564**	**57.1**
429.mcf	4	**408**	**89.3**	409	89.3	408	89.4	4	**409**	**89.3**	410	89.1	408	89.5
445.gobmk	4	683	61.4	**678**	**61.8**	676	62.1	4	637	65.9	639	65.7	**639**	**65.7**
456.hmmer	4	358	104	361	103	**359**	**104**	4	313	119	312	120	**313**	**119**
458.sjeng	4	**774**	**62.6**	773	62.6	774	62.5	4	**713**	**67.9**	713	67.9	713	67.9
462.libquantum	4	345	240	**345**	**240**	345	240	4	**332**	**250**	331	250	332	250
464.h264ref	4	920	96.3	**920**	**96.2**	920	96.2	4	**878**	**101**	878	101	879	101
471.omnetpp	4	**520**	**48.1**	520	48.1	521	48.0	4	474	52.7	475	52.7	**474**	**52.7**
473.astar	4	**714**	**39.3**	715	39.3	713	39.4	4	639	43.9	**639**	**43.9**	637	44.1
483.xalancbmk	4	**359**	**76.9**	358	77.0	359	76.8	4	**359**	**76.9**	358	77.0	359	76.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
    Default BIOS settings were used.
```

Base Compiler Invocation

C benchmarks:
```
    icc -m32
```

C++ benchmarks:
```
    icpc -m32
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
```

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Base Portability Flags (Continued)

462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
    `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
    `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
    `-L/opt/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

        403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
    `icc -m32`

        401.bzip2: `icc -m64`

        456.hmmer: `icc -m64`

        458.sjeng: `icc -m64`

    462.libquantum: `icc -m64`

C++ benchmarks (except as noted below):
    `icpc -m32`

        473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
    401.bzip2: `-DSPEC_CPU_LP64`
    456.hmmer: `-DSPEC_CPU_LP64`

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Test date: Jul-2010

Hardware Availability: Jun-2010

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Peak Portability Flags (Continued)

```
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -ansi-alias

      401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

        403.gcc: basepeak = yes

        429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

      445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                 -ipo -no-prec-div -ansi-alias

      456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                 -ansi-alias -auto-ilp32

      458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll4 -auto-ilp32

 462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                 -opt-prefetch

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/opt/SmartHeap_8.1/lib -lsmartheap

      473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
                 -L/opt/SmartHeap_8.1/lib64 -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100721.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100721.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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