# SPEC® CINT2006 Result

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## Fujitsu

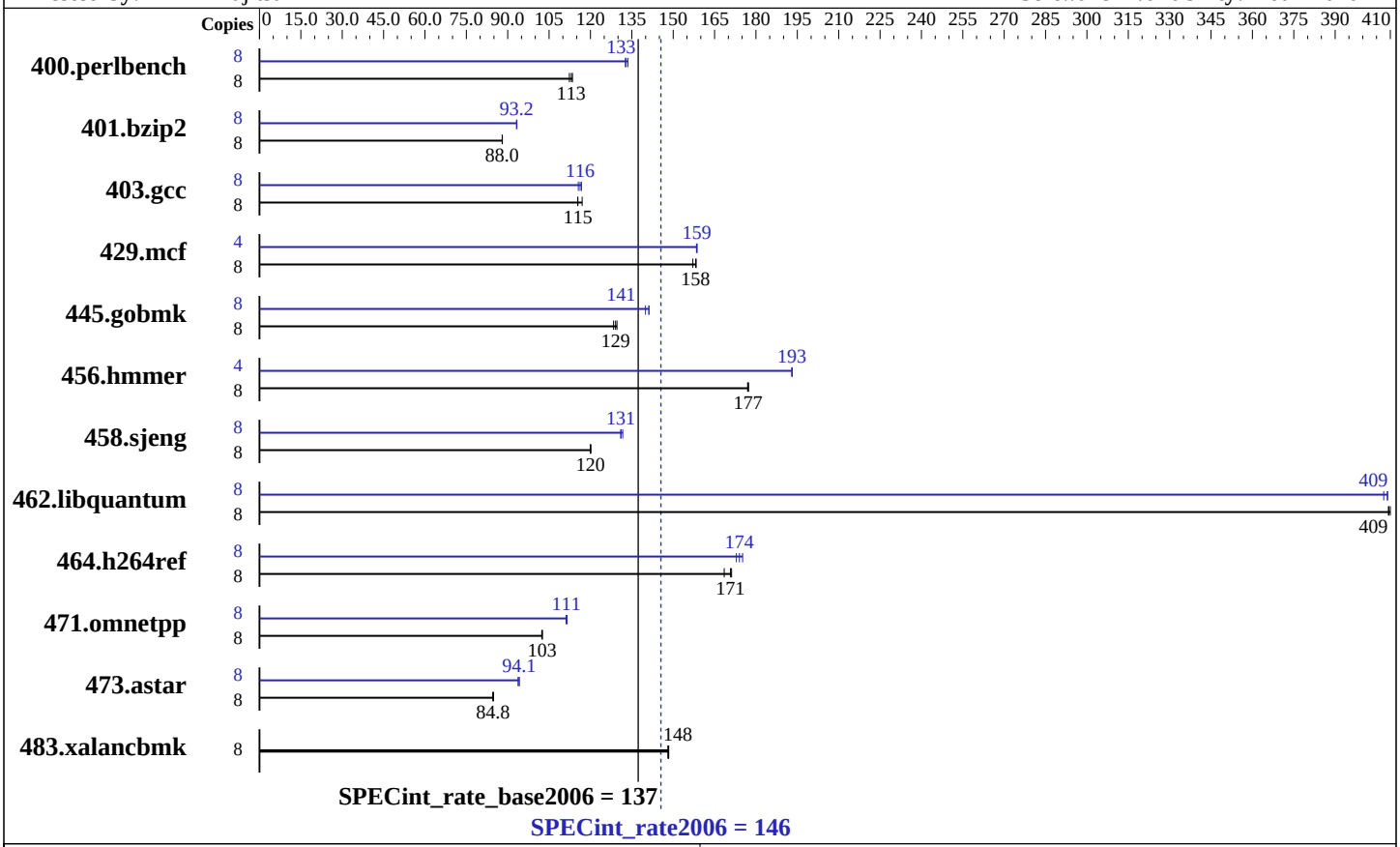
PRIMERGY BX924 S2, Intel Xeon X5667, 3.06 GHz

SPECint®_rate2006 = 146

SPECint_rate_base2006 = 137

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Jun-2010
**Hardware Availability:** Jun-2010
**Software Availability:** Jan-2010




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5667 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.46 GHz |
| CPU MHz: | 3067 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 12 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC) |
| Disk Subsystem: | 1 x SAS, 300 GB, 10 krpm |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default |
| Compiler: | Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Multi-User Run Level 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 689 | 113 | **691** | **113** | 696 | 112 | 8 | 585 | 134 | 589 | 133 | **588** | **133** |
| 401.bzip2 | 8 | **877** | **88.0** | 877 | 88.1 | 877 | 88.0 | 8 | 828 | 93.2 | **828** | **93.2** | 827 | 93.3 |
| 403.gcc | 8 | **558** | **115** | 550 | 117 | 558 | 115 | 8 | 557 | 116 | **554** | **116** | 552 | 117 |
| 429.mcf | 8 | **461** | **158** | 461 | 158 | 464 | 157 | 4 | **230** | **159** | 230 | 159 | 230 | 158 |
| 445.gobmk | 8 | **650** | **129** | 647 | 130 | 654 | 128 | 8 | **594** | **141** | 594 | 141 | 600 | 140 |
| 456.hmmer | 8 | 421 | 177 | **421** | **177** | 422 | 177 | 4 | 193 | 193 | 193 | 193 | **193** | **193** |
| 458.sjeng | 8 | **807** | **120** | 806 | 120 | 807 | 120 | 8 | **739** | **131** | 735 | 132 | 739 | 131 |
| 462.libquantum | 8 | **405** | **409** | 405 | 409 | 404 | 410 | 8 | 407 | 408 | 405 | 409 | **405** | **409** |
| 464.h264ref | 8 | 1034 | 171 | **1037** | **171** | 1051 | 169 | 8 | 1011 | 175 | **1018** | **174** | 1024 | 173 |
| 471.omnetpp | 8 | 489 | 102 | **488** | **103** | 488 | 103 | 8 | 448 | 112 | **449** | **111** | 450 | 111 |
| 473.astar | 8 | **663** | **84.8** | 662 | 84.9 | 664 | 84.6 | 8 | 599 | 93.7 | **597** | **94.1** | 596 | 94.2 |
| 483.xalancbmk | 8 | **372** | **148** | 373 | 148 | 372 | 148 | 8 | **372** | **148** | 373 | 148 | 372 | 148 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

## Platform Notes

```
BIOS configuration:
Data Reuse Optimization = Disable
Performance/Power Setting = Traditional
```

## General Notes

```
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

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## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

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## Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml`


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