SPEC® CINT2006 Result

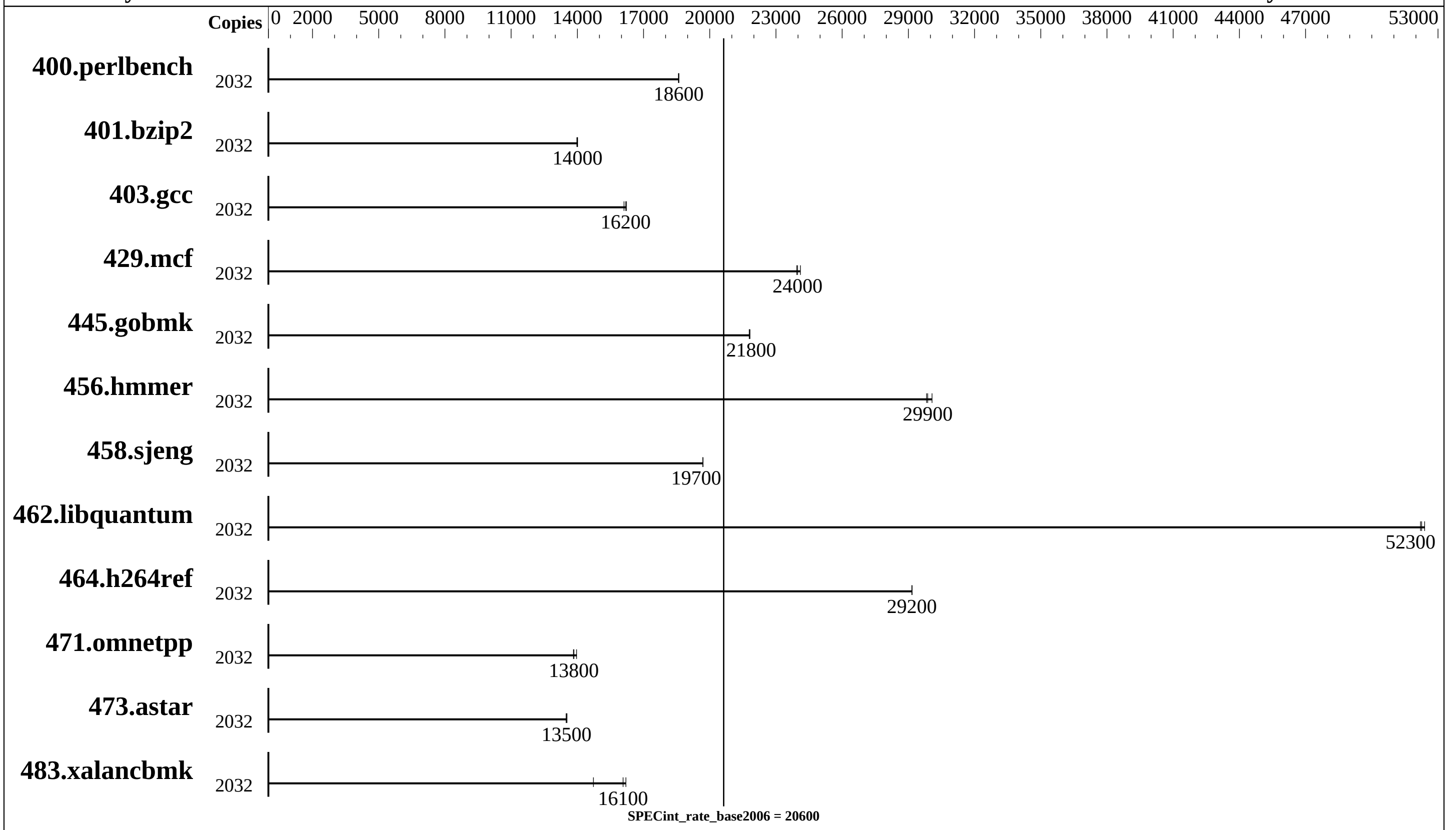
SGI

SPECint®_rate2006 = Not Run

SGI Altix UV 1000 (Intel Xeon X7560, 2.26 GHz)

SPECint_rate_base2006 = 20600

CPU2006 license:	4	**Test date:**	Jun-2010
Test sponsor:	SGI	**Hardware Availability:**	Jun-2010
Tested by:	SGI	**Software Availability:**	Jun-2010



Hardware

CPU Name:	Intel Xeon X7560
CPU Characteristics:	Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz:	2266
FPU:	Integrated
CPU(s) enabled:	1024 cores, 128 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	2-256 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	24 MB I+D on chip per chip
Other Cache:	None
Memory:	5 TB (768 x 4GB + 256 x 8GB dual-rank DDR3-1066 CL7 RDIMMs)
Disk Subsystem:	4 TB tmpfs
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7.1.1381.0.PTF-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	tmpfs
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	2032	1067	18600	1069	18600	**1068**	**18600**							
401.bzip2	2032	1400	14000	**1400**	**14000**	1403	14000							
403.gcc	2032	1008	16200	1015	16100	**1010**	**16200**							
429.mcf	2032	769	24100	774	23900	**773**	**24000**							
445.gobmk	2032	978	21800	**978**	**21800**	977	21800							
456.hmmer	2032	**635**	**29900**	630	30100	636	29800							
458.sjeng	2032	1249	19700	1249	19700	**1249**	**19700**							
462.libquantum	2032	804	52400	806	52200	**806**	**52300**							
464.h264ref	2032	1542	29200	**1542**	**29200**	1542	29200							
471.omnetpp	2032	**917**	**13800**	909	14000	919	13800							
473.astar	2032	1057	13500	1054	13500	**1056**	**13500**							
483.xalancbmk	2032	865	16200	952	14700	**872**	**16100**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
Tmpfs filesystem set up with:
  mkdir -p /mnt/shm
  mount -t tmpfs -o size=4096g,rw,mpol=interleave tmpfs /mnt/shm/
  The mpol=interleave option sets the NUMA memory allocation
  policy for all files to allocate from each node in turn.
```

General Notes

```
Memory configuration:
  The 4 GB DIMMs are attached to chips 0-95;
  the 8 GB DIMMs are attached to chips 96-127.
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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