



SPEC® CINT2006 Result

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Supermicro

Motherboard X8DAI (Intel Xeon X5677, 3.46 GHz)

SPECint®2006 = 43.1

SPECint_base2006 = 40.0

CPU2006 license: 001176

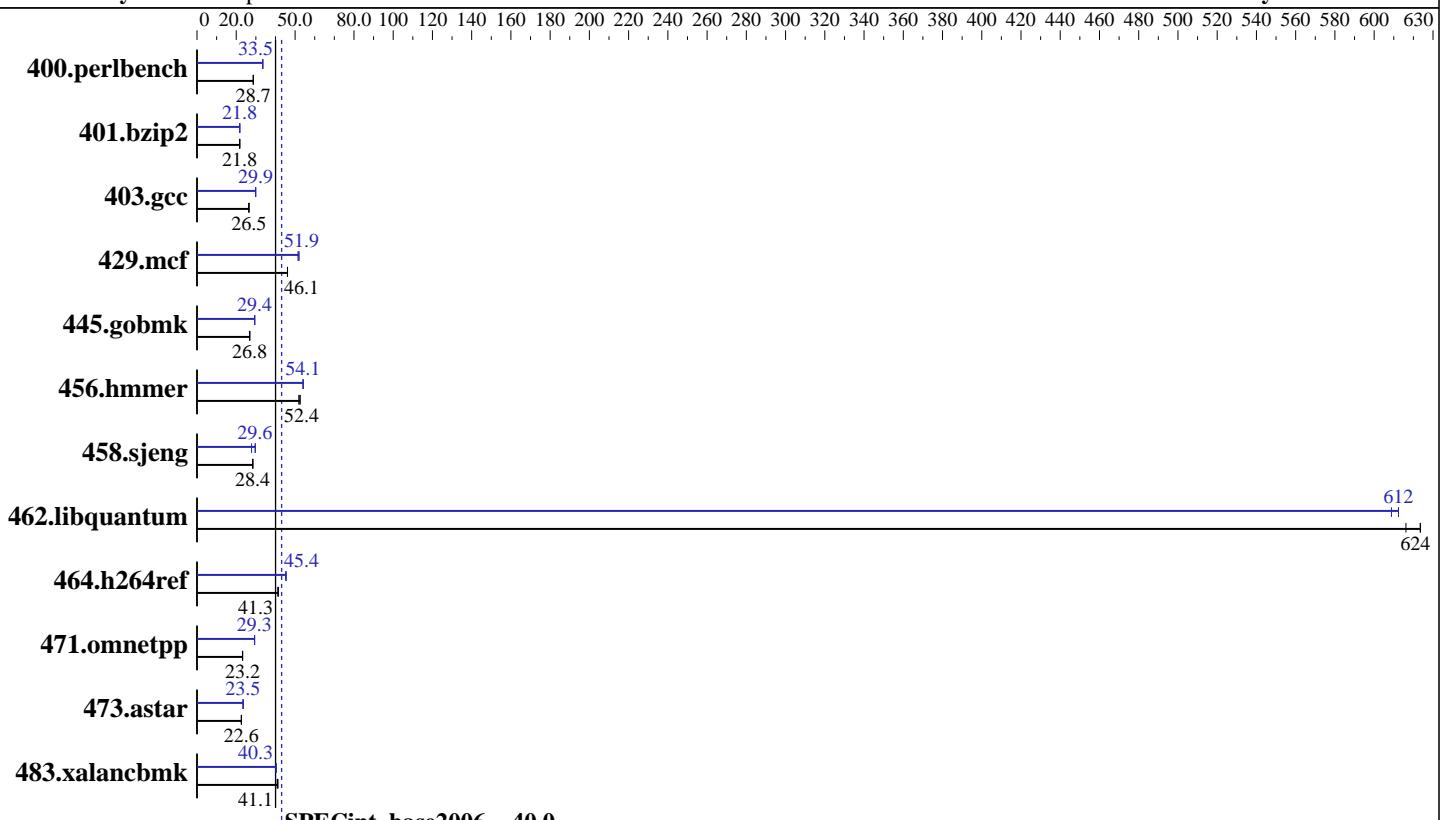
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5677
CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz
CPU MHz: 3467
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB DDR3-1333 RDIMM, ECC, CL9)
Disk Subsystem: 1 x 1000 GB SATA II, 7200 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
Compiler: Kernel 2.6.27.19-5-default
Auto Parallel: Intel C++ Professional Compiler for IA32 and
File System: Intel 64, Version 11.1
System State: Build 20091130 Package ID: l_cproc_p_11.1.064
Base Pointers: Yes
Peak Pointers: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	341	28.7	340	28.7	340	28.7	291	33.5	291	33.5	291	33.5
401.bzip2	443	21.8	443	21.8	443	21.8	442	21.8	443	21.8	443	21.8
403.gcc	305	26.4	304	26.5	304	26.5	269	29.9	269	29.9	269	29.9
429.mcf	198	46.1	198	46.1	198	46.1	177	51.5	176	51.9	175	52.0
445.gobmk	391	26.8	390	26.9	391	26.8	357	29.4	357	29.4	357	29.4
456.hmmer	177	52.6	178	52.4	180	51.9	173	54.0	172	54.1	172	54.2
458.sjeng	426	28.4	426	28.4	425	28.4	408	29.6	435	27.8	407	29.7
462.libquantum	33.6	616	33.2	624	33.2	624	33.8	612	34.0	609	33.8	612
464.h264ref	533	41.5	536	41.3	535	41.3	487	45.4	489	45.2	488	45.4
471.omnetpp	270	23.2	270	23.2	268	23.3	213	29.3	213	29.3	212	29.4
473.astar	311	22.6	310	22.7	310	22.6	300	23.4	298	23.5	298	23.6
483.xalancbmk	167	41.2	168	41.1	169	40.9	171	40.2	171	40.4	171	40.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter

Platform Notes

Fan speed set to Full Speed in BIOS Setup.

As tested, the system used a Supermicro CSE-743TQ-865B chassis and a Maxtrox G550 graphics card.
 The chassis is configured with a PWS-865-PQ power supply, 2 SNK-P0038P heatsinks,
 as well as 4 FAN-0074L and 2 FAN-0082L4 cooling fans.

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks (except as noted below):

icc -m64

C++ benchmarks:

icpc -m64



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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64  
    403.gcc: -DSPEC_CPU_LP64  
    429.mcf: -DSPEC_CPU_LP64  
    445.gobmk: -DSPEC_CPU_LP64  
    456.hammer: -DSPEC_CPU_LP64  
    458.sjeng: -DSPEC_CPU_LP64  
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX  
    464.h264ref: -DSPEC_CPU_LP64  
    471.omnetpp: -DSPEC_CPU_LP64  
    473.astar: -DSPEC_CPU_LP64  
    483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
                  -opt-prefetch  
  
    403.gcc: Same as 400.perlbench  
  
    429.mcf: Same as 400.perlbench  
  
    445.gobmk: Same as 400.perlbench  
  
    456.hammer: Same as 400.perlbench  
  
    458.sjeng: Same as 400.perlbench  
  
    462.libquantum: Same as 400.perlbench  
  
    464.h264ref: Same as 400.perlbench
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl11.1/libicl11.1-64bit -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
-auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4_2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4_2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4_2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

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Peak Optimization Flags (Continued)

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll12
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll14

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmpllr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmpllr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-Wl,-z,muldefs
-L/home/cmpllr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.xml>



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