SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5680, 3.33 GHz

$SPECint®_rate2006 = $ 378

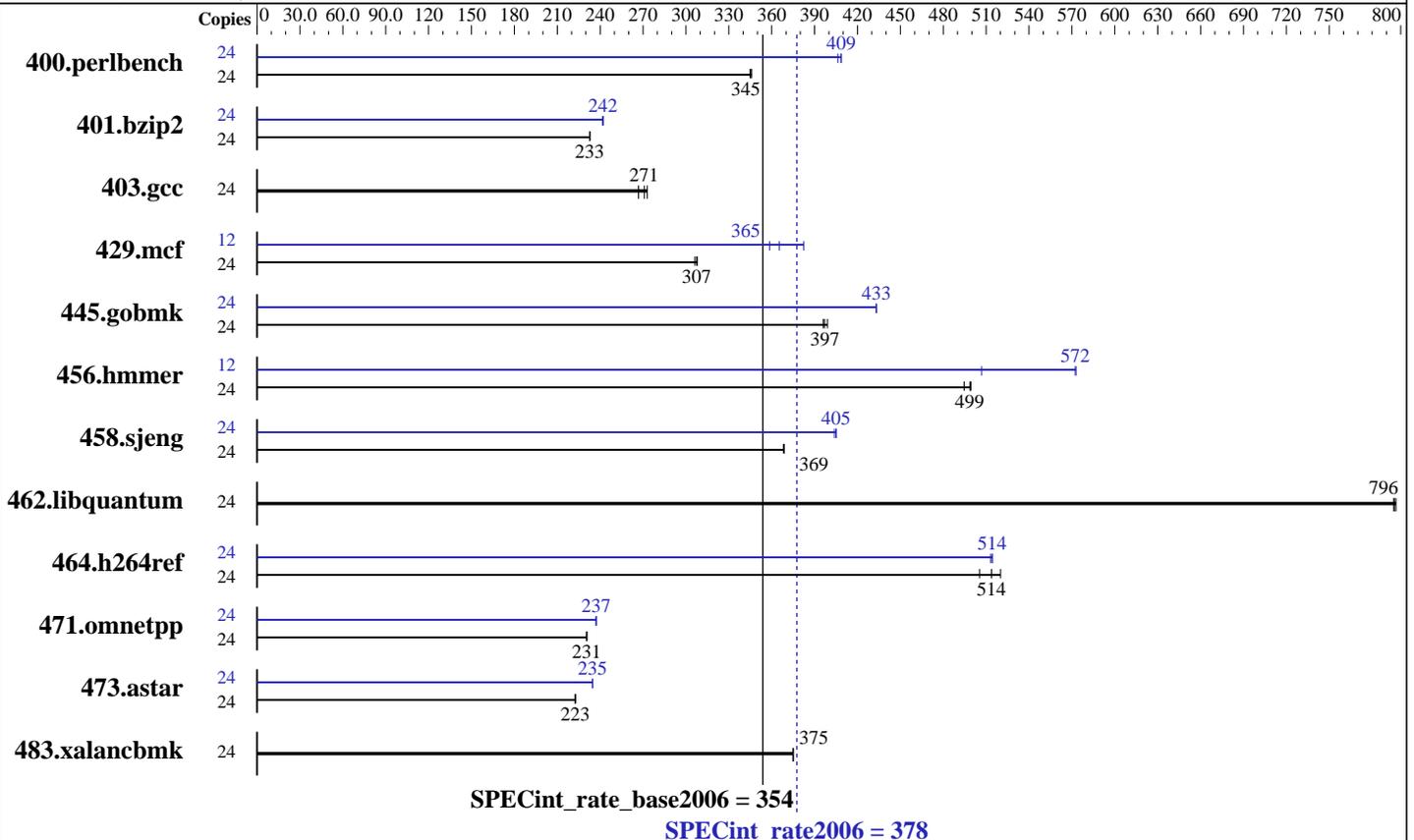$SPECint_rate_base2006 = $ 354

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2010
Hardware Availability: Jun-2010
Software Availability: Jan-2010

	Copies		
400.perlbench	24		409
	24		345
401.bzip2	24		242
	24		233
403.gcc	24		271
429.mcf	12		365
	24		307
445.gobmk	24		433
	24		397
456.hmmer	12		572
	24		499
458.sjeng	24		405
	24		369
462.libquantum	24		796
464.h264ref	24		514
	24		514
471.omnetpp	24		237
	24		231
473.astar	24		235
	24		223
483.xalancbmk	24		375

SPECint_rate_base2006 = 354

SPECint_rate2006 = 378

Hardware

CPU Name:	Intel Xeon X5680
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SAS, 300 GB, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5680, 3.33 GHz

SPECint_rate2006 = 378

SPECint_rate_base2006 = 354

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2010
Hardware Availability: Jun-2010
Software Availability: Jan-2010

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	680	345	**_679_**	**_345_**	678	346	24	577	406	**_574_**	**_409_**	574	409
401.bzip2	24	994	233	**_996_**	**_233_**	996	233	24	958	242	956	242	**_957_**	**_242_**
403.gcc	24	**_713_**	**_271_**	708	273	724	267	24	**_713_**	**_271_**	708	273	724	267
429.mcf	24	**_712_**	**_307_**	715	306	711	308	12	305	359	**_300_**	**_365_**	286	382
445.gobmk	24	636	396	**_634_**	**_397_**	631	399	24	582	433	**_581_**	**_433_**	581	433
456.hmmer	24	**_449_**	**_499_**	453	495	448	499	12	221	507	195	573	**_196_**	**_572_**
458.sjeng	24	789	368	787	369	**_788_**	**_369_**	24	717	405	719	404	**_717_**	**_405_**
462.libquantum	24	625	795	**_625_**	**_796_**	624	797	24	625	795	**_625_**	**_796_**	624	797
464.h264ref	24	**_1034_**	**_514_**	1021	520	1051	505	24	1033	514	**_1033_**	**_514_**	1035	513
471.omnetpp	24	**_651_**	**_231_**	650	231	651	230	24	**_632_**	**_237_**	632	237	632	237
473.astar	24	756	223	757	222	**_757_**	**_223_**	24	717	235	**_718_**	**_235_**	718	234
483.xalancbmk	24	442	375	**_442_**	**_375_**	441	375	24	442	375	**_442_**	**_375_**	441	375

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    BIOS configuration:
    Data Reuse Optimization = Disable
    Performance/Power Setting = Traditional

General Notes

    For information about Fujitsu please visit: http://www.fujitsu.com
    Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
     icc -m32

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5680, 3.33 GHz

SPECint_rate2006 =   378

SPECint_rate_base2006 =   354

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2010

Hardware Availability: Jun-2010

Software Availability: Jan-2010

Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

      400.perlbench: -DSPEC_CPU_LINUX_IA32
      462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

      401.bzip2: icc -m64

    456.hmmer: icc -m64

      458.sjeng: icc -m64

C++ benchmarks (except as noted below):
    icpc -m32

      473.astar: icpc -m64

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5680, 3.33 GHz

SPECint_rate2006 =   378

SPECint_rate_base2006 = 354

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2010
Hardware Availability: Jun-2010
Software Availability: Jan-2010

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

   401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

     403.gcc: basepeak = yes

     429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

   445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
               -ipo -no-prec-div -ansi-alias

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias -auto-ilp32

   458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

SPEC CINT2006 Result

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5680, 3.33 GHz

SPECint_rate2006 =   378

SPECint_rate_base2006 = 354

CPU2006 license: 19	**Test date:** May-2010
Test sponsor: Fujitsu	**Hardware Availability:** Jun-2010
Tested by: Fujitsu	**Software Availability:** Jan-2010

Peak Optimization Flags (Continued)

  473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
    `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
    `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
    `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

  483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

  403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml

SPEC and SPECint are registered trademarks of the Standard Performance
Evaluation Corporation. All other brand and product names appearing in
this result are trademarks or registered trademarks of their respective
holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 13:12:22 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 July 2010.