SPEC® CFP2006 Result
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Acer Incorporated

Acer AT150 F1 (Intel Xeon E5502)

$SPECfp®\_rate2006 = \quad 71.5$

$SPECfp\_rate\_base2006 = \quad 69.3$
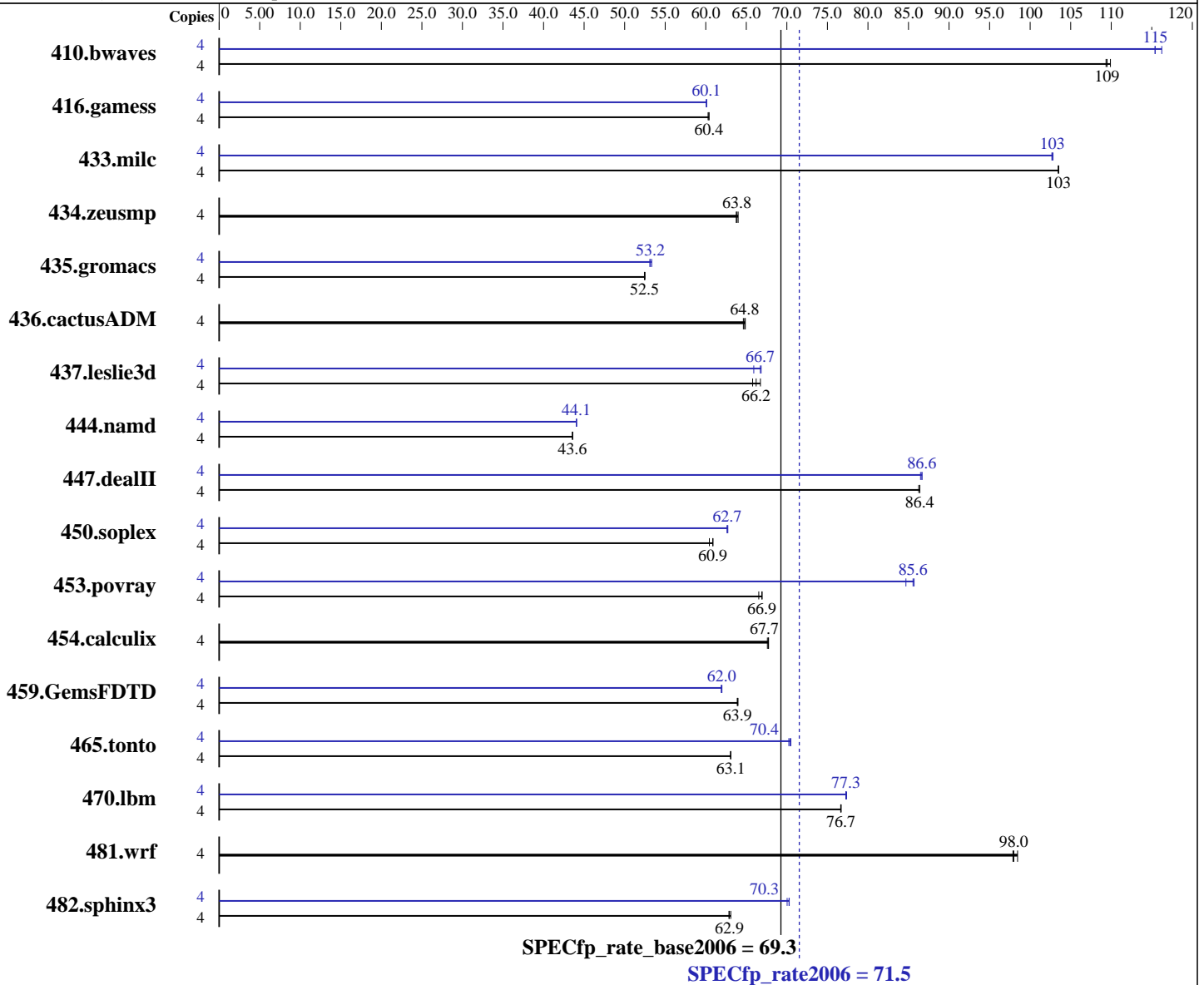
CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2010

Hardware Availability: May-2010

Software Availability: Jan-2010

Copies	Value
410.bwaves	4 → 115
	4 → 109
416.gamess	4 → 60.1
	4 → 60.4
433.milc	4 → 103
	4 → 103
434.zeusmp	4 → 63.8
435.gromacs	4 → 53.2
	4 → 52.5
436.cactusADM	4 → 64.8
437.leslie3d	4 → 66.7
	4 → 66.2
444.namd	4 → 44.1
	4 → 43.6
447.dealII	4 → 86.6
	4 → 86.4
450.soplex	4 → 62.7
	4 → 60.9
453.povray	4 → 85.6
	4 → 66.9
454.calculix	4 → 67.7
459.GemsFDTD	4 → 62.0
	4 → 63.9
465.tonto	4 → 70.4
	4 → 63.1
470.lbm	4 → 77.3
	4 → 76.7
481.wrf	4 → 98.0
482.sphinx3	4 → 70.3
	4 → 62.9

SPECfp_rate_base2006 = 69.3

SPECfp_rate2006 = 71.5

Hardware

CPU Name: Intel Xeon E5502

CPU Characteristics:

CPU MHz: 1866

FPU: Integrated

CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip

CPU(s) orderable: 1, 2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5

Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064

Auto Parallel: No

File System: ReiserFS

System State: Run level 3 (multi-user)

Continued on next page

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L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (12 x 2GB DDR3-1333 RDIMM, running at 800 MHz)
Disk Subsystem:	1000 GB SATAII, 7200 RPM
Other Hardware:	None

Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	495	110	**496**	**109**	497	109	4	471	115	468	116	**471**	**115**
416.gamess	4	**1297**	**60.4**	1299	60.3	1297	60.4	4	1304	60.1	**1303**	**60.1**	1303	60.1
433.milc	4	**355**	**103**	355	104	355	103	4	357	103	**357**	**103**	358	103
434.zeusmp	4	**570**	**63.8**	569	64.0	571	63.8	4	**570**	**63.8**	569	64.0	571	63.8
435.gromacs	4	**544**	**52.5**	545	52.4	544	52.5	4	535	53.3	538	53.1	**537**	**53.2**
436.cactusADM	4	**738**	**64.8**	740	64.6	737	64.9	4	**738**	**64.8**	740	64.6	737	64.9
437.leslie3d	4	572	65.8	563	66.7	**568**	**66.2**	4	570	65.9	563	66.8	**563**	**66.7**
444.namd	4	736	43.6	736	43.6	**736**	**43.6**	4	728	44.1	**728**	**44.1**	728	44.1
447.dealII	4	530	86.4	**530**	**86.4**	531	86.3	4	528	86.7	529	86.5	**528**	**86.6**
450.soplex	4	548	60.9	**548**	**60.9**	552	60.4	4	533	62.6	**532**	**62.7**	532	62.7
453.povray	4	318	67.0	**318**	**66.9**	320	66.5	4	**249**	**85.6**	248	85.7	251	84.7
454.calculix	4	488	67.6	487	67.7	**487**	**67.7**	4	488	67.6	487	67.7	**487**	**67.7**
459.GemsFDTD	4	664	63.9	663	64.0	**664**	**63.9**	4	685	62.0	686	61.9	**685**	**62.0**
465.tonto	4	624	63.1	**624**	**63.1**	624	63.1	4	**559**	**70.4**	560	70.2	558	70.5
470.lbm	4	**717**	**76.7**	717	76.7	717	76.6	4	710	77.4	**711**	**77.3**	711	77.3
481.wrf	4	456	97.9	**456**	**98.0**	454	98.4	4	456	97.9	**456**	**98.0**	454	98.4
482.sphinx3	4	**1239**	**62.9**	1240	62.9	1236	63.1	4	1109	70.3	1113	70.0	**1110**	**70.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
    'ulimit -s unlimited' was set for stacksize unlimited
```

General Notes

```
    Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

    This result was measured on the Gateway GT150 F1.
    The Acer AT150 F1 and Gateway GT150 F1 are electronically equivalent.
```

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Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
  410.bwaves: -DSPEC_CPU_LP64
  416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
   447.dealII: -DSPEC_CPU_LP64
   450.soplex: -DSPEC_CPU_LP64
   453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
     470.lbm: -DSPEC_CPU_LP64
     481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

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Peak Compiler Invocation

C benchmarks (except as noted below):
        `icc    -m64`

        482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
        `icpc   -m64`

         450.soplex: `icpc -m32`

Fortran benchmarks:
        `ifort -m64`

Benchmarks using both Fortran and C:
        `icc    -m64 ifort -m64`

Peak Portability Flags

        410.bwaves: `-DSPEC_CPU_LP64`
        416.gamess: `-DSPEC_CPU_LP64`
          433.milc: `-DSPEC_CPU_LP64`
        434.zeusmp: `-DSPEC_CPU_LP64`
       435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
      436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
       437.leslie3d: `-DSPEC_CPU_LP64`
          444.namd: `-DSPEC_CPU_LP64`
         447.dealII: `-DSPEC_CPU_LP64`
         453.povray: `-DSPEC_CPU_LP64`
        454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
      459.GemsFDTD: `-DSPEC_CPU_LP64`
          465.tonto: `-DSPEC_CPU_LP64`
           470.lbm: `-DSPEC_CPU_LP64`
           481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

        433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
                `-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)`
                `-fno-alias -opt-prefetch`

        470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
                `-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)`
                `-opt-malloc-options=3 -ansi-alias -auto-ilp32`

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml

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