



SPEC® CFP2006 Result

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Supermicro

SPECfp®2006 = 37.8

Motherboard X8DTE-F (Intel Xeon E5630, 2.53 GHz)

SPECfp_base2006 = 35.5

CPU2006 license: 001176

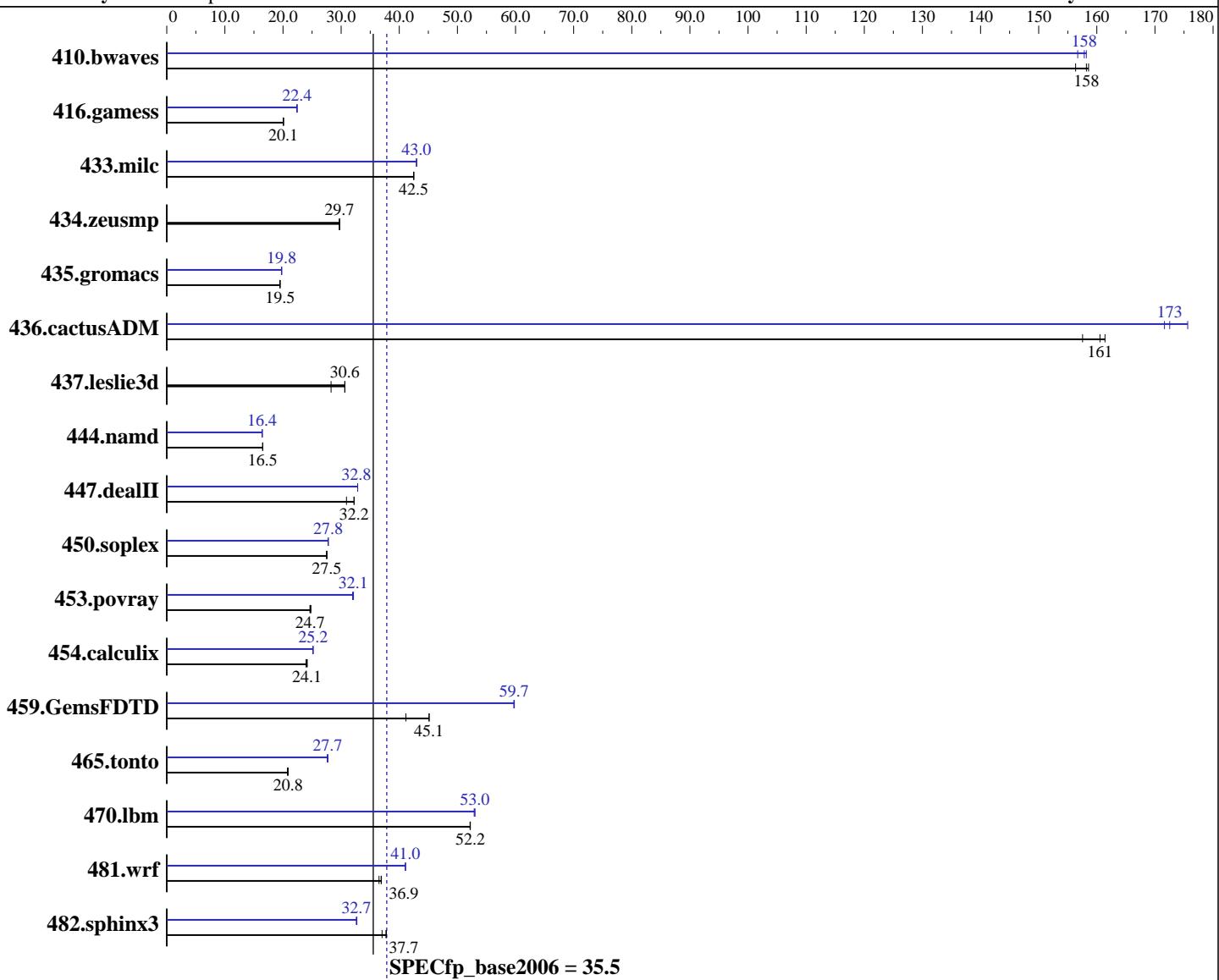
Test date: May-2010

Test sponsor: Supermicro

Hardware Availability: Mar-2010

Tested by: Supermicro

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E5630
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2533
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
Compiler: Kernel 2.6.27.19-5-default
Auto Parallel: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
File System: Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
System State: Yes
ext3
Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip

Other Cache: None

Memory: 48 GB (12 x 4 GB DDR3-1066 RDIMM, ECC, CL7)

Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM

Other Hardware: None

Base Pointers: 64-bit

Peak Pointers: 32/64-bit

Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	86.9	156	85.9	158	85.7	159	86.7	157	86.1	158	85.9	158
416.gamess	975	20.1	976	20.1	976	20.1	873	22.4	875	22.4	873	22.4
433.milc	216	42.5	216	42.5	216	42.5	214	43.0	214	43.0	214	43.0
434.zeusmp	306	29.7	306	29.7	306	29.7	306	29.7	306	29.7	306	29.7
435.gromacs	367	19.4	366	19.5	366	19.5	362	19.7	361	19.8	361	19.8
436.cactusADM	74.0	161	75.8	158	74.4	161	68.0	176	69.6	172	69.3	173
437.leslie3d	333	28.3	307	30.6	307	30.6	333	28.3	307	30.6	307	30.6
444.namd	486	16.5	486	16.5	486	16.5	488	16.4	489	16.4	488	16.4
447.dealII	355	32.2	370	30.9	355	32.2	348	32.8	348	32.8	348	32.8
450.soplex	303	27.5	303	27.6	303	27.5	301	27.8	300	27.8	301	27.8
453.povray	216	24.7	214	24.8	216	24.6	166	32.1	166	32.1	166	32.0
454.calculix	342	24.1	342	24.1	344	24.0	328	25.2	328	25.2	328	25.1
459.GemsFDTD	235	45.1	258	41.1	235	45.2	178	59.7	178	59.7	177	59.8
465.tonto	474	20.8	472	20.9	472	20.8	355	27.7	356	27.6	355	27.7
470.lbm	263	52.2	263	52.2	263	52.2	259	53.0	259	53.0	260	52.9
481.wrf	302	36.9	303	36.9	306	36.5	272	41.0	272	41.1	272	41.0
482.sphinx3	516	37.8	517	37.7	526	37.0	597	32.7	598	32.6	596	32.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M

Platform Notes

Fan speed set to Full Speed in BIOS Setup.

As tested, the system used a Supermicro CSE-743TQ-865B chassis.

The chassis is configured with a PWS-865-PQ power supply, 2 SNK-P0038P heatsinks, as well as 4 FAN-0074L and 2 FAN-0082L4 cooling fans.



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General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -ansi-alias
```

```
470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -parallel -ansi-alias -auto-ilp32
```

```
482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
              -unroll2
```

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll2 -ansi-alias -scalar-rep -auto-ilp32
```

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Peak Optimization Flags (Continued)

450.soplex: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-malloc-options=3 -auto-ilp32

453.povray: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsse4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
 -parallel

416.gamess: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll12 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll12 -Ob0 -opt-prefetch -parallel

465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -inline-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32

436.cactusADM: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll12 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xsse4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.xml>

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