SPEC® CINT2006 Result

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Supermicro

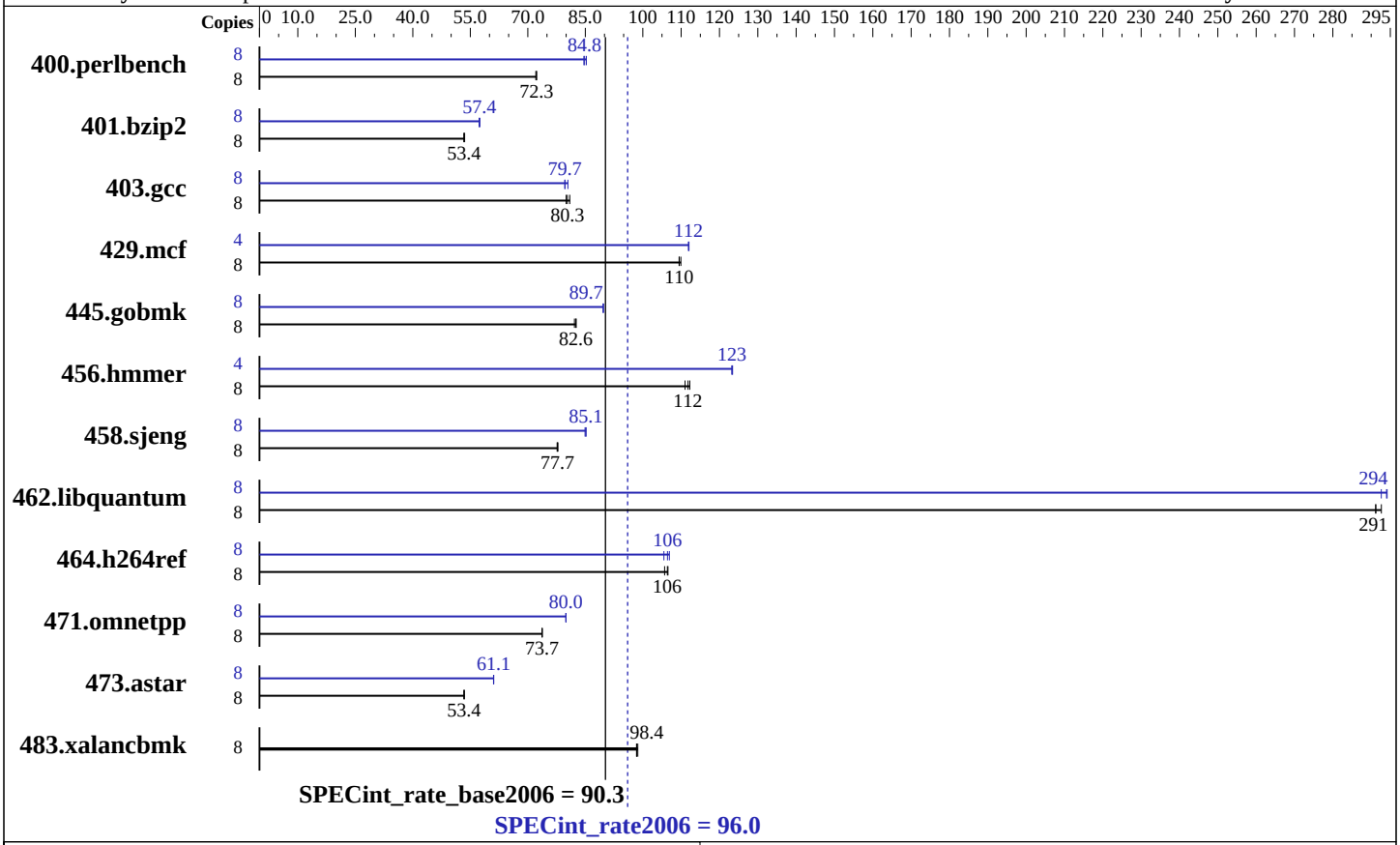
Motherboard X8SIT-F (Intel Xeon L3426, 1.86 GHz)

SPECint®_rate2006 = 96.0

SPECint_rate_base2006 = 90.3

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010



Hardware

CPU Name:	Intel Xeon L3426
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4 x 4 GB DDR3-1333 RDIMM, ECC, CL9)
Disk Subsystem:	1 x 160 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	1084	72.1	1081	72.3	**1082**	**72.3**	8	917	85.3	**921**	**84.8**	924	84.6
401.bzip2	8	**1447**	**53.4**	1447	53.3	1442	53.5	8	1344	57.4	1348	57.3	**1344**	**57.4**
403.gcc	8	805	80.0	**802**	**80.3**	796	81.0	8	**808**	**79.7**	800	80.5	809	79.6
429.mcf	8	**666**	**110**	664	110	667	109	4	**326**	**112**	326	112	326	112
445.gobmk	8	1021	82.2	1016	82.6	**1016**	**82.6**	8	936	89.7	936	89.7	**936**	**89.7**
456.hmmer	8	665	112	672	111	**668**	**112**	4	302	123	303	123	**303**	**123**
458.sjeng	8	**1245**	**77.7**	1247	77.6	1243	77.9	8	**1137**	**85.1**	1140	84.9	1136	85.2
462.libquantum	8	569	291	566	293	**569**	**291**	8	**564**	**294**	563	294	566	293
464.h264ref	8	1675	106	1661	107	**1664**	**106**	8	**1663**	**106**	1656	107	1678	106
471.omnetpp	8	678	73.8	**678**	**73.7**	679	73.7	8	**625**	**80.0**	625	80.0	626	79.9
473.astar	8	**1052**	**53.4**	1051	53.4	1053	53.3	8	920	61.1	919	61.1	**920**	**61.1**
483.xalancbmk	8	561	98.4	559	98.7	**561**	**98.4**	8	561	98.4	559	98.7	**561**	**98.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

Platform Notes

```
Fan speed set to Full Speed in BIOS Setup.
As tested, the system used a Supermicro CSE-827H-R920B chassis.
The chassis is bundled with a PWS-920P-1R power supply, SNK-P0046P heatsink,
and 4 FAN-00111L4 cooling fans.
```

General Notes

```
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.xml`

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