# SPEC® CINT2006 Result

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## IBM Corporation

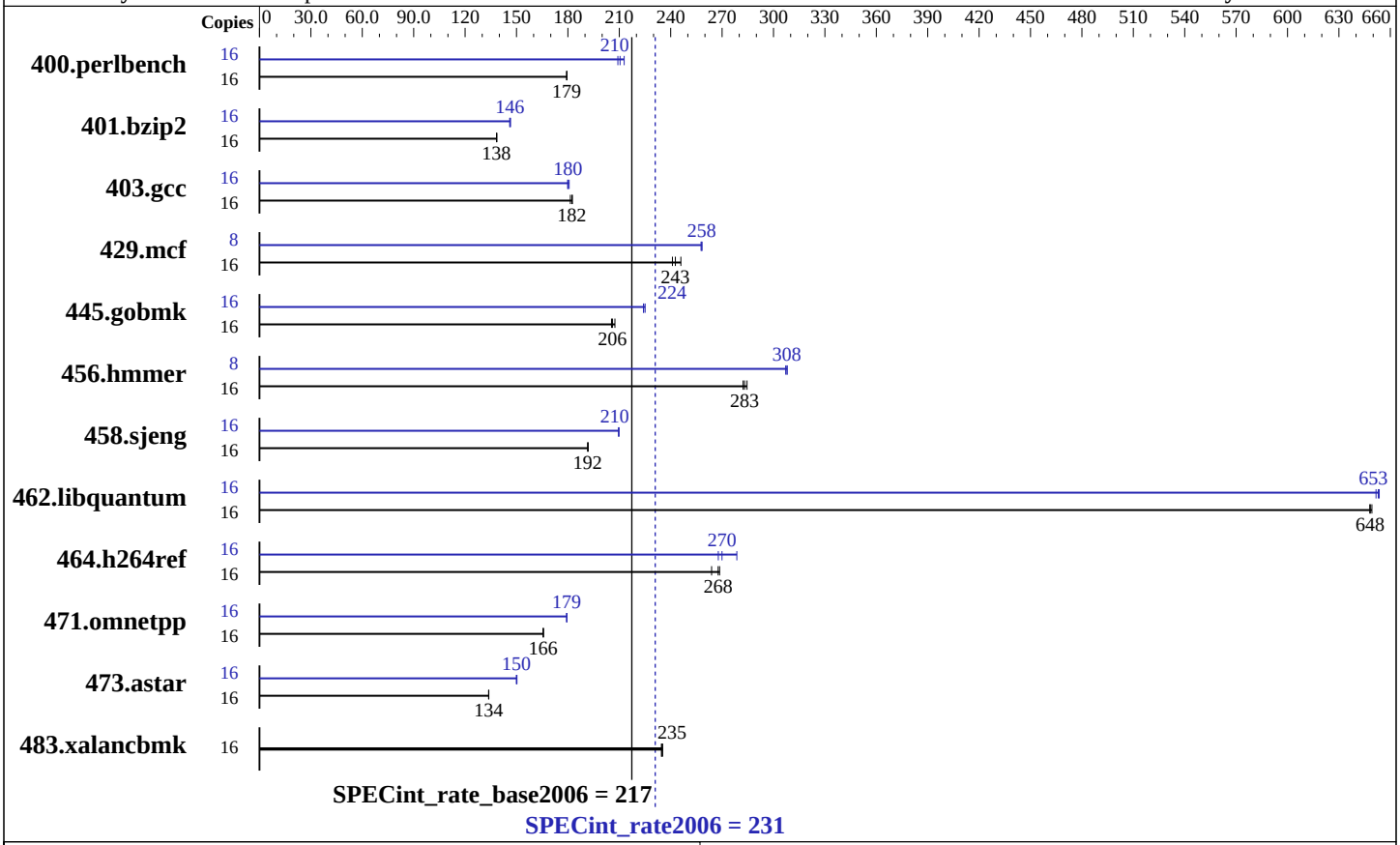
## IBM BladeCenter HS22V (Intel Xeon E5630)

**SPECint®_rate2006 = 231**

**SPECint_rate_base2006 = 217**

**CPU2006 license:** 11
**Test sponsor:** IBM Corporation
**Tested by:** IBM Corporation

**Test date:** Apr-2010
**Hardware Availability:** Mar-2010
**Software Availability:** Jan-2010




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5630 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.80 GHz |
| CPU MHz: | 2533 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 12 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (12 x 4 GB PC3-10600R, 2 Rank, running at 1066 MHz) |
| Disk Subsystem: | 2 x 50 GB SATA, SSD |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default |
| Compiler: | Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | 872 | 179 | **872** | **179** | 871 | 179 | 16 | 747 | 209 | **743** | **210** | 734 | 213 |
| 401.bzip2 | 16 | 1114 | 139 | **1116** | **138** | 1116 | 138 | 16 | 1054 | 147 | 1057 | 146 | **1055** | **146** |
| 403.gcc | 16 | **706** | **182** | 710 | 181 | 705 | 183 | 16 | **715** | **180** | 713 | 181 | 716 | 180 |
| 429.mcf | 16 | 605 | 241 | 593 | 246 | **601** | **243** | 8 | 283 | 258 | 283 | 258 | **283** | **258** |
| 445.gobmk | 16 | **814** | **206** | 817 | 205 | 809 | 207 | 16 | 749 | 224 | **748** | **224** | 746 | 225 |
| 456.hmmer | 16 | 525 | 285 | **527** | **283** | 529 | 282 | 8 | 242 | 308 | **242** | **308** | 243 | 307 |
| 458.sjeng | 16 | 1009 | 192 | **1010** | **192** | 1011 | 192 | 16 | 924 | 210 | **923** | **210** | 923 | 210 |
| 462.libquantum | 16 | 512 | 648 | **511** | **648** | 511 | 649 | 16 | 509 | 652 | 507 | 654 | **508** | **653** |
| 464.h264ref | 16 | 1342 | 264 | 1319 | 269 | **1323** | **268** | 16 | **1312** | **270** | 1271 | 279 | 1322 | 268 |
| 471.omnetpp | 16 | 603 | 166 | 604 | 166 | **604** | **166** | 16 | **558** | **179** | 557 | 179 | 558 | 179 |
| 473.astar | 16 | **839** | **134** | 839 | 134 | 840 | 134 | 16 | 749 | 150 | **749** | **150** | 747 | 150 |
| 483.xalancbmk | 16 | 469 | 235 | **470** | **235** | 470 | 235 | 16 | 469 | 235 | **470** | **235** | 470 | 235 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Platform Notes

```
 Turbo Mode enabled
 Turbo Boost set to Traditional
 Power C-states enabled
 Demand Scrub disabled
```

## General Notes

```
 'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

# Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

462.libquantum: `icc -m64`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`

Continued on next page

## Peak Portability Flags (Continued)

456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.03.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.03.xml`


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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 06:50:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 11 May 2010.