



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## IBM Corporation

**SPECfp®2006 = 36.0**

### IBM System x3850 X5 (Intel Xeon X7550)

**SPECfp\_base2006 = 32.7**

CPU2006 license: 11

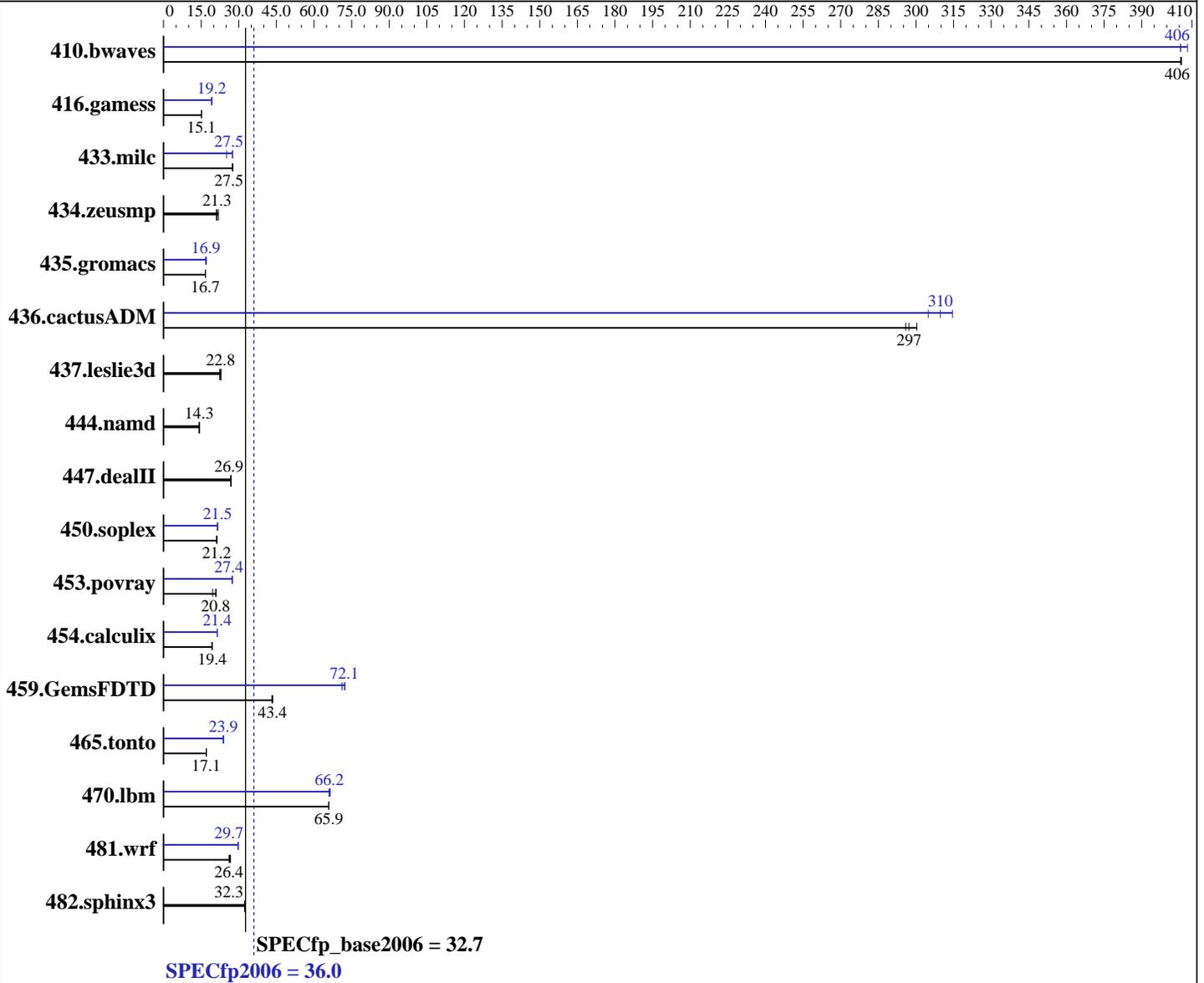
Test date: Apr-2010

Test sponsor: IBM Corporation

Hardware Availability: Mar-2010

Tested by: IBM Corporation

Software Availability: Jan-2010



**Hardware**

CPU Name: Intel Xeon X7550  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

**Software**

Operating System: SuSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp2006 = 36.0

IBM System x3850 X5 (Intel Xeon X7550)

SPECfp\_base2006 = 32.7

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010

L3 Cache: 18 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (64 x 4 GB PC3-8500R, Quad Rank)  
Disk Subsystem: 3 x 50 GB SATA, SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	33.5	406	33.5	406	<b><u>33.5</u></b>	<b><u>406</u></b>	33.5	405	33.3	408	<b><u>33.5</u></b>	<b><u>406</u></b>
416.gamess	1290	15.2	1295	15.1	<b><u>1293</u></b>	<b><u>15.1</u></b>	<b><u>1019</u></b>	<b><u>19.2</u></b>	1019	19.2	1017	19.2
433.milc	332	27.7	333	27.5	<b><u>333</u></b>	<b><u>27.5</u></b>	<b><u>334</u></b>	<b><u>27.5</u></b>	364	25.2	333	27.6
434.zeusmp	431	21.1	416	21.9	<b><u>427</u></b>	<b><u>21.3</u></b>	431	21.1	416	21.9	<b><u>427</u></b>	<b><u>21.3</u></b>
435.gromacs	425	16.8	427	16.7	<b><u>427</u></b>	<b><u>16.7</u></b>	418	17.1	423	16.9	<b><u>421</u></b>	<b><u>16.9</u></b>
436.cactusADM	<b><u>40.2</u></b>	<b><u>297</u></b>	40.4	296	39.8	300	39.2	305	38.0	315	<b><u>38.6</u></b>	<b><u>310</u></b>
437.leslie3d	410	22.9	419	22.5	<b><u>412</u></b>	<b><u>22.8</u></b>	410	22.9	419	22.5	<b><u>412</u></b>	<b><u>22.8</u></b>
444.namd	562	14.3	562	14.3	<b><u>562</u></b>	<b><u>14.3</u></b>	562	14.3	562	14.3	<b><u>562</u></b>	<b><u>14.3</u></b>
447.dealII	425	26.9	<b><u>425</u></b>	<b><u>26.9</u></b>	425	26.9	425	26.9	<b><u>425</u></b>	<b><u>26.9</u></b>	425	26.9
450.soplex	393	21.2	<b><u>393</u></b>	<b><u>21.2</u></b>	391	21.3	387	21.6	388	21.5	<b><u>388</u></b>	<b><u>21.5</u></b>
453.povray	254	20.9	271	19.6	<b><u>255</u></b>	<b><u>20.8</u></b>	<b><u>194</u></b>	<b><u>27.4</u></b>	193	27.5	195	27.3
454.calculix	428	19.3	423	19.5	<b><u>425</u></b>	<b><u>19.4</u></b>	385	21.4	<b><u>385</u></b>	<b><u>21.4</u></b>	385	21.5
459.GemsFDTD	244	43.5	<b><u>244</u></b>	<b><u>43.4</u></b>	246	43.2	<b><u>147</u></b>	<b><u>72.1</u></b>	147	72.4	149	71.2
465.tonto	577	17.1	573	17.2	<b><u>576</u></b>	<b><u>17.1</u></b>	412	23.9	<b><u>412</u></b>	<b><u>23.9</u></b>	413	23.9
470.lbm	209	65.8	208	65.9	<b><u>209</u></b>	<b><u>65.9</u></b>	207	66.4	208	66.0	<b><u>208</u></b>	<b><u>66.2</u></b>
481.wrf	418	26.7	<b><u>424</u></b>	<b><u>26.4</u></b>	427	26.1	<b><u>376</u></b>	<b><u>29.7</u></b>	374	29.9	376	29.7
482.sphinx3	602	32.4	<b><u>603</u></b>	<b><u>32.3</u></b>	603	32.3	<b><u>602</u></b>	<b><u>32.4</u></b>	<b><u>603</u></b>	<b><u>32.3</u></b>	603	32.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Turbo Boost set to Traditional  
Demand Scrub disabled

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
KMP\_STACKSIZE set to 200M  
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp2006 = 36.0

IBM System x3850 X5 (Intel Xeon X7550)

SPECfp\_base2006 = 32.7

CPU2006 license: 11

Test date: Apr-2010

Test sponsor: IBM Corporation

Hardware Availability: Mar-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.lelie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp2006 = 36.0

IBM System x3850 X5 (Intel Xeon X7550)

SPECfp\_base2006 = 32.7

CPU2006 license: 11

Test date: Apr-2010

Test sponsor: IBM Corporation

Hardware Availability: Mar-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-parallel -ansi-alias -auto-ilp32

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

Fortran benchmarks:

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp2006 = 36.0

IBM System x3850 X5 (Intel Xeon X7550)

SPECfp\_base2006 = 32.7

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010

## Peak Optimization Flags (Continued)

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.03.xml>



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp2006 = 36.0

IBM System x3850 X5 (Intel Xeon X7550)

SPECfp\_base2006 = 32.7

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 07:09:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 11 May 2010.