Acer Incorporated

Gateway GT350 F1 (Intel Xeon E5506)

| SPECint_rate2006 | 148 |
| SPECint_rate_base2006 | 139 |

**CPU2006 license**: 97

**Test sponsor**: Acer Incorporated

**Tested by**: Acer Incorporated

**Test date**: Apr-2010

**Hardware Availability**: Mar-2010

- **CPU Name**: Intel Xeon E5506
- **CPU Characteristics**: 8 cores, 2 chips, 4 cores/chip
- **CPU MHz**: 2133
- **FPU**: Integrated
- **Primary Cache**: 32 KB I + 32 KB D on chip per core
- **Secondary Cache**: 256 KB I+D on chip per core
- **L3 Cache**: 4 MB I+D on chip per chip
- **Other Cache**: None
- **Memory**: 24 GB (12 x 2 GB DDR3-1066 RDIMM, running at 800 MHz)
- **Disk Subsystem**: 1000 GB SATAII, 7200 RPM
- **Other Hardware**: None

- **Operating System**: SUSE Linux Enterprise Server 11 (x86_64)
- **Compiler**: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
- **Auto Parallel**: No
- **File System**: ReiserFS
- **System State**: Run level 3 (multi-user)
- **Base Pointers**: 32-bit
- **Peak Pointers**: 32/64-bit
- **Other Software**: Microquill SmartHeap V8.1, Binutils 2.18.50.0.7.20000502

### SPECint Rate 2006

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECint_rate2006</th>
<th>SPECint_rate_base2006</th>
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<td>400.perlbench</td>
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<td>124</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>78.5</td>
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<tr>
<td>403.gcc</td>
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<tr>
<td>429.mcf</td>
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<td>136</td>
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<tr>
<td>445.gobmk</td>
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<tr>
<td>456.hmmer</td>
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<tr>
<td>462.libquantum</td>
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<td>464.h264ref</td>
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<tr>
<td>471.omnetpp</td>
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<td>473.astar</td>
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<tr>
<td>483.xalancbmk</td>
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SPECint_rate2006 = 148
SPECint_rate_base2006 = 139

Results Table

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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes
'ulimit -s unlimited' was set for stacksize unlimited

General Notes
This result was measured on the Gateway GT350 F1.
The Acer AT350 F1 and Gateway GT350 F1 are electronically equivalent.

Base Compiler Invocation
C benchmarks:
   icc -m32

C++ benchmarks:
icpc -m32
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Gateway GT350 F1 (Intel Xeon E5506)

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**CPU2006 license:** 97  
**Test sponsor:** Acer Incorporated  
**Tested by:** Acer Incorporated

**Test date:** Apr-2010  
**Hardware Availability:** Mar-2010  
**Software Availability:** Dec-2010

### Base Portability Flags

- 400.perlbench: `-DSPEC_CPU_LINUX_IA32`
- 462.libquantum: `-DSPEC_CPU_LINUX`
- 483.xalancbmk: `-DSPEC_CPU_LINUX`

### Base Optimization Flags

**C benchmarks:**

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

**C++ benchmarks:**

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
```

### Base Other Flags

**C benchmarks:**

```
403.gcc: -Dalloca=_alloca
```

### Peak Compiler Invocation

**C benchmarks (except as noted below):**

```
icc -m32
```

- 401.bzip2: `icc -m64`
- 456.hmmer: `icc -m64`
- 458.sjeng: `icc -m64`
- 462.libquantum: `icc -m64`

**C++ benchmarks (except as noted below):**

```
icpc -m32
```

- 473.astar: `icpc -m64`

### Peak Portability Flags

- 400.perlbench: `-DSPEC_CPU_LINUX_IA32`
- 401.bzip2: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

- 456.hmmer: -DSPEC_CPU_LP64
- 458.sjeng: -DSPEC_CPU_LP64
- 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
- 473.astar: -DSPEC_CPU_LP64
- 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

- 400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2) -ansi-alias
- 401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
- 403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static
- 429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
- 445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
  -ipo -no-prec-div -ansi-alias
- 456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
  -ansi-alias -auto-ilp32
- 458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2) -unroll4 -auto-ilp32
- 462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
  -opt-prefetch
- 464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

- 471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2)
  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
  -Wl,-z,muldefs
- 473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2)
  -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## SPEC CINT2006 Result

**Acer Incorporated**

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### Peak Optimization Flags (Continued)

473.astar (continued):

483.xalancbmk: basepeak = yes

### Peak Other Flags

**C benchmarks:**

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at [http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html](http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html)

You can also download the XML flags source by saving the following link: [http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml](http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml)

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For other inquiries, please contact webmaster@spec.org.

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