# SPEC® CFP2006 Result

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## Supermicro

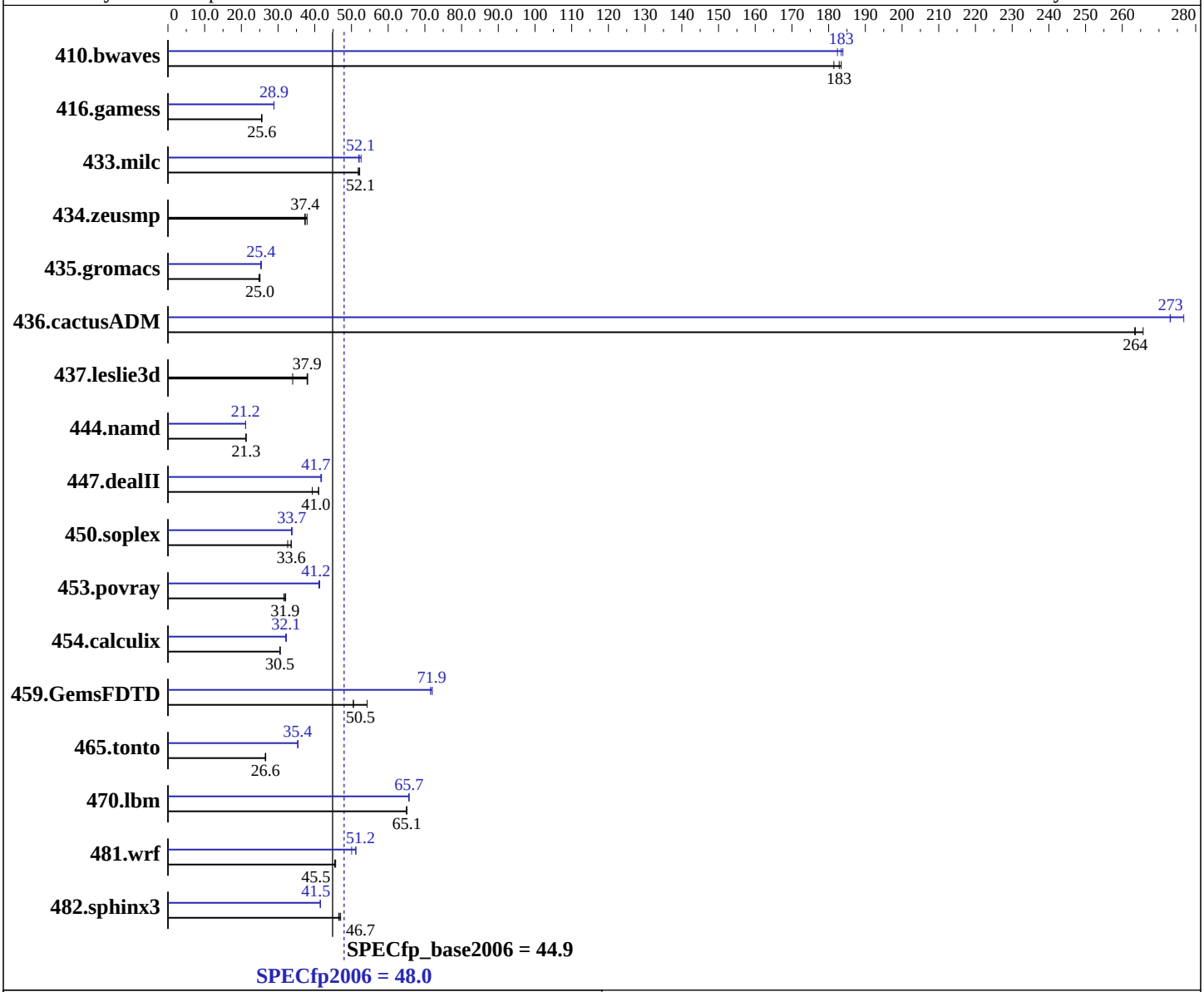
Motherboard X8DTN+ (Intel Xeon X5680, 3.33 GHz)

SPECfp®2006 = 48.0

SPECfp_base2006 = 44.9

**CPU2006 license:** 001176
**Test sponsor:** Supermicro
**Tested by:** Supermicro

**Test date:** Mar-2010
**Hardware Availability:** Mar-2010
**Software Availability:** Jan-2010




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5680 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.60 GHz |
| CPU MHz: | 3333 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default |
| Compiler: | Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064 |
| Auto Parallel: | Yes |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB DDR3-1333 RDIMM, ECC, CL9)
Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 74.9 | 181 | 74.1 | 183 | **74.3** | **183** | 73.9 | 184 | 74.5 | 182 | **74.1** | **183** |
| 416.gamess | 769 | 25.5 | **766** | **25.6** | 765 | 25.6 | 678 | 28.9 | 678 | 28.9 | **678** | **28.9** |
| 433.milc | **176** | **52.1** | 177 | 51.8 | 176 | 52.2 | **176** | **52.1** | 177 | 52.0 | 174 | 52.6 |
| 434.zeusmp | **243** | **37.4** | 240 | 37.9 | 244 | 37.3 | **243** | **37.4** | 240 | 37.9 | 244 | 37.3 |
| 435.gromacs | 285 | 25.1 | **285** | **25.0** | 288 | 24.8 | 283 | 25.3 | 281 | 25.4 | **281** | **25.4** |
| 436.cactusADM | 45.4 | 263 | 45.0 | 266 | **45.3** | **264** | 43.8 | 273 | 43.2 | 277 | **43.8** | **273** |
| 437.leslie3d | 277 | 34.0 | **248** | **37.9** | 247 | 38.1 | 277 | 34.0 | **248** | **37.9** | 247 | 38.1 |
| 444.namd | 377 | 21.3 | **377** | **21.3** | 377 | 21.3 | 379 | 21.2 | **379** | **21.2** | 379 | 21.2 |
| 447.dealII | 279 | 41.1 | 291 | 39.3 | **279** | **41.0** | 274 | 41.7 | 273 | 41.8 | **274** | **41.7** |
| 450.soplex | 248 | 33.6 | 256 | 32.6 | **248** | **33.6** | **247** | **33.7** | 247 | 33.8 | 248 | 33.7 |
| 453.povray | **167** | **31.9** | 168 | 31.6 | 166 | 32.1 | **129** | **41.2** | 129 | 41.1 | 129 | 41.3 |
| 454.calculix | **270** | **30.5** | 270 | 30.6 | 270 | 30.5 | 256 | 32.3 | 257 | 32.1 | **257** | **32.1** |
| 459.GemsFDTD | 195 | 54.3 | **210** | **50.5** | 210 | 50.5 | **148** | **71.9** | 147 | 72.0 | 148 | 71.5 |
| 465.tonto | 371 | 26.5 | 369 | 26.6 | **371** | **26.6** | 278 | 35.4 | **278** | **35.4** | 278 | 35.4 |
| 470.lbm | 211 | 65.1 | 212 | 65.0 | **211** | **65.1** | 209 | 65.7 | **209** | **65.7** | 209 | 65.6 |
| 481.wrf | 246 | 45.5 | 244 | 45.7 | **245** | **45.5** | **218** | **51.2** | 218 | 51.2 | 223 | 50.0 |
| 482.sphinx3 | **418** | **46.7** | 419 | 46.5 | 414 | 47.0 | 469 | 41.5 | 470 | 41.5 | **469** | **41.5** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## Platform Notes

Fan speed set to Full Speed in BIOS Setup.
As tested, the system used a Supermicro
PWS-865-PQ power supply, 2 SNK-P0038P heatsinks,
and 2 FAN-0077L cooling fans.

## General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

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## Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

## Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

`Same as Base Portability Flags`

## Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-ansi-alias`

470.lbm: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32 -unroll2`

C++ benchmarks:

444.namd: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

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## Peak Optimization Flags (Continued)

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml


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