SPEC® CINT2006 Result

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Intel Corporation

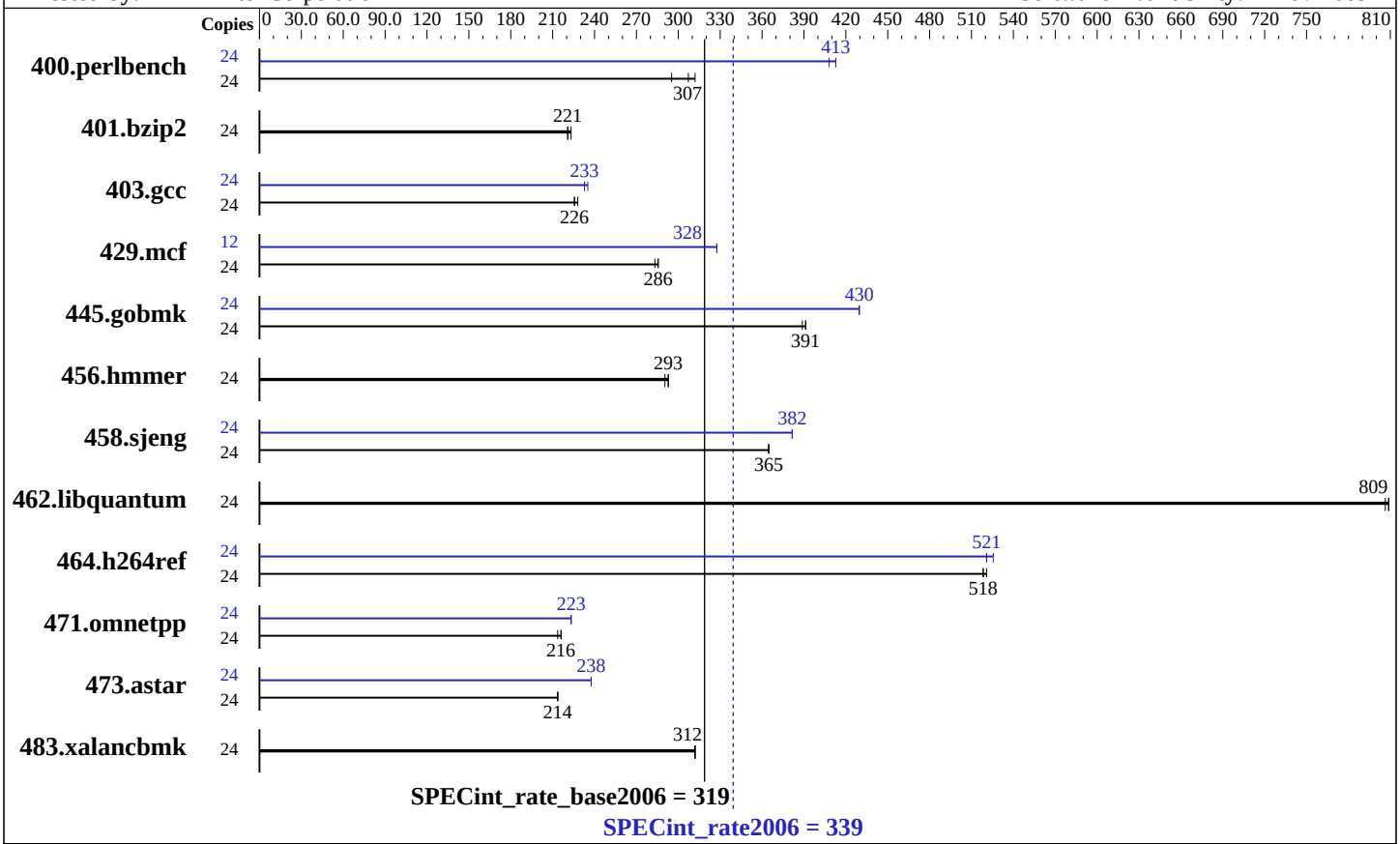
Supermicro SuperServer 7046A-T (Intel Xeon X5680, 3.33 GHz)

SPECint®_rate2006 = 339

SPECint_rate_base2006 = 319

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Mar-2010
Hardware Availability: Mar-2010
Software Availability: Nov-2009



Hardware

CPU Name:	Intel Xeon X5680
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6x 4GB DDR3-1333, ECC, CL9)
Disk Subsystem:	1 x 300 GB SATA, 10000 RPM
Other Hardware:	None

Software

Operating System:	Windows 7 Enterprise (64-bit)
Compiler:	Intel C++ Compiler Professional 11.1 for IA32 Build 20090903 Package ID: w_cproc_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	749	312	795	295	**762**	**307**	24	**569**	**413**	568	413	576	408
401.bzip2	24	**1049**	**221**	1041	223	1053	221	24	**1049**	**221**	1041	223	1053	221
403.gcc	24	847	228	**855**	**226**	855	226	24	819	235	**826**	**233**	829	233
429.mcf	24	772	283	766	286	**767**	**286**	12	334	328	**334**	**328**	334	328
445.gobmk	24	**646**	**391**	647	389	645	391	24	**586**	**430**	587	430	585	430
456.hmmer	24	**768**	**293**	764	293	771	290	24	**768**	**293**	764	293	771	290
458.sjeng	24	**795**	**365**	795	365	794	365	24	759	382	761	382	**761**	**382**
462.libquantum	24	617	806	614	809	**615**	**809**	24	617	806	614	809	**615**	**809**
464.h264ref	24	1022	521	**1023**	**518**	1026	518	24	1012	526	1022	521	**1020**	**521**
471.omnetpp	24	698	216	699	214	**698**	**216**	24	672	223	673	223	**672**	**223**
473.astar	24	**786**	**214**	785	214	786	214	24	710	238	711	238	**710**	**238**
483.xalancbmk	24	**530**	**312**	532	312	530	312	24	**530**	**312**	532	312	530	312

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
Tested systems is used with tower case,
PC Power and Cooling dual 865W power supplys
System was configured with nVidia Quadro FX 370 discrete graphics card
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DWIN32 -DSPEC_CPU_NO_INTTYPES`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlW32M.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DWIN32 -DSPEC_CPU_NO_INTTYPES`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch`
`/F512000000 shlW32M.lib            -link /FORCE:MULTIPLE`

401.bzip2: `basepeak = yes`

403.gcc: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

429.mcf: `-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch`
`/F512000000`

Continued on next page

Peak Optimization Flags (Continued)

445.gobmk: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000`

456.hmmer: `basepeak = yes`

458.sjeng: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000`

462.libquantum: `basepeak = yes`

464.h264ref: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000`

C++ benchmarks:

471.omnetpp: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlW32M.lib -link /FORCE:MULTIPLE`

473.astar: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlW32M.lib -link /FORCE:MULTIPLE`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.00.xml`

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