SPEC® CINT2006 Result
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Acer Incorporated

Gateway GR380 F1(Intel Xeon E5502)

SPECint®_rate2006 =   73.9

SPECint_rate_base2006 = 68.7

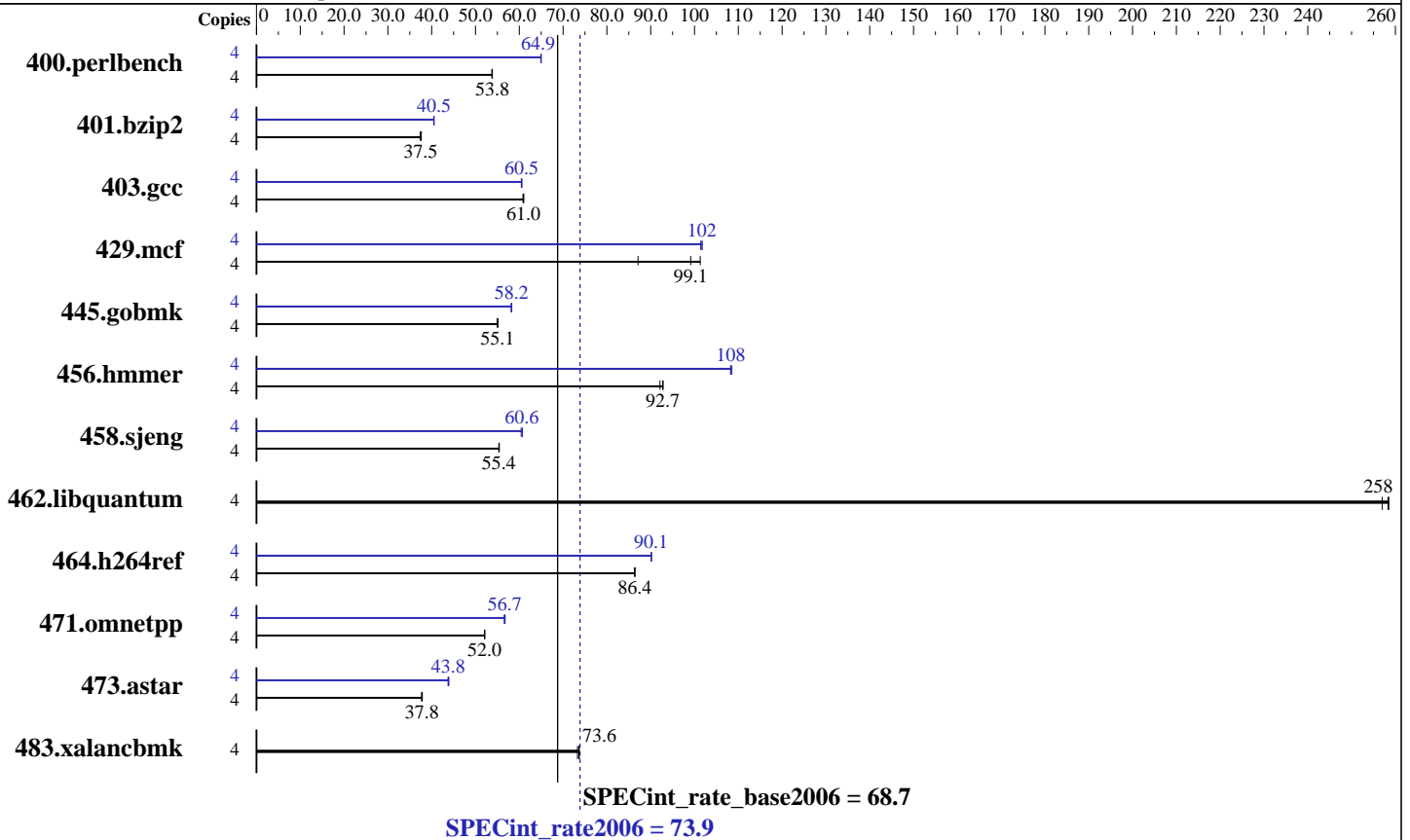**CPU2006 license:** 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Mar-2010

Hardware Availability: Jan-2010

Software Availability: Oct-2009

Copies

Benchmark	Copies	Result (blue / black)
400.perlbench	4 / 4	64.9 / 53.8
401.bzip2	4 / 4	40.5 / 37.5
403.gcc	4 / 4	60.5 / 61.0
429.mcf	4 / 4	102 / 99.1
445.gobmk	4 / 4	58.2 / 55.1
456.hmmer	4 / 4	108 / 92.7
458.sjeng	4 / 4	60.6 / 55.4
462.libquantum	4	258
464.h264ref	4 / 4	90.1 / 86.4
471.omnetpp	4 / 4	56.7 / 52.0
473.astar	4 / 4	43.8 / 37.8
483.xalancbmk	4	73.6

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Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (12 x 2GB DDR3-1333 RDIMM, running at 800 MHz)
Disk Subsystem:	1000 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	726	53.8	**726**	**53.8**	727	53.7	4	**602**	**64.9**	601	65.0	602	64.9
401.bzip2	4	**1029**	**37.5**	1026	37.6	1032	37.4	4	952	40.5	954	40.5	**952**	**40.5**
403.gcc	4	528	61.0	**528**	**61.0**	529	60.8	4	533	60.5	531	60.6	**532**	**60.5**
429.mcf	4	**368**	**99.1**	360	101	419	87.1	4	**359**	**102**	360	101	359	102
445.gobmk	4	763	55.0	761	55.1	**762**	**55.1**	4	720	58.3	722	58.1	**721**	**58.2**
456.hmmer	4	**402**	**92.7**	402	92.8	405	92.1	4	345	108	344	109	**345**	**108**
458.sjeng	4	**874**	**55.4**	873	55.4	874	55.4	4	797	60.7	**799**	**60.6**	801	60.4
462.libquantum	4	323	257	321	259	**321**	**258**	4	323	257	321	259	**321**	**258**
464.h264ref	4	1026	86.3	1024	86.4	**1025**	**86.4**	4	**982**	**90.1**	983	90.1	981	90.2
471.omnetpp	4	479	52.2	480	52.0	**480**	**52.0**	4	442	56.5	440	56.8	**441**	**56.7**
473.astar	4	742	37.9	745	37.7	**743**	**37.8**	4	639	43.9	**641**	**43.8**	642	43.7
483.xalancbmk	4	374	73.7	**375**	**73.6**	377	73.3	4	374	73.7	**375**	**73.6**	377	73.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
    'ulimit -s unlimited' was set for stacksize unlimited
```

General Notes

```
    This result was measured on the Gateway GR380 F1.
    The Acer AR380 F1 and Gateway GR380 F1 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
```
     icc -m32
```

C++ benchmarks:
```
     icpc -m32
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
462.libquantum: -DSPEC_CPU_LINUX
     473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

      403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

      429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
               -ipo -no-prec-div -ansi-alias

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias -auto-ilp32

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

      473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
                 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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