SPEC® CINT2006 Result

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Hewlett-Packard Company

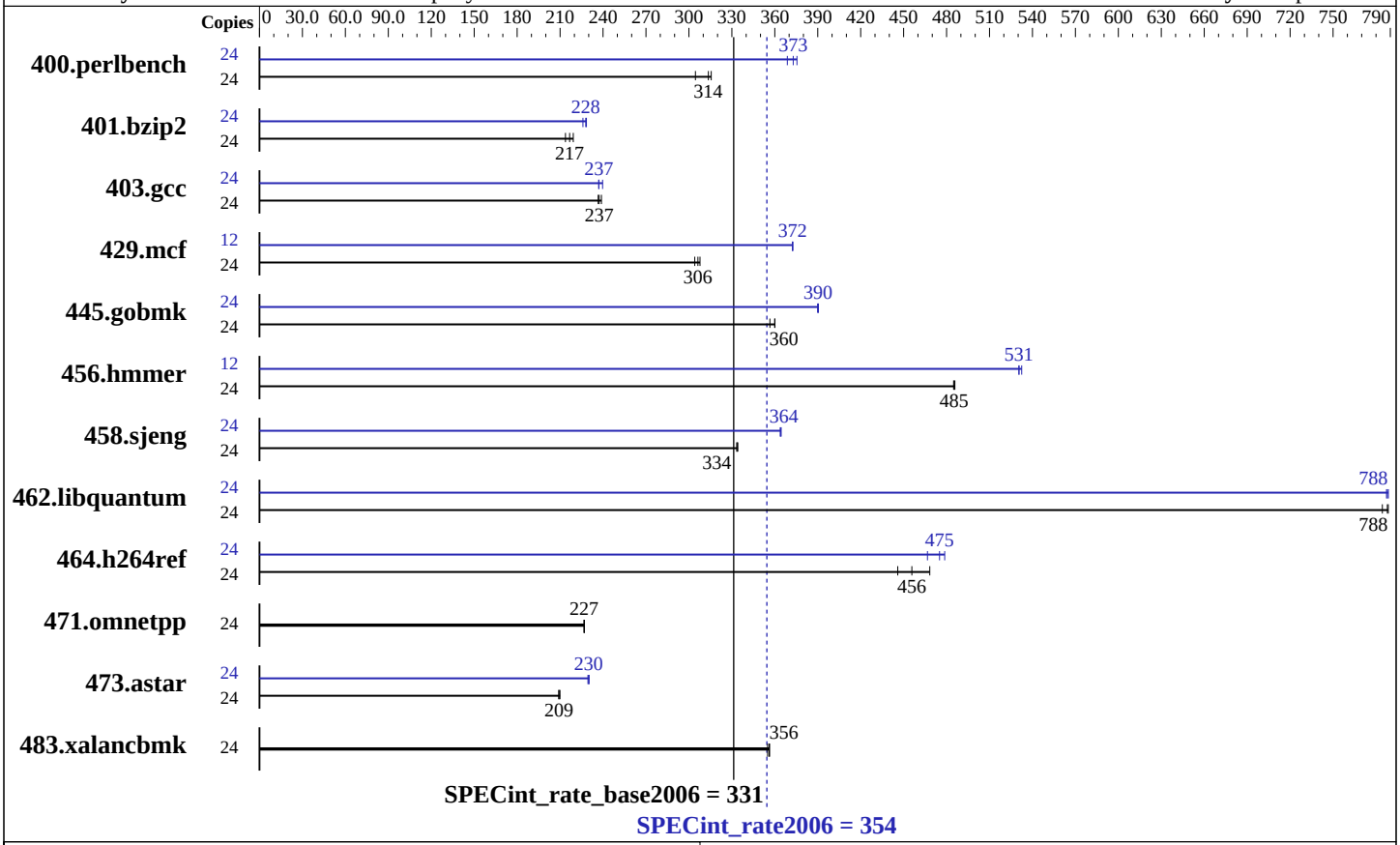
ProLiant DL380 G6
(2.93 GHz, Intel Xeon X5670)

SPECint®_rate2006 = 354

SPECint_rate_base2006 = 331

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Mar-2010
Hardware Availability: Jun-2010
Software Availability: Sep-2009



Hardware

CPU Name:	Intel Xeon X5670
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-10600R CL9)
Disk Subsystem:	1 x 120 GB 5.4 K SATA
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 5.4, Kernel 2.6.18-164.el5
Compiler:	Intel C++ Compiler 11.1 for Linux Build 20090827 Package ID: l_cproc_p_11.1.056
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.18

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	**748**	**314**	770	305	743	316	24	636	369	624	375	**629**	**373**
401.bzip2	24	**1069**	**217**	1083	214	1056	219	24	**1016**	**228**	1025	226	1014	228
403.gcc	24	**814**	**237**	817	237	809	239	24	815	237	**815**	**237**	806	240
429.mcf	24	720	304	711	308	**715**	**306**	12	**294**	**372**	294	373	294	372
445.gobmk	24	706	357	**700**	**360**	699	360	24	**645**	**390**	646	390	645	390
456.hmmer	24	462	485	461	486	**461**	**485**	12	**211**	**531**	210	532	211	531
458.sjeng	24	**870**	**334**	869	334	871	333	24	798	364	797	364	**798**	**364**
462.libquantum	24	634	784	**631**	**788**	631	789	24	632	787	631	789	**631**	**788**
464.h264ref	24	1134	468	1191	446	**1165**	**456**	24	**1118**	**475**	1110	479	1138	467
471.omnetpp	24	661	227	**661**	**227**	661	227	24	661	227	**661**	**227**	661	227
473.astar	24	806	209	**805**	**209**	803	210	24	**733**	**230**	734	229	732	230
483.xalancbmk	24	467	355	465	356	**465**	**356**	24	467	355	465	356	**465**	**356**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
 HP Power Profile set to Maximum Performance
 Thermal Configuration set to Increased Cooling
 Memory Speed with 2 DIMMs per Channel set to 1333Mhz Maximum
 Data Reuse set to Disabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.1/056/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.1/056/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.1/056/bin/intel64/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100511.html`
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20100511.00.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100511.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20100511.00.xml`

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For other inquiries, please contact webmaster@spec.org.

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