SPEC® CFP2006 Result

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NEC Corporation

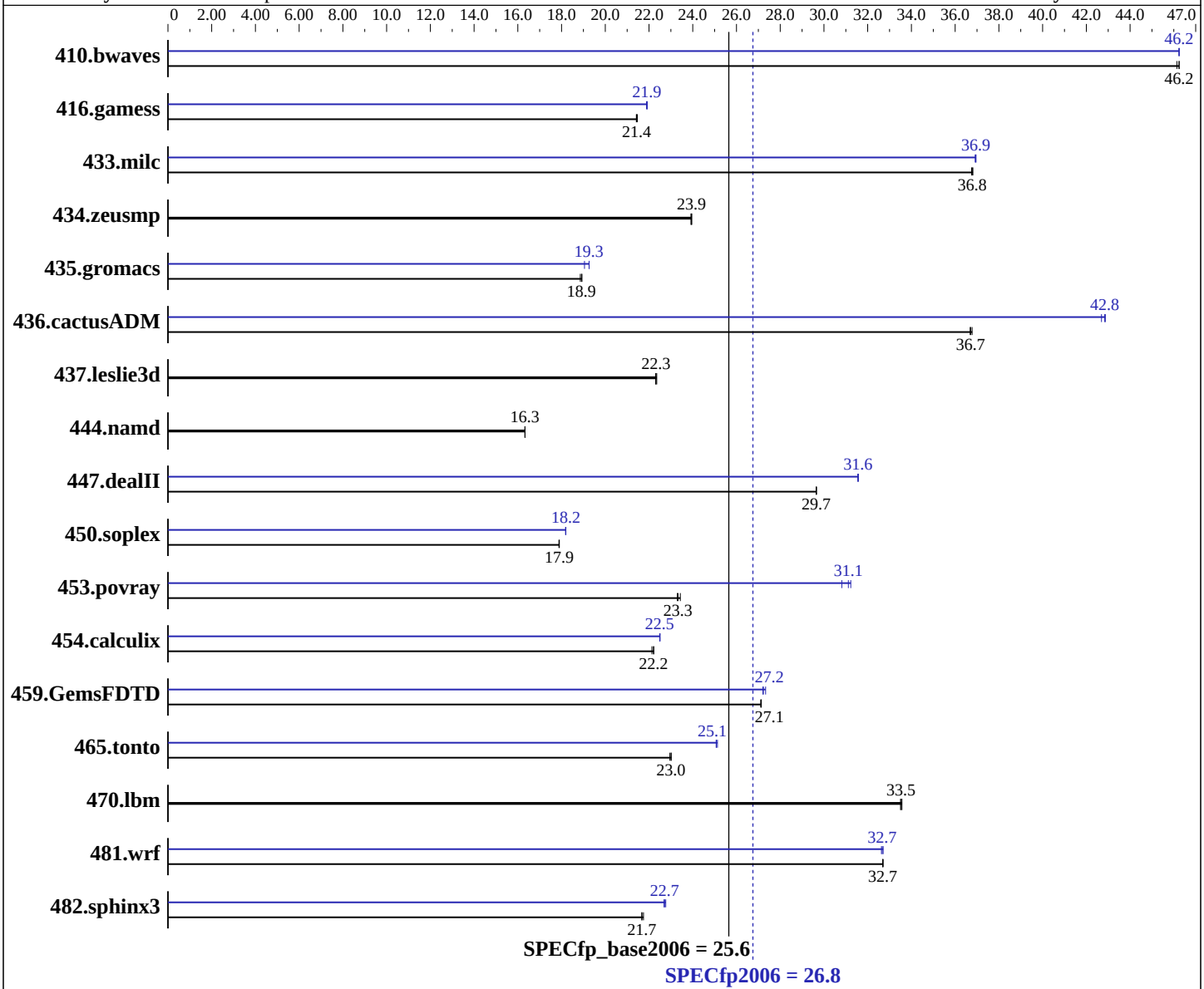
Express5800/T110b
(Intel Pentium G6950)

SPECfp®2006 = 26.8

SPECfp_base2006 = 25.6

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Feb-2010
Hardware Availability: Jan-2010
Software Availability: Nov-2009



Hardware

CPU Name:	Intel Pentium G6950
CPU Characteristics:	
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059, l_cprof_p_11.1.059
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache: 3 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (4 x 2 GB PC3-10600E running at 1066 MHz)
Disk Subsystem: 1x160 GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	295	46.1	294	46.2	**294**	**46.2**	**294**	**46.2**	294	46.2	294	46.2
416.gamess	914	21.4	912	21.5	**913**	**21.4**	895	21.9	**894**	**21.9**	893	21.9
433.milc	250	36.8	249	36.8	**250**	**36.8**	**248**	**36.9**	248	37.0	249	36.9
434.zeusmp	380	24.0	**380**	**23.9**	380	23.9	380	24.0	**380**	**23.9**	380	23.9
435.gromacs	**378**	**18.9**	377	18.9	379	18.9	375	19.0	371	19.3	**371**	**19.3**
436.cactusADM	325	36.8	326	36.7	**326**	**36.7**	280	42.7	**279**	**42.8**	279	42.9
437.leslie3d	**421**	**22.3**	422	22.3	421	22.4	**421**	**22.3**	422	22.3	421	22.4
444.namd	491	16.3	**491**	**16.3**	491	16.3	491	16.3	**491**	**16.3**	491	16.3
447.dealII	**386**	**29.7**	386	29.6	386	29.7	**362**	**31.6**	362	31.6	363	31.5
450.soplex	466	17.9	**466**	**17.9**	466	17.9	459	18.2	**458**	**18.2**	458	18.2
453.povray	228	23.3	227	23.4	**228**	**23.3**	170	31.2	173	30.8	**171**	**31.1**
454.calculix	373	22.1	371	22.2	**372**	**22.2**	367	22.5	**367**	**22.5**	367	22.5
459.GemsFDTD	**391**	**27.1**	391	27.1	391	27.1	390	27.2	388	27.3	**390**	**27.2**
465.tonto	**428**	**23.0**	428	23.0	429	23.0	392	25.1	**392**	**25.1**	392	25.1
470.lbm	410	33.5	**410**	**33.5**	409	33.6	410	33.5	**410**	**33.5**	409	33.6
481.wrf	342	32.7	**342**	**32.7**	342	32.7	**342**	**32.7**	342	32.7	342	32.6
482.sphinx3	899	21.7	**899**	**21.7**	896	21.7	856	22.8	**858**	**22.7**	859	22.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Default BIOS settings were used.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

C++ benchmarks:
```
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Fortran benchmarks:
```
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -fno-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll2`

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revE.20100302.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revE.20100302.xml`

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For other inquiries, please contact webmaster@spec.org.

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