SPEC® CFP2006 Result

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Intel Corporation

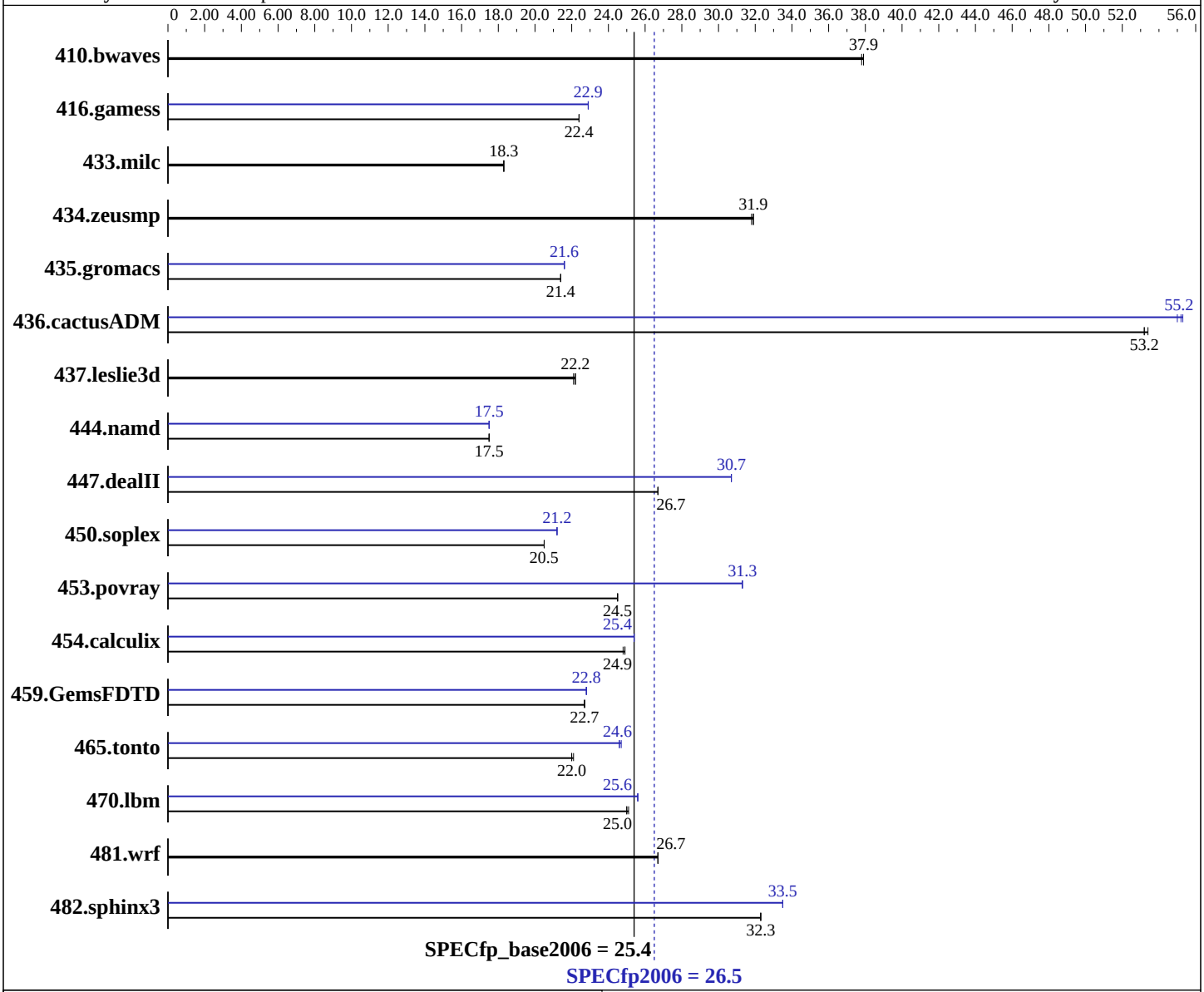
Asus P5E3 Pro motherboard (Intel Core 2 Extreme QX9770)

SPECfp®2006 = 26.5

SPECfp_base2006 = 25.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Feb-2010
Hardware Availability: Jun-2009
Software Availability: Oct-2009



Hardware

CPU Name:	Intel Core 2 Extreme QX9770
CPU Characteristics:	
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045 Intel Visual Fortran Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045, w_cprof_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	Yes

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L3 Cache: None
Other Cache: None
Memory: 4 GB (2x2GB Micron MT16JTF25664AZ-1G4 DDR3-1333 CL9)
Disk Subsystem: Intel X25-M 80GB SSD
Other Hardware: None

File System: NTFS
System State: Default
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: None
SmartHeap Library Version 8.1 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	359	37.9	359	37.8	**359**	**37.9**	359	37.9	359	37.8	**359**	**37.9**
416.gamess	**875**	**22.4**	874	22.4	875	22.4	854	22.9	855	22.9	**854**	**22.9**
433.milc	501	18.3	**501**	**18.3**	501	18.3	501	18.3	**501**	**18.3**	501	18.3
434.zeusmp	**286**	**31.9**	286	31.8	285	31.9	**286**	**31.9**	286	31.8	285	31.9
435.gromacs	333	21.4	**333**	**21.4**	333	21.4	331	21.6	**331**	**21.6**	331	21.6
436.cactusADM	224	53.4	225	53.2	**225**	**53.2**	**216**	**55.2**	216	55.3	217	55.0
437.leslie3d	425	22.1	423	22.2	**424**	**22.2**	425	22.1	423	22.2	**424**	**22.2**
444.namd	457	17.5	**457**	**17.5**	457	17.5	457	17.5	**458**	**17.5**	458	17.5
447.dealII	428	26.7	428	26.7	**428**	**26.7**	373	30.7	**373**	**30.7**	373	30.7
450.soplex	**406**	**20.5**	406	20.5	407	20.5	393	21.2	**393**	**21.2**	394	21.2
453.povray	217	24.5	**217**	**24.5**	217	24.5	170	31.3	**170**	**31.3**	170	31.3
454.calculix	**332**	**24.9**	333	24.8	332	24.9	325	25.4	325	25.4	**325**	**25.4**
459.GemsFDTD	468	22.7	467	22.7	**467**	**22.7**	466	22.8	465	22.8	**465**	**22.8**
465.tonto	**447**	**22.0**	447	22.0	446	22.1	399	24.6	**399**	**24.6**	399	24.7
470.lbm	549	25.0	549	25.1	**549**	**25.0**	537	25.6	537	25.6	**537**	**25.6**
481.wrf	418	26.7	419	26.7	**418**	**26.7**	418	26.7	419	26.7	**418**	**26.7**
482.sphinx3	604	32.3	**603**	**32.3**	603	32.3	**582**	**33.5**	582	33.5	583	33.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
System was configured with nVidia GTX 280 discrete graphics card
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 /Qlowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
-Qauto-ilp32 /F1000000000

C++ benchmarks:
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
-Qcxx-features -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
/F1000000000

Benchmarks using both Fortran and C:
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
-Qauto-ilp32 /F1000000000

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qansi-alias -Qparallel -Qauto-ilp32 /F1000000000`

482.sphinx3: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Qauto-ilp32 /F1000000000`

C++ benchmarks:

444.namd: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

447.dealII: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qansi-alias -Qscalar-rep- -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

450.soplex: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

453.povray: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

Continued on next page

Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel /F1000000000

465.tonto: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000

436.cactusADM: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2 -Qauto-ilp32 /F1000000000

454.calculix: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F1000000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.01.xml

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