# SPEC® CINT2006 Result

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## Bull SAS

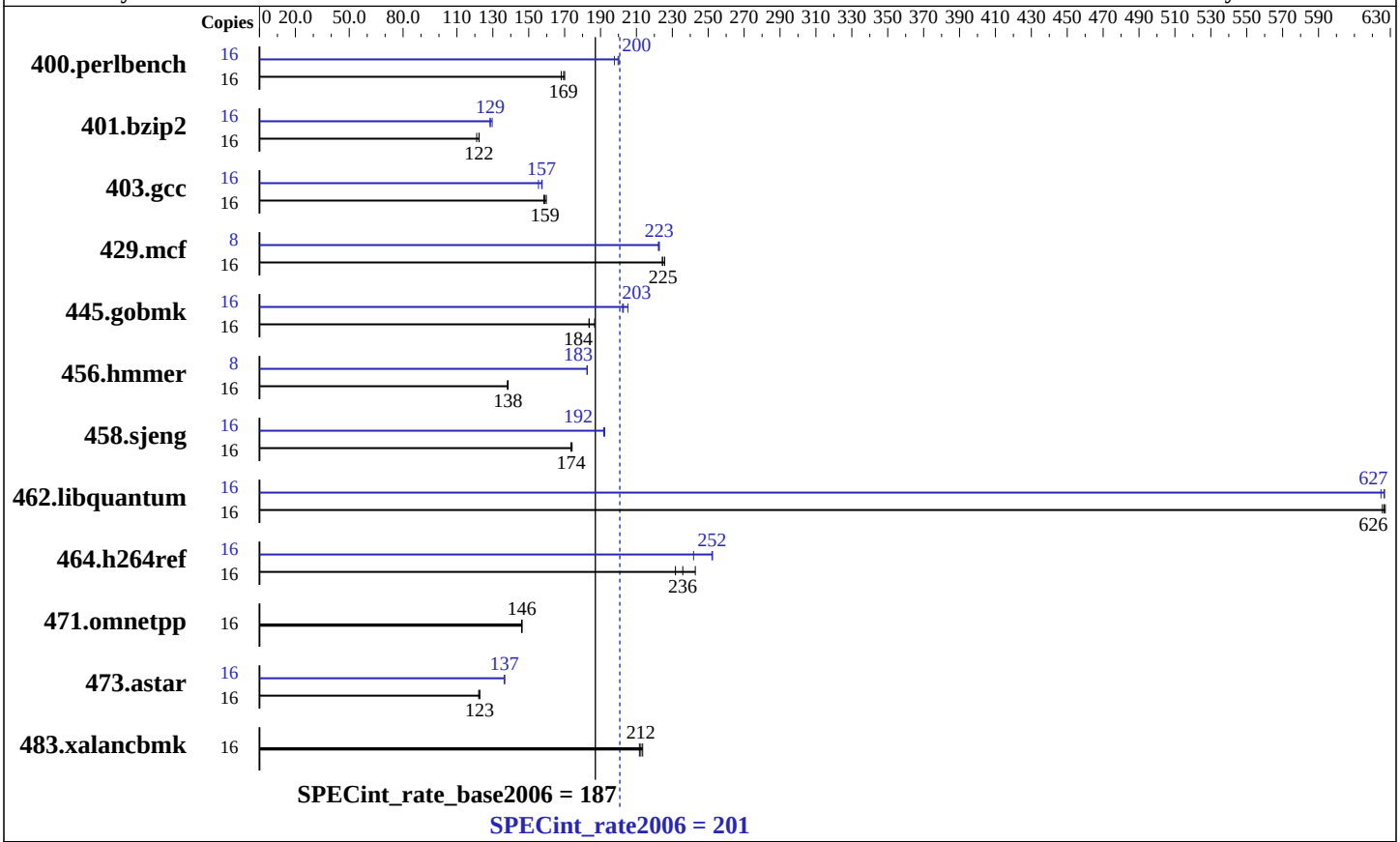
BL265
(Intel Xeon E5520, 2.27 GHz)

SPECint®_rate2006 = 201

SPECint_rate_base2006 = 187

**CPU2006 license:** 20
**Test sponsor:** Bull SAS
**Tested by:** Bull SAS

**Test date:** Jan-2010
**Hardware Availability:** May-2009
**Software Availability:** Feb-2009




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5520 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.53 GHz |
| CPU MHz: | 2267 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (6 x 4 GB PC3-10600R, 2 Rank, CL9-9-9, ECC, running at 1066 MHz) |
| Disk Subsystem: | 1 x 73 GB SAS, 10000 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP2, SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp |
| Compiler: | Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080 |
| Auto Parallel: | No |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | 930 | 168 | 919 | 170 | **923** | **169** | 16 | 791 | 198 | 781 | 200 | **783** | **200** |
| 401.bzip2 | 16 | 1276 | 121 | **1263** | **122** | 1261 | 122 | 16 | **1200** | **129** | 1202 | 128 | 1191 | 130 |
| 403.gcc | 16 | **810** | **159** | 813 | 158 | 806 | 160 | 16 | 818 | 157 | **819** | **157** | 829 | 155 |
| 429.mcf | 16 | 650 | 224 | **649** | **225** | 646 | 226 | 8 | 328 | 223 | **328** | **223** | 328 | 222 |
| 445.gobmk | 16 | 899 | 187 | **914** | **184** | 914 | 184 | 16 | 818 | 205 | **828** | **203** | 830 | 202 |
| 456.hmmer | 16 | 1081 | 138 | **1078** | **138** | 1078 | 139 | 8 | 409 | 183 | 409 | 183 | **409** | **183** |
| 458.sjeng | 16 | 1112 | 174 | 1116 | 174 | **1114** | **174** | 16 | **1008** | **192** | 1010 | 192 | 1007 | 192 |
| 462.libquantum | 16 | 530 | 626 | 529 | 627 | **529** | **626** | 16 | 529 | 627 | **529** | **627** | 531 | 625 |
| 464.h264ref | 16 | 1458 | 243 | **1502** | **236** | 1528 | 232 | 16 | **1405** | **252** | 1403 | 252 | 1464 | 242 |
| 471.omnetpp | 16 | 684 | 146 | **684** | **146** | 685 | 146 | 16 | 684 | 146 | **684** | **146** | 685 | 146 |
| 473.astar | 16 | 916 | 123 | 920 | 122 | **916** | **123** | 16 | 822 | 137 | 824 | 136 | **823** | **137** |
| 483.xalancbmk | 16 | 517 | 213 | **520** | **212** | 522 | 212 | 16 | 517 | 213 | **520** | **212** | 522 | 212 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

## Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

# Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

# Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20100202.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20100202.xml`


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For other inquiries, please contact webmaster@spec.org.

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