# SPEC® CINT2006 Result

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## Cisco Systems

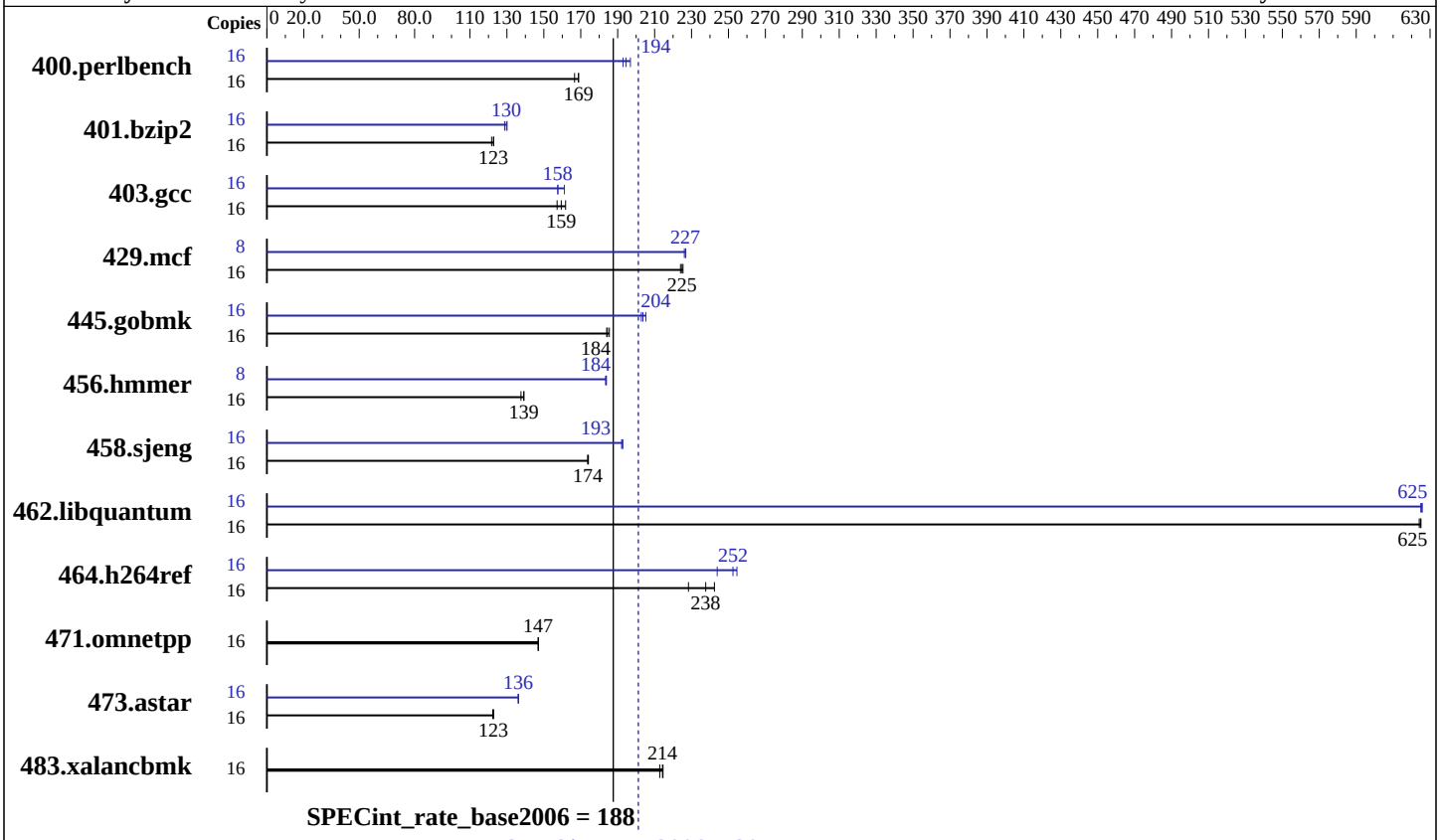
## UCS C200 M1 (Intel Xeon E5520)

**SPECint®_rate2006 = 201**

**SPECint_rate_base2006 = 188**

**CPU2006 license:** 9019
**Test sponsor:** Cisco Systems
**Tested by:** Cisco Systems

**Test date:** Jan-2010
**Hardware Availability:** Oct-2009
**Software Availability:** Mar-2009




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5520 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.53 GHz |
| CPU MHz: | 2267 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 Chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (12 * 2GB DDR3-1066 MHz) |
| Disk Subsystem: | 73 GB SATA, 15kRPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SuSe Linux Enterprise Server 11 (x86_64), Kernel 2.6.27-15-2-default, RC4 |
| Compiler: | Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080 |
| Auto Parallel: | No |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Binutils 2.18.50.0.7.20080502 and SmartHeap |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | **926** | **169** | 938 | 167 | 926 | 169 | 16 | 810 | 193 | 794 | 197 | **804** | **194** |
| 401.bzip2 | 16 | 1269 | 122 | **1258** | **123** | 1257 | 123 | 16 | 1199 | 129 | **1190** | **130** | 1187 | 130 |
| 403.gcc | 16 | 796 | 162 | 819 | 157 | **808** | **159** | 16 | 799 | 161 | 817 | 158 | **817** | **158** |
| 429.mcf | 16 | 647 | 225 | 651 | 224 | **649** | **225** | 8 | **322** | **227** | 323 | 226 | 322 | 227 |
| 445.gobmk | 16 | 905 | 185 | 912 | 184 | **910** | **184** | 16 | 818 | 205 | **824** | **204** | 828 | 203 |
| 456.hmmer | 16 | **1073** | **139** | 1072 | 139 | 1085 | 138 | 8 | **406** | **184** | 407 | 183 | 406 | 184 |
| 458.sjeng | 16 | 1114 | 174 | **1113** | **174** | 1112 | 174 | 16 | 1008 | 192 | **1005** | **193** | 1004 | 193 |
| 462.libquantum | 16 | 530 | 625 | 531 | 624 | **531** | **625** | 16 | **530** | **625** | 530 | 625 | 530 | 626 |
| 464.h264ref | 16 | 1461 | 242 | 1551 | 228 | **1490** | **238** | 16 | **1403** | **252** | 1391 | 255 | 1452 | 244 |
| 471.omnetpp | 16 | 680 | 147 | **680** | **147** | 680 | 147 | 16 | 680 | 147 | **680** | **147** | 680 | 147 |
| 473.astar | 16 | 918 | 122 | **916** | **123** | 915 | 123 | 16 | 825 | 136 | **825** | **136** | 824 | 136 |
| 483.xalancbmk | 16 | 515 | 215 | **515** | **214** | 519 | 213 | 16 | 515 | 215 | **515** | **214** | 519 | 213 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
ulimit -s unlimited was used to set the stacksize to unlimited prior to run
```

## Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revH.20100317.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revH.20100317.xml`


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