# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

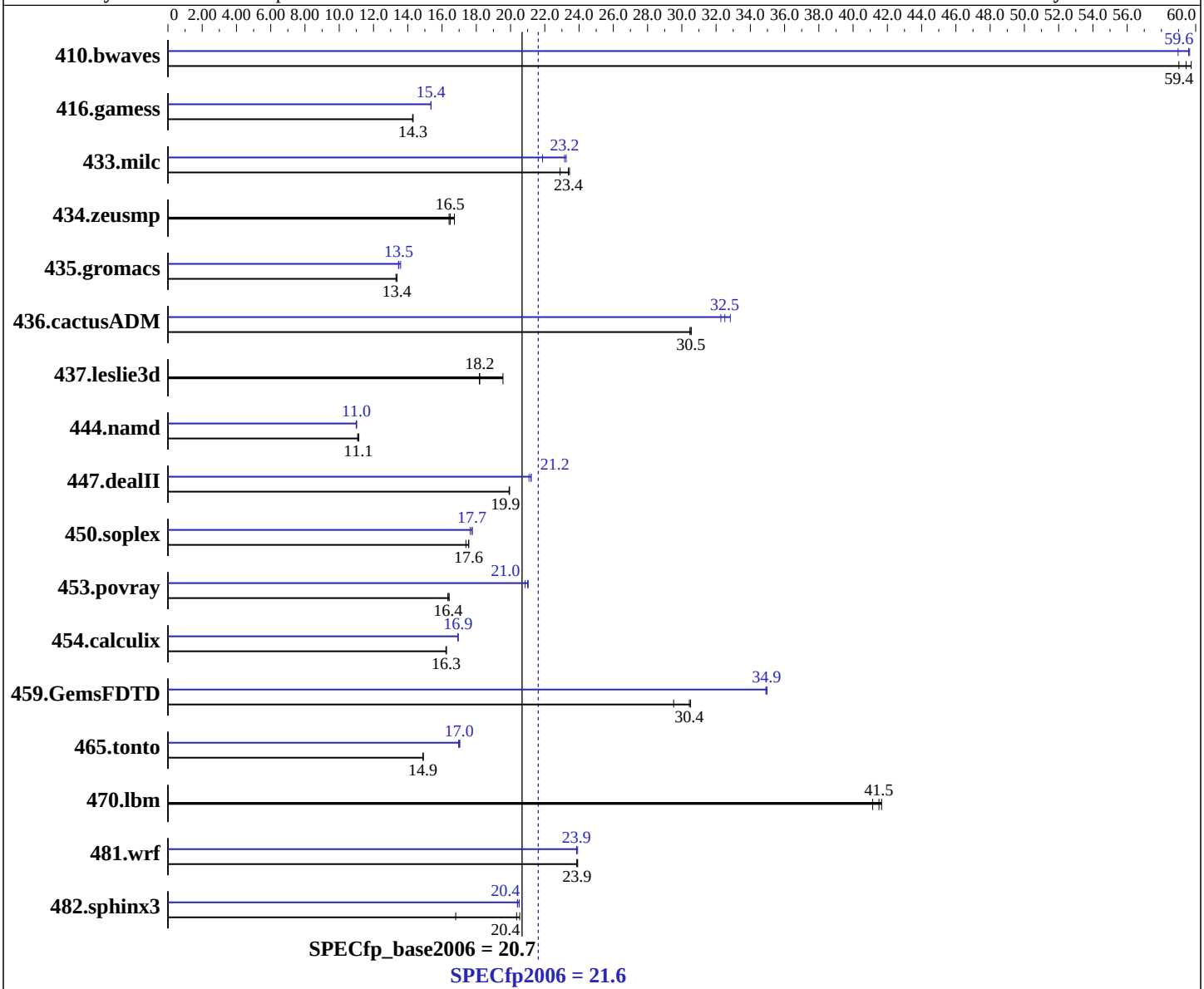
## Gateway GR160 F1 (Intel Xeon E5502)

SPECfp®2006 = 21.6

SPECfp_base2006 = 20.7

**CPU2006 license:** 97
**Test sponsor:** Acer Incorporated
**Tested by:** Acer Incorporated

**Test date:** Dec-2009
**Hardware Availability:** Jan-2010
**Software Availability:** Feb-2009




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5502 |
| CPU Characteristics: | |
| CPU MHz: | 1867 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 2 chips, 2 cores/chip |
| CPU(s) orderable: | 1, 2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

Continued on next page

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5 |
| Compiler: | Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080 |
| Auto Parallel: | Yes |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |

Continued on next page

# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

## Gateway GR160 F1 (Intel Xeon E5502)

SPECfp2006 = 21.6

SPECfp_base2006 = 20.7

**CPU2006 license:** 97
**Test sponsor:** Acer Incorporated
**Tested by:** Acer Incorporated

**Test date:** Dec-2009
**Hardware Availability:** Jan-2010
**Software Availability:** Feb-2009

L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM)
Disk Subsystem: 300 GB SATA II, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 227 | 59.7 | **229** | **59.4** | 230 | 59.0 | 228 | 59.6 | 230 | 59.0 | **228** | **59.6** |
| 416.gamess | 1368 | 14.3 | 1371 | 14.3 | **1370** | **14.3** | 1275 | 15.4 | 1275 | 15.4 | **1275** | **15.4** |
| 433.milc | 392 | 23.4 | 401 | 22.9 | **393** | **23.4** | **396** | **23.2** | 420 | 21.9 | 395 | 23.2 |
| 434.zeusmp | 554 | 16.4 | 544 | 16.7 | **552** | **16.5** | 554 | 16.4 | 544 | 16.7 | **552** | **16.5** |
| 435.gromacs | 534 | 13.4 | **534** | **13.4** | 536 | 13.3 | 530 | 13.5 | **530** | **13.5** | 526 | 13.6 |
| 436.cactusADM | **391** | **30.5** | 391 | 30.5 | 392 | 30.5 | 364 | 32.8 | **368** | **32.5** | 370 | 32.3 |
| 437.leslie3d | **516** | **18.2** | 517 | 18.2 | 481 | 19.6 | **516** | **18.2** | 517 | 18.2 | 481 | 19.6 |
| 444.namd | 720 | 11.1 | **721** | **11.1** | 724 | 11.1 | 728 | 11.0 | **729** | **11.0** | 730 | 11.0 |
| 447.dealII | 574 | 19.9 | 574 | 19.9 | **574** | **19.9** | 539 | 21.2 | **540** | **21.2** | 543 | 21.1 |
| 450.soplex | **475** | **17.6** | 475 | 17.6 | 479 | 17.4 | 469 | 17.8 | **470** | **17.7** | 473 | 17.6 |
| 453.povray | 324 | 16.4 | 326 | 16.3 | **325** | **16.4** | **253** | **21.0** | 255 | 20.8 | 253 | 21.0 |
| 454.calculix | **508** | **16.3** | 508 | 16.3 | 508 | 16.2 | 487 | 16.9 | **487** | **16.9** | 487 | 16.9 |
| 459.GemsFDTD | 348 | 30.5 | **349** | **30.4** | 359 | 29.5 | **304** | **34.9** | 304 | 34.9 | 303 | 35.0 |
| 465.tonto | 661 | 14.9 | 660 | 14.9 | **661** | **14.9** | **578** | **17.0** | 580 | 17.0 | 577 | 17.0 |
| 470.lbm | **331** | **41.5** | 330 | 41.7 | 334 | 41.1 | **331** | **41.5** | 330 | 41.7 | 334 | 41.1 |
| 481.wrf | **468** | **23.9** | 468 | 23.9 | 467 | 23.9 | **468** | **23.9** | 467 | 23.9 | 468 | 23.9 |
| 482.sphinx3 | 949 | 20.5 | 1160 | 16.8 | **957** | **20.4** | 950 | 20.5 | 955 | 20.4 | **954** | **20.4** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

This result was measured on the Gateway GR160 F1.
The Acer AR160 F1 and Gateway GR160 F1 are electronically equivalent.
```

## Base Compiler Invocation

C benchmarks:
icc

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

# Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

# Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

# Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-fno-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`

Continued on next page

# Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

Continued on next page

# Peak Optimization Flags (Continued)

454.calculix: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100201.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100201.xml`


SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 06:41:16 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 February 2010.