SPEC® CINT2006 Result

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Dell Inc.

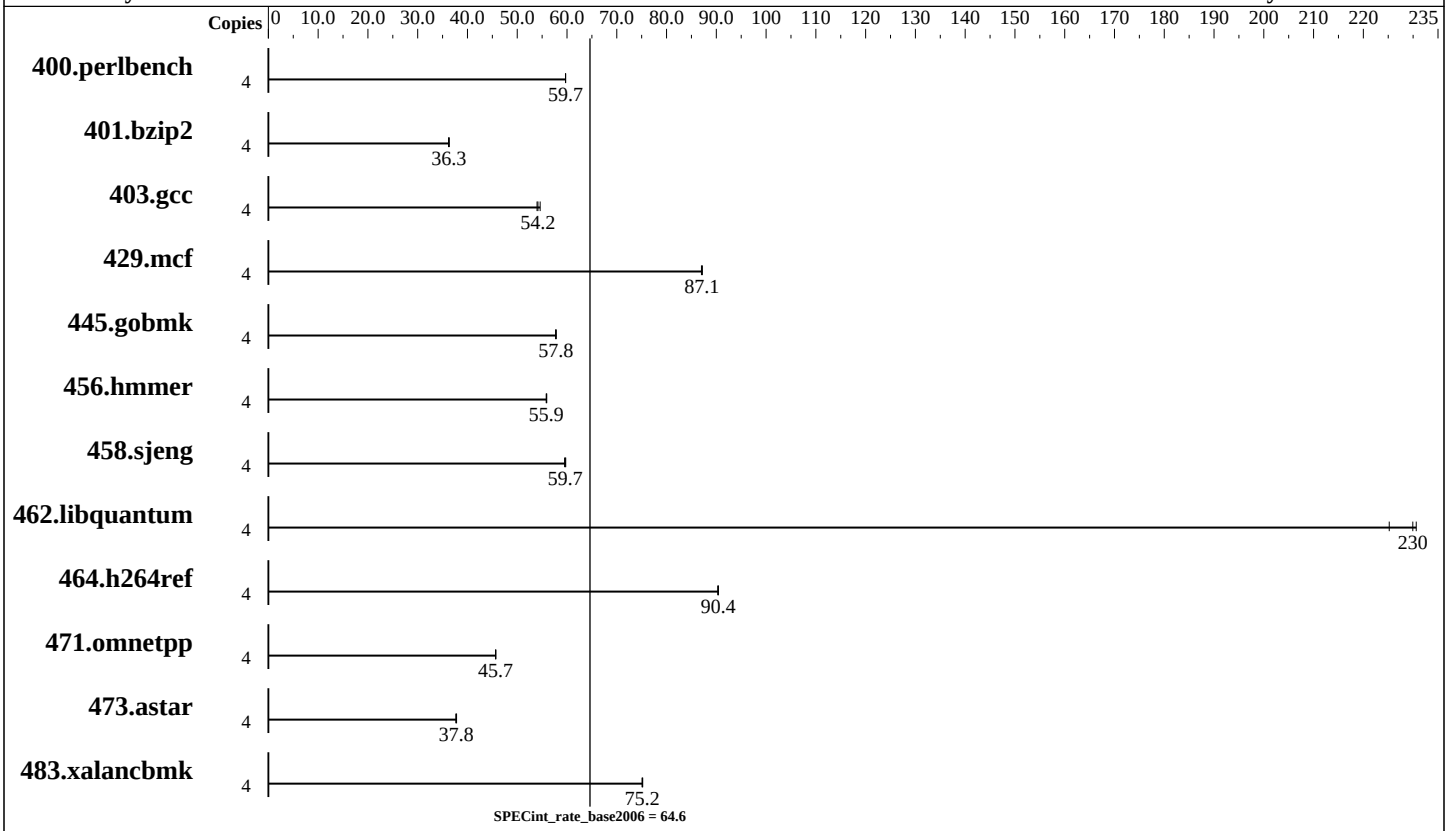
PowerEdge T410 (Intel Xeon E5504, 2.0 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 64.6

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2009
Hardware Availability: May-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon E5504
CPU Characteristics:	
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	12 GB (3 x 4 GB DDR3-1066 DR RDIMM downclocked to 800MHz)
Disk Subsystem:	1 x 160 GB 7200 RPM SATA
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	655	59.7	**655**	**59.7**	654	59.7							
401.bzip2	4	**1064**	**36.3**	1062	36.3	1066	36.2							
403.gcc	4	590	54.6	597	54.0	**595**	**54.2**							
429.mcf	4	418	87.2	419	87.0	**419**	**87.1**							
445.gobmk	4	727	57.7	726	57.8	**726**	**57.8**							
456.hmmer	4	**668**	**55.9**	667	55.9	668	55.8							
458.sjeng	4	810	59.7	**811**	**59.7**	813	59.5							
462.libquantum	4	359	231	368	225	**360**	**230**							
464.h264ref	4	**979**	**90.4**	979	90.4	980	90.3							
471.omnetpp	4	547	45.7	**547**	**45.7**	548	45.6							
473.astar	4	743	37.8	745	37.7	**744**	**37.8**							
483.xalancbmk	4	368	75.1	**367**	**75.2**	367	75.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.xml`

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For other inquiries, please contact webmaster@spec.org.

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