SPEC® CINT2006 Result

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Dell Inc.

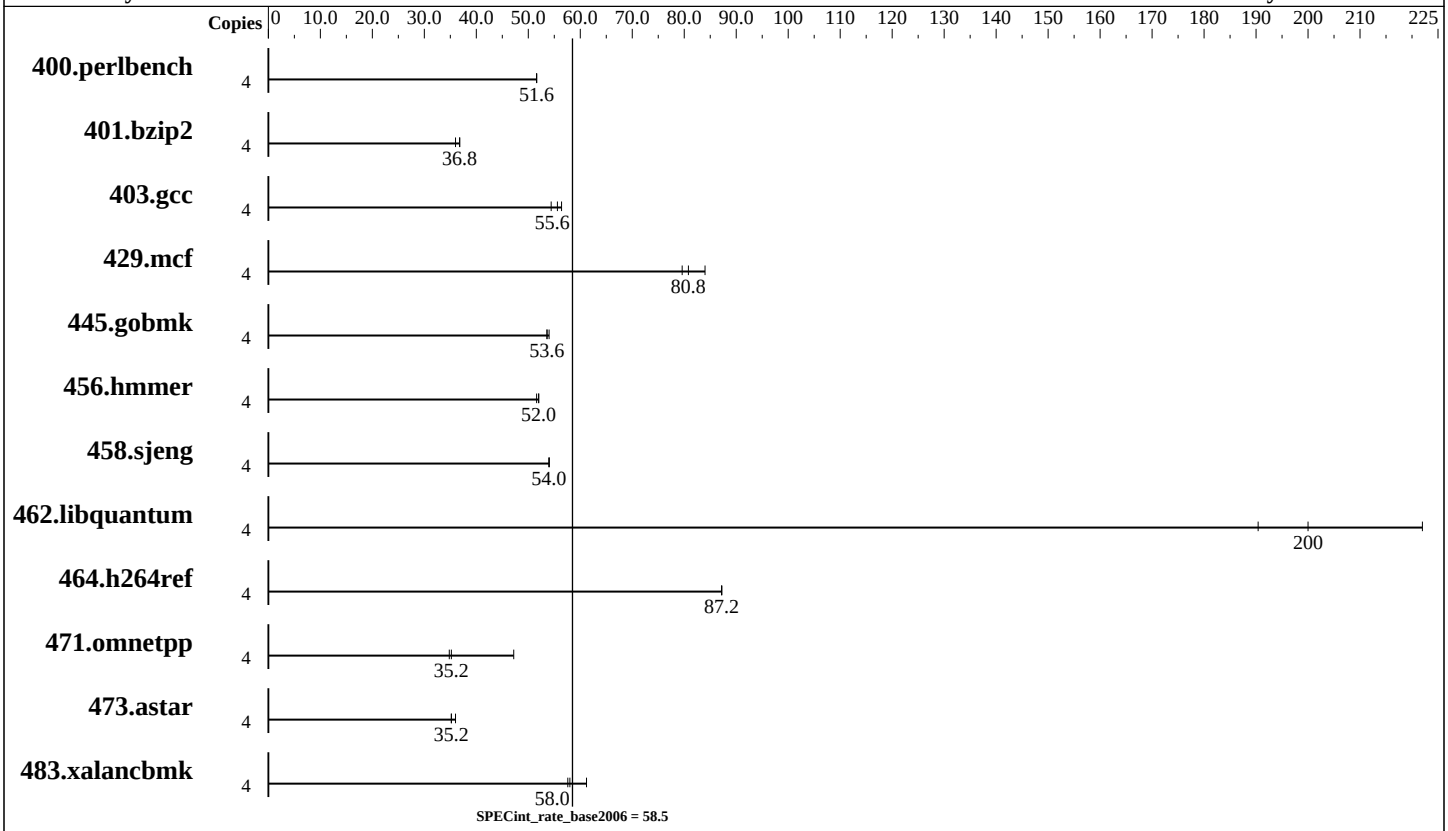
PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 58.5

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (4 x 1 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
Disk Subsystem:	1 x 73 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2008 Enterprise x64 Edition SP2
Compiler:	Intel C++ Compiler for IA-32, Version 11.0 Build 20090131 Package ID: w_cproc_p_11.0.072 Microsoft Visual Studio 2008 SP1
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**757**	**51.6**	756	51.6	758	51.6							
401.bzip2	4	1051	36.8	1068	36.0	**1051**	**36.8**							
403.gcc	4	570	56.4	593	54.4	**579**	**55.6**							
429.mcf	4	434	84.0	458	79.6	**451**	**80.8**							
445.gobmk	4	782	53.6	**781**	**53.6**	779	54.0							
456.hmmer	4	**720**	**52.0**	720	52.0	722	51.6							
458.sjeng	4	896	54.0	**897**	**54.0**	897	54.0							
462.libquantum	4	**414**	**200**	373	222	435	190							
464.h264ref	4	**1014**	**87.2**	1013	87.2	1014	87.2							
471.omnetpp	4	**712**	**35.2**	528	47.2	715	34.8							
473.astar	4	777	36.0	797	35.2	**797**	**35.2**							
483.xalancbmk	4	450	61.2	479	57.6	**477**	**58.0**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Business (64-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features
/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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