SPEC® CINT2006 Result

Dell Inc.

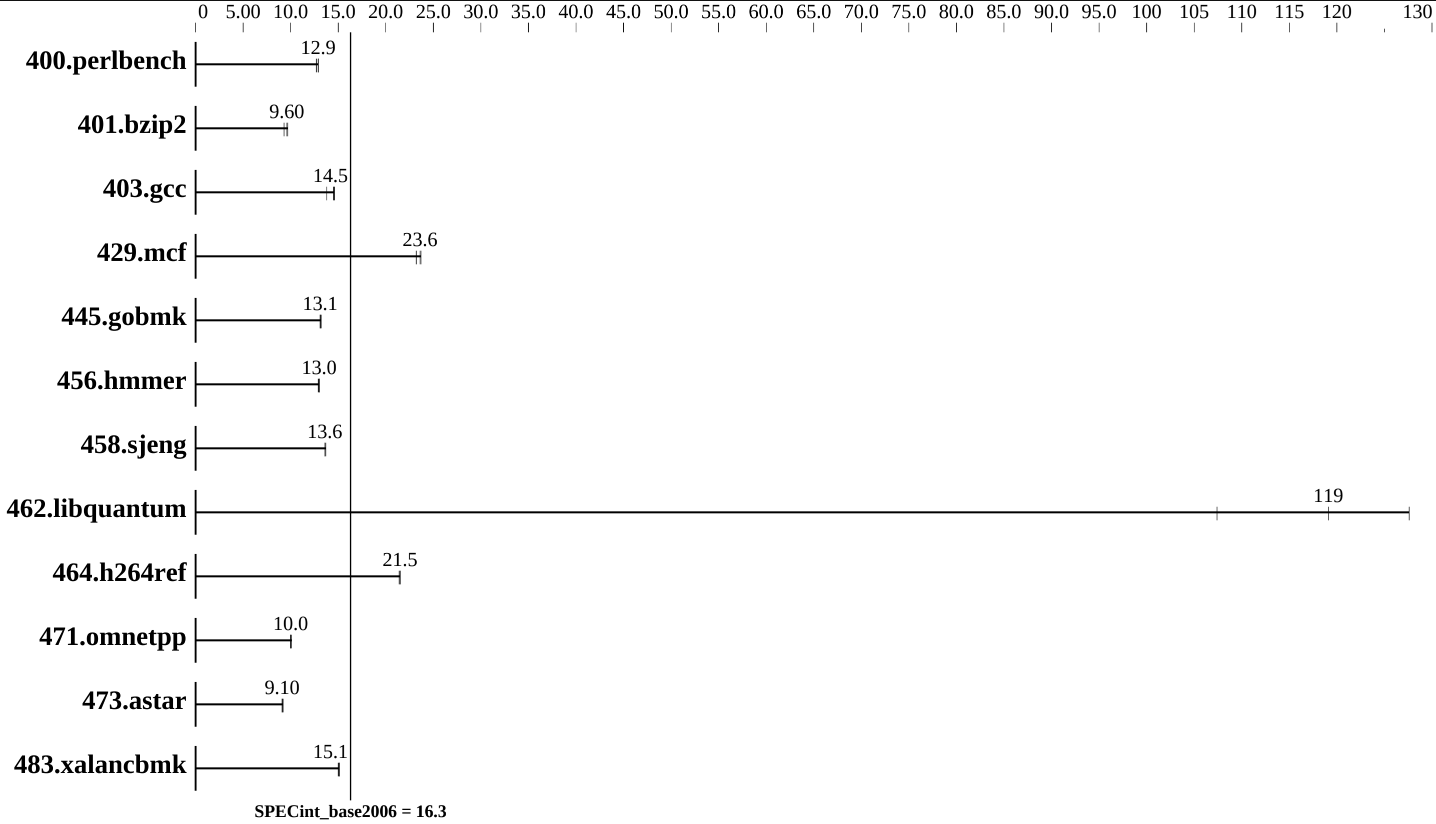
PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECint®2006 = Not Run

SPECint_base2006 = 16.3

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (4 x 1 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
Disk Subsystem:	1 x 73 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2008 Enterprise x64 Edition SP2
Compiler:	Intel C++ Compiler for IA-32, Version 11.0 Build 20090131 Package ID: w_cproc_p_11.0.072 Microsoft Visual Studio 2008 SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	768	12.7	**758**	**12.9**	757	12.9						
401.bzip2	994	9.70	**1010**	**9.60**	1036	9.30						
403.gcc	583	13.8	**556**	**14.5**	553	14.6						
429.mcf	394	23.2	385	23.7	**386**	**23.6**						
445.gobmk	797	13.2	798	13.1	**798**	**13.1**						
456.hmmer	721	12.9	719	13.0	**720**	**13.0**						
458.sjeng	889	13.6	886	13.7	**887**	**13.6**						
462.libquantum	193	107	162	128	**174**	**119**						
464.h264ref	1032	21.4	**1030**	**21.5**	1030	21.5						
471.omnetpp	**627**	**10.0**	620	10.1	627	10.0						
473.astar	770	9.10	**770**	**9.10**	765	9.20						
483.xalancbmk	**458**	**15.1**	456	15.1	459	15.0						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Business (64-bit)
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features
/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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