SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

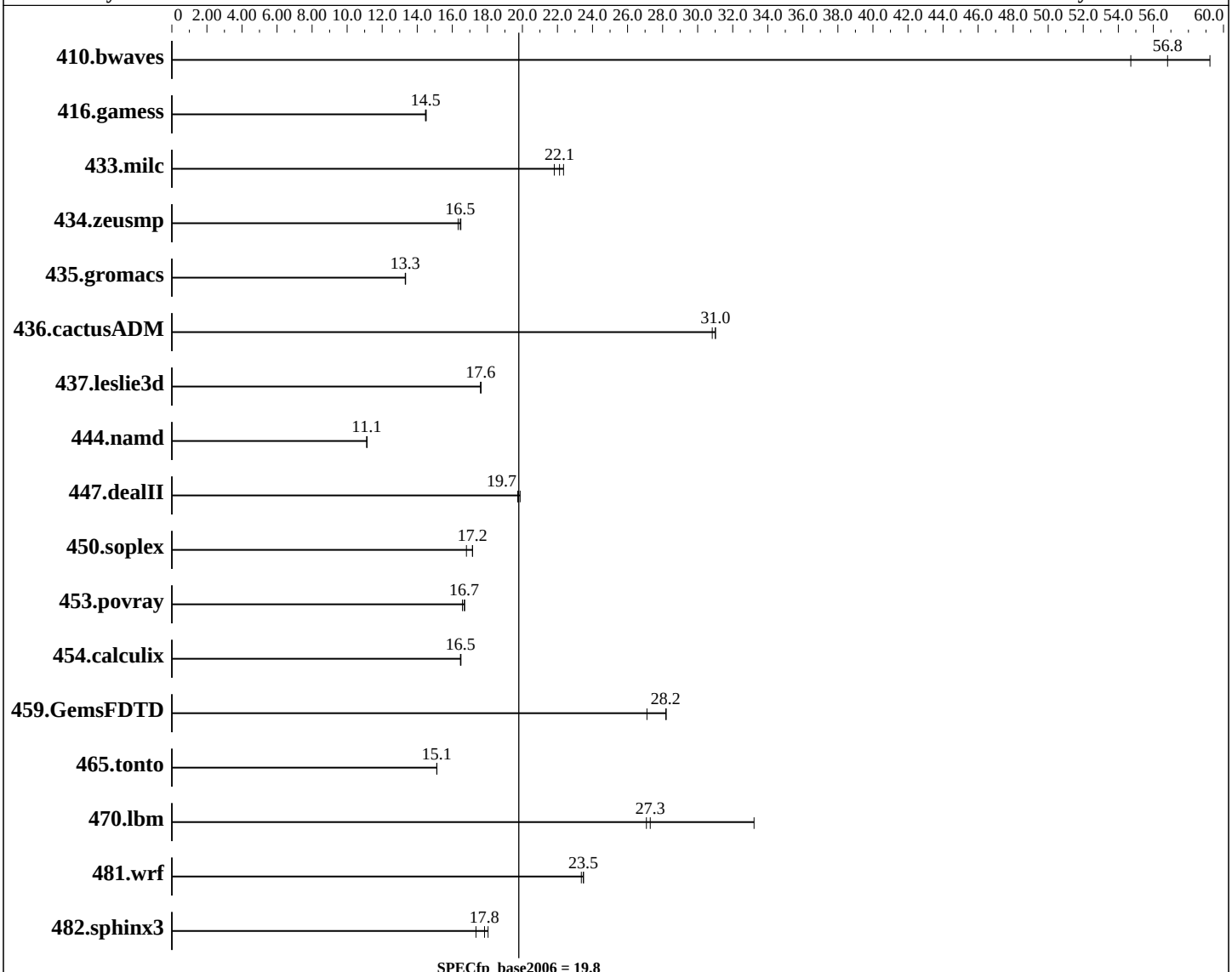
PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECfp®2006 = Not Run

SPECfp_base2006 = 19.8

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECfp2006 = Not Run

SPECfp_base2006 = 19.8

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (4 x 1 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
Disk Subsystem: 1 x 73 GB 15000 RPM SAS
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	229	59.2	**239**	**56.8**	248	54.7						
416.gamess	1353	14.5	**1352**	**14.5**	1349	14.5						
433.milc	**415**	**22.1**	421	21.8	411	22.3						
434.zeusmp	**553**	**16.5**	552	16.5	557	16.3						
435.gromacs	536	13.3	535	13.3	**536**	**13.3**						
436.cactusADM	**385**	**31.0**	385	31.0	388	30.8						
437.leslie3d	**533**	**17.6**	533	17.6	534	17.6						
444.namd	721	11.1	**721**	**11.1**	721	11.1						
447.dealII	**579**	**19.7**	576	19.9	580	19.7						
450.soplex	496	16.8	**486**	**17.2**	486	17.2						
453.povray	318	16.7	**319**	**16.7**	321	16.6						
454.calculix	501	16.5	**501**	**16.5**	500	16.5						
459.GemsFDTD	376	28.2	**377**	**28.2**	391	27.1						
465.tonto	651	15.1	**651**	**15.1**	651	15.1						
470.lbm	414	33.2	**503**	**27.3**	507	27.1						
481.wrf	476	23.5	**476**	**23.5**	478	23.4						
482.sphinx3	1123	17.4	**1093**	**17.8**	1080	18.0						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:42:49 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 13 October 2009.