SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

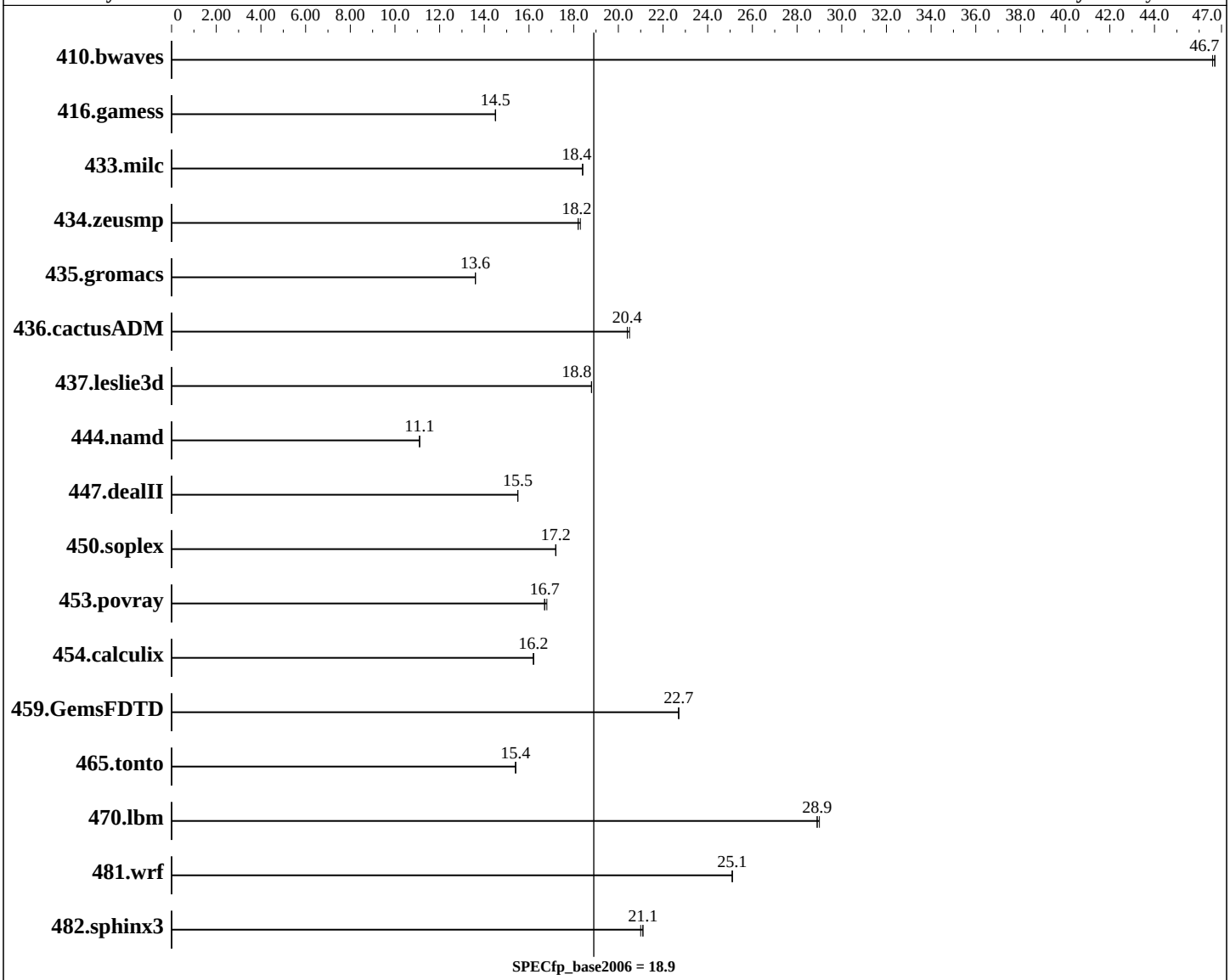
PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECfp®2006 = Not Run

SPECfp_base2006 = 18.9

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2009
Hardware Availability: Mar-2009
Software Availability: May-2009



Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Microsoft Windows Server 2008 Enterprise x64 Edition SP2
Compiler:	Intel C++ Compiler for Intel 64, Version 11.0 Build 20090131 Package ID: w_cproc_p_11.0.072 Intel Visual Fortran Compiler for Intel 64, Version 11.0 Build 20090131 Package ID: w_cprof_p_11.0.072 Microsoft Visual Studio 2008 SP1
Auto Parallel:	Yes
File System:	NTFS

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECfp2006 = Not Run

SPECfp_base2006 = 18.9

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2009
Hardware Availability: Mar-2009
Software Availability: May-2009

L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 2 GB (2 x 1 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
Disk Subsystem: 1 x 73 GB 15000 RPM SAS
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: Not Applicable
Other Software: MicroQuill SmartHeap Library 8.1 for x64

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	292	46.6	291	46.7	**291**	**46.7**						
416.gamess	1350	14.5	**1350**	**14.5**	1349	14.5						
433.milc	**499**	**18.4**	499	18.4	498	18.4						
434.zeusmp	499	18.2	499	18.3	**499**	**18.2**						
435.gromacs	**527**	**13.6**	527	13.6	527	13.6						
436.cactusADM	**586**	**20.4**	582	20.5	586	20.4						
437.leslie3d	500	18.8	500	18.8	**500**	**18.8**						
444.namd	724	11.1	724	11.1	**724**	**11.1**						
447.dealII	**738**	**15.5**	738	15.5	737	15.5						
450.soplex	**485**	**17.2**	485	17.2	485	17.2						
453.povray	317	16.8	**318**	**16.7**	318	16.7						
454.calculix	508	16.2	**508**	**16.2**	508	16.2						
459.GemsFDTD	467	22.7	468	22.7	**468**	**22.7**						
465.tonto	641	15.4	**641**	**15.4**	640	15.4						
470.lbm	**475**	**28.9**	474	29.0	475	28.9						
481.wrf	446	25.1	**446**	**25.1**	446	25.1						
482.sphinx3	**923**	**21.1**	926	21.0	923	21.1						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Business (64-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

Continued on next page

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 /assume:underscore
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qparallel
-Qopt-prefetch /F512000000

C++ benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qparallel
-Qopt-prefetch -Qcxx_features /F512000000 shlw64mt.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qparallel
-Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qparallel
-Qopt-prefetch /F1000000000

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:36:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 13 October 2009.