SPEC® CINT2006 Result

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Fujitsu

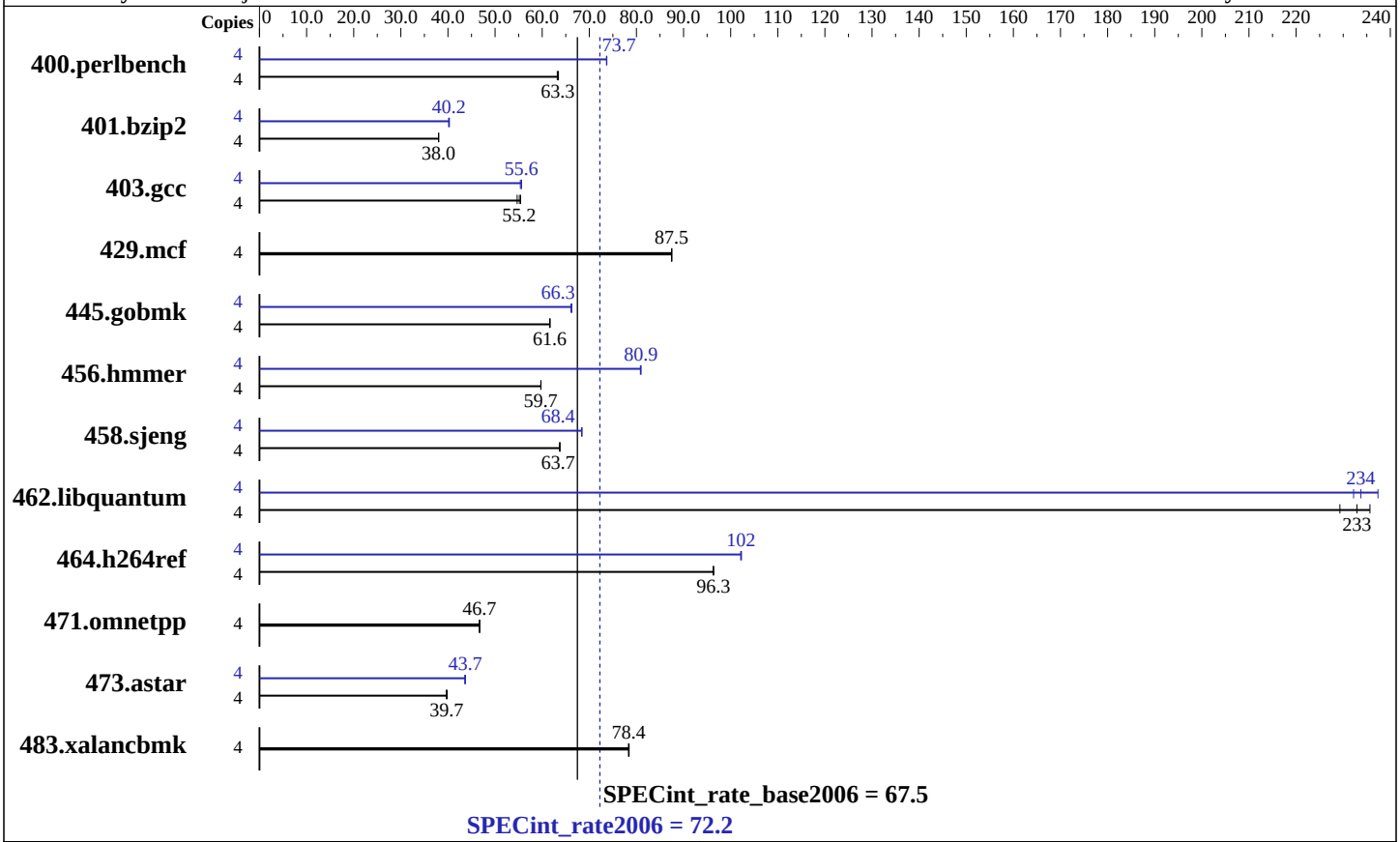
PRIMERGY RX300 S5, Intel Xeon E5506, 2.13 GHz

SPECint®_rate2006 = 72.2

SPECint_rate_base2006 = 67.5

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon E5506
CPU Characteristics:	
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	36 GB (9x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem:	1 x SATA, 250 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**617**	**63.3**	616	63.5	618	63.2	4	530	73.7	**531**	**73.7**	531	73.7
401.bzip2	4	1016	38.0	1014	38.1	**1016**	**38.0**	4	**959**	**40.2**	958	40.3	962	40.1
403.gcc	4	581	55.4	589	54.7	**584**	**55.2**	4	579	55.6	**579**	**55.6**	582	55.4
429.mcf	4	**417**	**87.5**	417	87.5	417	87.5	4	**417**	**87.5**	417	87.5	417	87.5
445.gobmk	4	**681**	**61.6**	681	61.6	681	61.7	4	**633**	**66.3**	635	66.1	633	66.3
456.hmmer	4	625	59.7	**625**	**59.7**	625	59.7	4	461	81.0	462	80.8	**461**	**80.9**
458.sjeng	4	**759**	**63.7**	760	63.7	759	63.8	4	708	68.3	707	68.4	**707**	**68.4**
462.libquantum	4	352	236	**356**	**233**	361	229	4	357	232	349	237	**355**	**234**
464.h264ref	4	**919**	**96.3**	919	96.3	918	96.4	4	865	102	**866**	**102**	867	102
471.omnetpp	4	535	46.7	**535**	**46.7**	536	46.7	4	535	46.7	**535**	**46.7**	536	46.7
473.astar	4	**707**	**39.7**	705	39.8	708	39.6	4	642	43.7	645	43.6	**643**	**43.7**
483.xalancbmk	4	352	78.4	**352**	**78.4**	352	78.3	4	352	78.4	**352**	**78.4**	352	78.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 800 MHz.

General Notes

This result was measured on the PRIMERGY TX300 S5. The PRIMERGY TX300 S5 and the PRIMERGY RX300 S5 are electronically equivalent.

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):
icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.xml`

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For other inquiries, please contact webmaster@spec.org.

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