



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp[®]_rate2006 = Not Run

PowerEdge R410 (Intel Xeon E5502, 1.86 GHz)

SPECfp_rate_base2006 = 49.8

CPU2006 license: 55

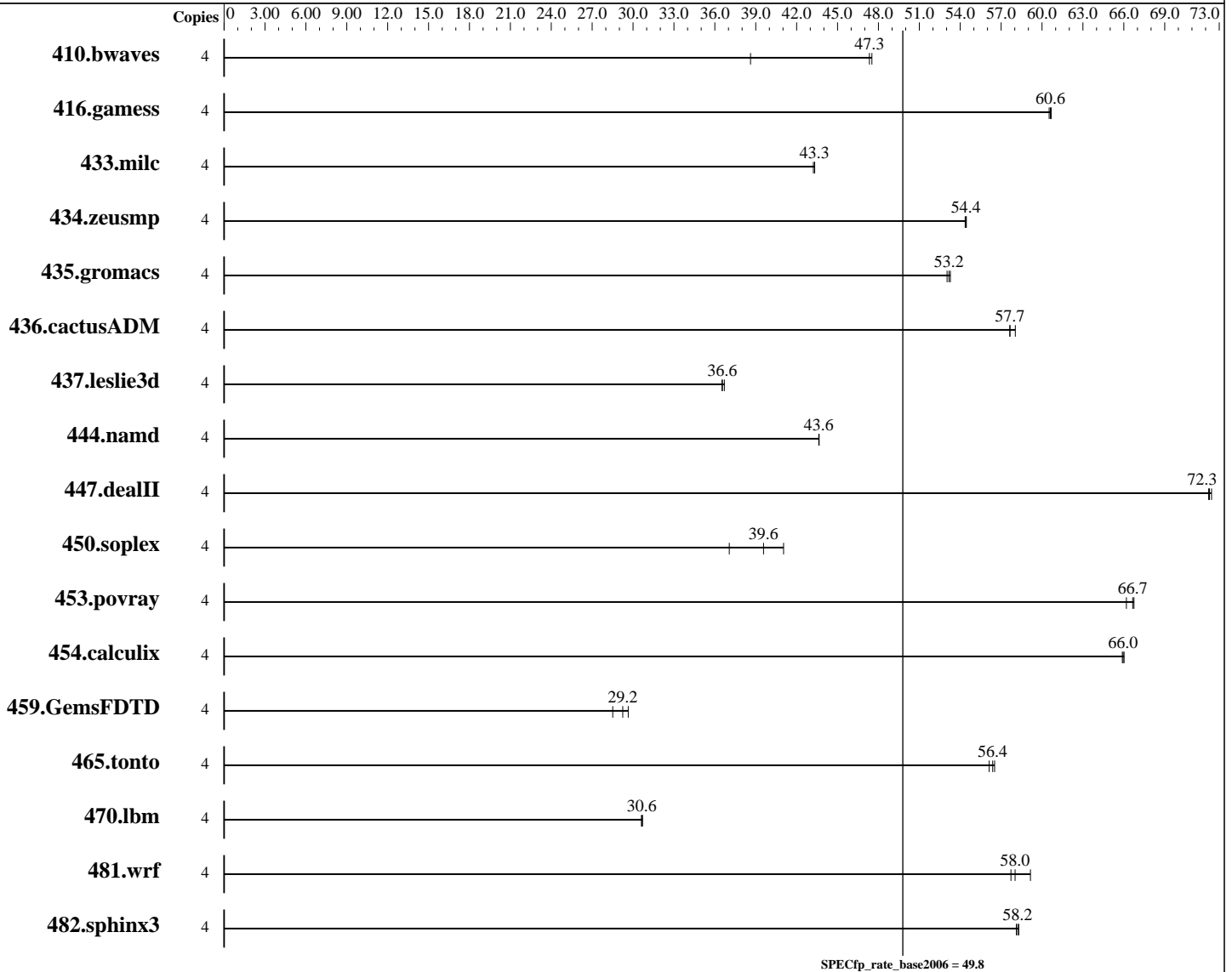
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Sep-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics:
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ and Fortran Compiler Professional 11.0 for Linux
 Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = Not Run

PowerEdge R410 (Intel Xeon E5502, 1.86 GHz)

SPECfp_rate_base2006 = 49.8

CPU2006 license: 55

Test date: Sep-2009

Test sponsor: Dell Inc.

Hardware Availability: Jun-2009

Tested by: Dell Inc.

Software Availability: Feb-2009

L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (2 x 2 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
Disk Subsystem: 1 x 73 GB 15000 RPM SAS
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1408	38.6	1144	47.5	1148	47.3							
416.gamess	4	1291	60.7	1292	60.6	1294	60.5							
433.milc	4	849	43.2	848	43.3	848	43.3							
434.zeusmp	4	669	54.4	670	54.4	669	54.4							
435.gromacs	4	536	53.3	537	53.2	539	53.0							
436.cactusADM	4	829	57.7	824	58.0	829	57.6							
437.leslie3d	4	1029	36.6	1029	36.5	1025	36.7							
444.namd	4	735	43.7	735	43.6	735	43.6							
447.dealII	4	634	72.2	632	72.4	633	72.3							
450.soplex	4	813	41.0	900	37.1	843	39.6							
453.povray	4	319	66.7	321	66.2	319	66.7							
454.calculix	4	501	65.9	500	66.0	500	66.0							
459.GemsFDTD	4	1432	29.6	1489	28.5	1451	29.2							
465.tonto	4	698	56.4	701	56.1	696	56.5							
470.lbm	4	1795	30.6	1794	30.6	1790	30.7							
481.wrf	4	755	59.2	774	57.7	770	58.0							
482.sphinx3	4	1341	58.1	1339	58.2	1337	58.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = Not Run

PowerEdge R410 (Intel Xeon E5502, 1.86 GHz)

SPECfp_rate_base2006 = 49.8

CPU2006 license: 55

Test date: Sep-2009

Test sponsor: Dell Inc.

Hardware Availability: Jun-2009

Tested by: Dell Inc.

Software Availability: Feb-2009

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp_rate2006 = Not Run

PowerEdge R410 (Intel Xeon E5502, 1.86 GHz)

SPECfp_rate_base2006 = 49.8

CPU2006 license: 55

Test date: Sep-2009

Test sponsor: Dell Inc.

Hardware Availability: Jun-2009

Tested by: Dell Inc.

Software Availability: Feb-2009

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 03:00:28 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 September 2009.