SPEC® CINT2006 Result

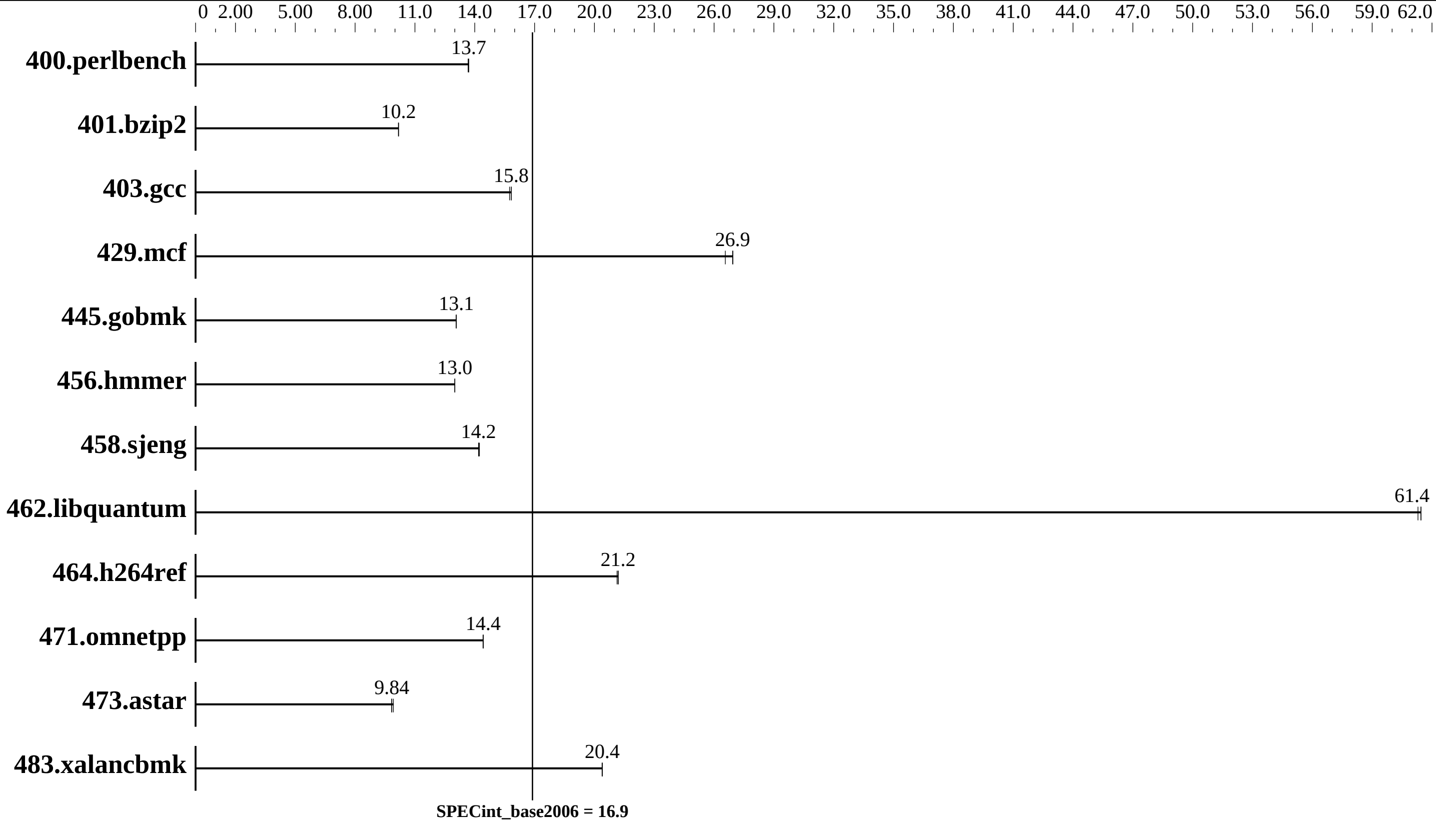
Dell Inc.

PowerEdge R410 (Intel Xeon E5502, 1.86 GHz)

SPECint®2006 = Not Run

SPECint_base2006 = 16.9

CPU2006 license: 55		**Test date:**	Sep-2009
Test sponsor:	Dell Inc.	**Hardware Availability:**	Jun-2009
Tested by:	Dell Inc.	**Software Availability:**	Feb-2009



Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	2 GB (2 x 1 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
Disk Subsystem:	1 x 73 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

	Base						Peak					
Benchmark	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**713**	**13.7**	713	13.7	715	13.7						
401.bzip2	948	10.2	949	10.2	**948**	**10.2**						
403.gcc	508	15.8	511	15.7	**508**	**15.8**						
429.mcf	343	26.6	**339**	**26.9**	338	26.9						
445.gobmk	803	13.1	802	13.1	**802**	**13.1**						
456.hmmer	717	13.0	718	13.0	**718**	**13.0**						
458.sjeng	853	14.2	850	14.2	**853**	**14.2**						
462.libquantum	337	61.4	**337**	**61.4**	338	61.3						
464.h264ref	**1044**	**21.2**	1047	21.1	1044	21.2						
471.omnetpp	433	14.4	**433**	**14.4**	434	14.4						
473.astar	714	9.83	**713**	**9.84**	708	9.92						
483.xalancbmk	338	20.4	338	20.4	**338**	**20.4**						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090805.01.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090805.01.xml

Tested with SPEC CPU2006 v1.1.
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