



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/B120a-d
(Intel Xeon L5520)

SPECfp[®]2006 = **31.3**

SPECfp_base2006 = **29.4**

CPU2006 license: 9006

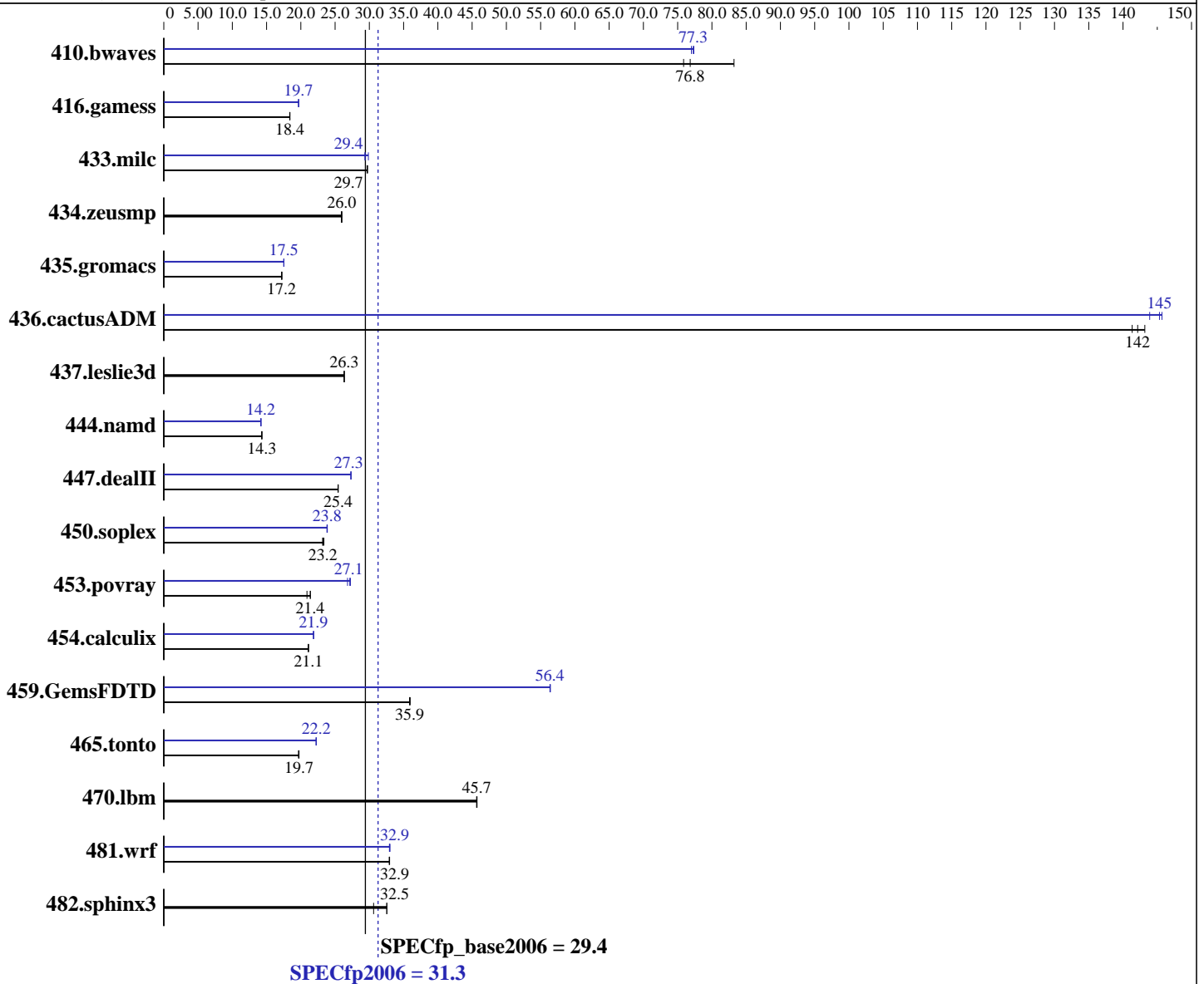
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Aug-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon L5520
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ and Fortran Compiler Professional 11.0 for Linux
 Build 20090131 Package ID: l_cproc_p_11.0.081, l_cprof_p_11.0.081
 Auto Parallel: Yes
 File System: ReiserFS

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/B120a-d
(Intel Xeon L5520)

SPECfp2006 = **31.3**

SPECfp_base2006 = **29.4**

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Aug-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009

L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 X 4 GB PC3-8500R, 2 rank, CL7, ECC)
Disk Subsystem: 1x73.2 GB SAS, 10000 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	179	75.9	177	76.8	163	83.2	176	77.3	176	77.4	176	77.0
416.gamess	1064	18.4	1064	18.4	1066	18.4	995	19.7	995	19.7	996	19.7
433.milc	309	29.7	310	29.7	309	29.7	307	29.9	312	29.4	313	29.4
434.zeusmp	350	26.0	351	25.9	350	26.0	350	26.0	351	25.9	350	26.0
435.gromacs	415	17.2	414	17.2	415	17.2	407	17.5	408	17.5	407	17.5
436.cactusADM	84.1	142	83.5	143	84.5	141	82.0	146	82.2	145	83.0	144
437.leslie3d	357	26.3	357	26.3	357	26.3	357	26.3	357	26.3	357	26.3
444.namd	560	14.3	560	14.3	561	14.3	566	14.2	566	14.2	565	14.2
447.dealII	450	25.4	450	25.4	450	25.4	419	27.3	419	27.3	419	27.3
450.soplex	360	23.2	357	23.4	359	23.2	349	23.9	350	23.8	350	23.8
453.povray	249	21.4	255	20.9	249	21.4	196	27.1	198	26.8	195	27.2
454.calculix	391	21.1	391	21.1	391	21.1	377	21.9	378	21.8	377	21.9
459.GemsFDTD	296	35.9	296	35.9	295	36.0	188	56.4	188	56.4	188	56.4
465.tonto	499	19.7	499	19.7	501	19.7	442	22.3	442	22.2	443	22.2
470.lbm	301	45.7	301	45.7	301	45.7	301	45.7	301	45.7	301	45.7
481.wrf	339	32.9	339	32.9	339	33.0	339	32.9	338	33.0	339	32.9
482.sphinx3	599	32.5	636	30.6	599	32.6	599	32.5	636	30.6	599	32.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

Platform Notes

BIOS setting:
NUMA configuration : Enabled



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/B120a-d
(Intel Xeon L5520)

SPECfp2006 = 31.3

SPECfp_base2006 = 29.4

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Aug-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/B120a-d
(Intel Xeon L5520)

SPECfp2006 = 31.3

SPECfp_base2006 = 29.4

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Aug-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009

Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/B120a-d
(Intel Xeon L5520)

SPECfp2006 = 31.3

SPECfp_base2006 = 29.4

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Aug-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/B120a-d
(Intel Xeon L5520)

SPECfp2006 = 31.3

SPECfp_base2006 = 29.4

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Aug-2009

Hardware Availability: Jun-2009

Software Availability: Feb-2009

Peak Optimization Flags (Continued)

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revH.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revH.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 02:08:51 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 1 September 2009.