SPEC® CINT2006 Result

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ACTION S.A.

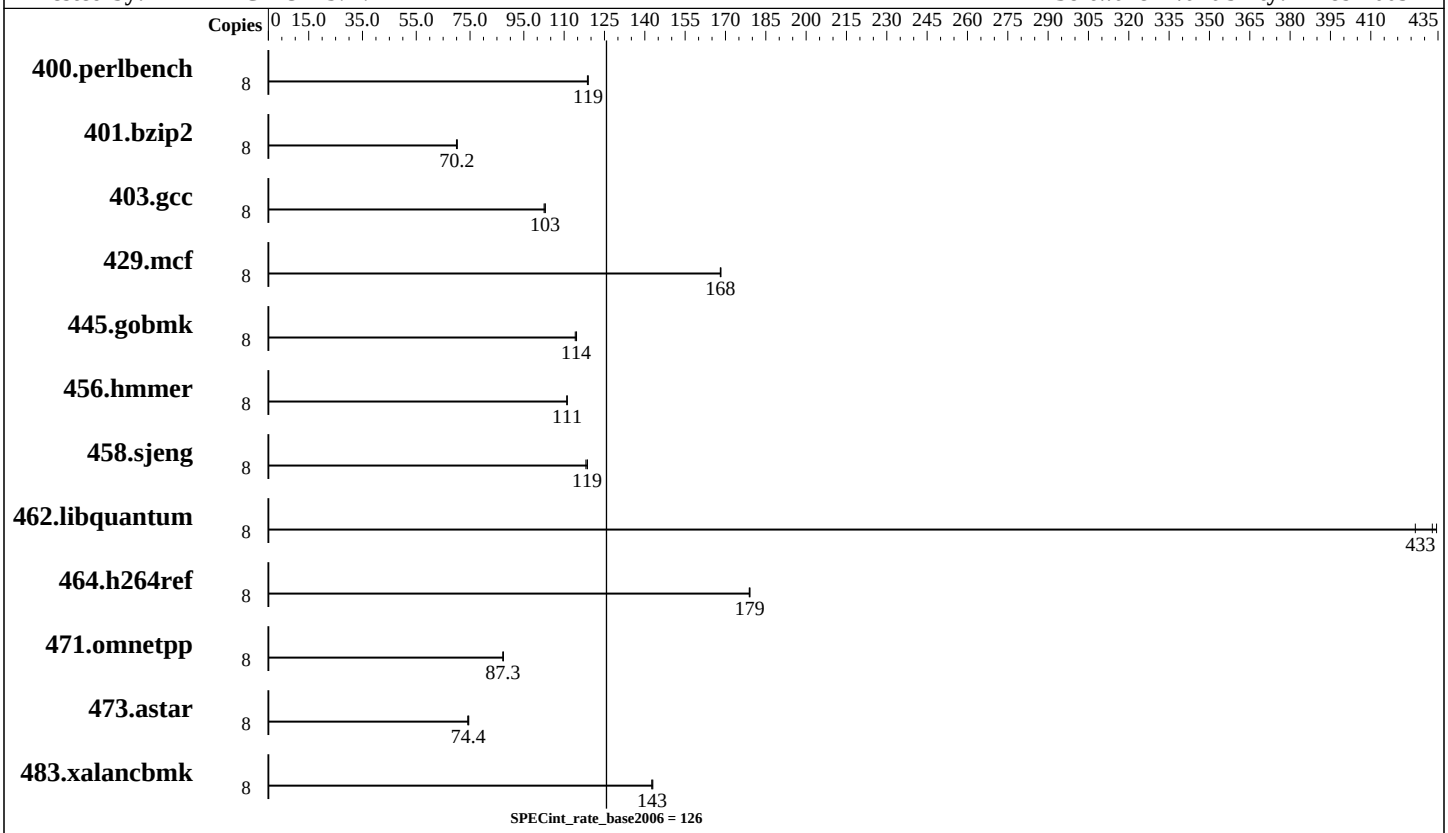
ACTINA SOLAR 200 X3 (Intel Xeon E5504)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 126

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Jul-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon E5504
CPU Characteristics:	
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB DDR3, 1066 MHz, Reg, ECC, running at 800
Disk Subsystem:	147 GB SAS, 15000 RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.18.50.0.7.20080502 Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	659	119	**657**	**119**	657	119							
401.bzip2	8	**1100**	**70.2**	1104	70.0	1100	70.2							
403.gcc	8	**626**	**103**	628	103	625	103							
429.mcf	8	**434**	**168**	434	168	433	168							
445.gobmk	8	732	115	**733**	**114**	735	114							
456.hmmer	8	672	111	672	111	**672**	**111**							
458.sjeng	8	816	119	**817**	**119**	820	118							
462.libquantum	8	382	434	389	427	**383**	**433**							
464.h264ref	8	989	179	**989**	**179**	990	179							
471.omnetpp	8	**573**	**87.3**	572	87.4	573	87.2							
473.astar	8	754	74.5	757	74.1	**755**	**74.4**							
483.xalancbmk	8	387	143	**387**	**143**	386	143							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
'numactl' was used to bind copies to the cores
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 03:26:42 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 18 August 2009.