SPEC® CINT2006 Result

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NEC Corporation

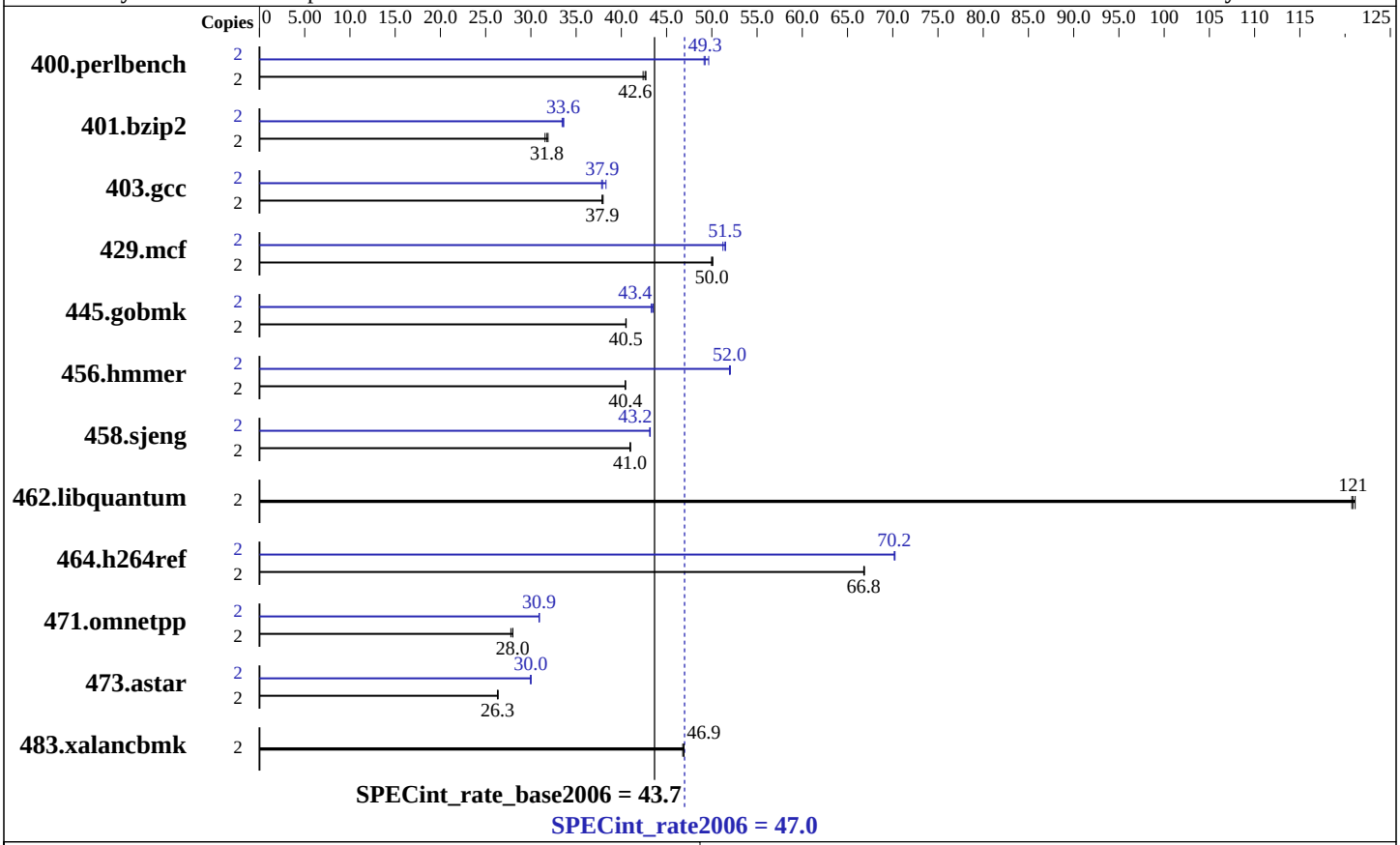
Express5800/iR110a-1
(Intel Xeon L3110)

SPECint®_rate2006 = 47.0

SPECint_rate_base2006 = 43.7

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Jul-2009
Hardware Availability: May-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon L3110
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)
Disk Subsystem:	1x160 GB SATA2, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler Professional 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.069
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**458**	**42.6**	461	42.4	457	42.7	2	**397**	**49.3**	398	49.1	393	49.7
401.bzip2	2	611	31.6	605	31.9	**607**	**31.8**	2	574	33.6	577	33.4	**575**	**33.6**
403.gcc	2	425	37.9	424	38.0	**425**	**37.9**	2	**425**	**37.9**	420	38.3	426	37.8
429.mcf	2	**365**	**50.0**	364	50.1	365	50.0	2	356	51.2	**354**	**51.5**	354	51.5
445.gobmk	2	**518**	**40.5**	518	40.5	518	40.5	2	484	43.3	**484**	**43.4**	482	43.5
456.hmmer	2	462	40.4	461	40.5	**461**	**40.4**	2	358	52.1	**359**	**52.0**	359	52.0
458.sjeng	2	591	41.0	**591**	**41.0**	590	41.0	2	560	43.2	**561**	**43.2**	561	43.1
462.libquantum	2	**343**	**121**	342	121	343	121	2	**343**	**121**	342	121	343	121
464.h264ref	2	663	66.8	**663**	**66.8**	662	66.9	2	**631**	**70.2**	631	70.2	630	70.3
471.omnetpp	2	446	28.0	450	27.8	**447**	**28.0**	2	405	30.9	404	30.9	**404**	**30.9**
473.astar	2	**533**	**26.3**	534	26.3	532	26.4	2	468	30.0	469	30.0	**468**	**30.0**
483.xalancbmk	2	294	46.9	**294**	**46.9**	295	46.8	2	294	46.9	**294**	**46.9**	295	46.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
Bios settings:
Hardware Prefetcher:              Enabled
Adjacent Cache Line Prefetch:    Disabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/069/bin/intel64/icc
-L/opt/intel/Compiler/11.0/069/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/069/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/069/bin/intel64/icc
-L/opt/intel/Compiler/11.0/069/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/069/ipp/em64t/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revG.html`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revG.xml`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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