SPEC® CINT2006 Result

Itautec

Servidor Itautec MX223 (Intel Xeon E5504)

SPECint®_rate2006 = 135

SPECint_rate_base2006 = 126

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Benchmark	Copies	Result
400.perlbench	8	138
400.perlbench	8	117
401.bzip2	8	72.8
401.bzip2	8	67.2
403.gcc	8	104
429.mcf	8	168
445.gobmk	8	122
445.gobmk	8	115
456.hmmer	8	151
456.hmmer	8	112
458.sjeng	8	128
458.sjeng	8	120
462.libquantum	8	438
464.h264ref	8	189
464.h264ref	8	179
471.omnetpp	8	88.3
473.astar	8	82.7
473.astar	8	74.7
483.xalancbmk	8	147

SPECint_rate_base2006 = 126

SPECint_rate2006 = 135

Hardware

CPU Name:	Intel Xeon E5504
CPU Characteristics:	
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	12 GB (6 x 2GB DDR3-1333, CL 9, ECC, running at 800 MHz)
Disk Subsystem:	1 x 160 GB SATA-2, 7200RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation
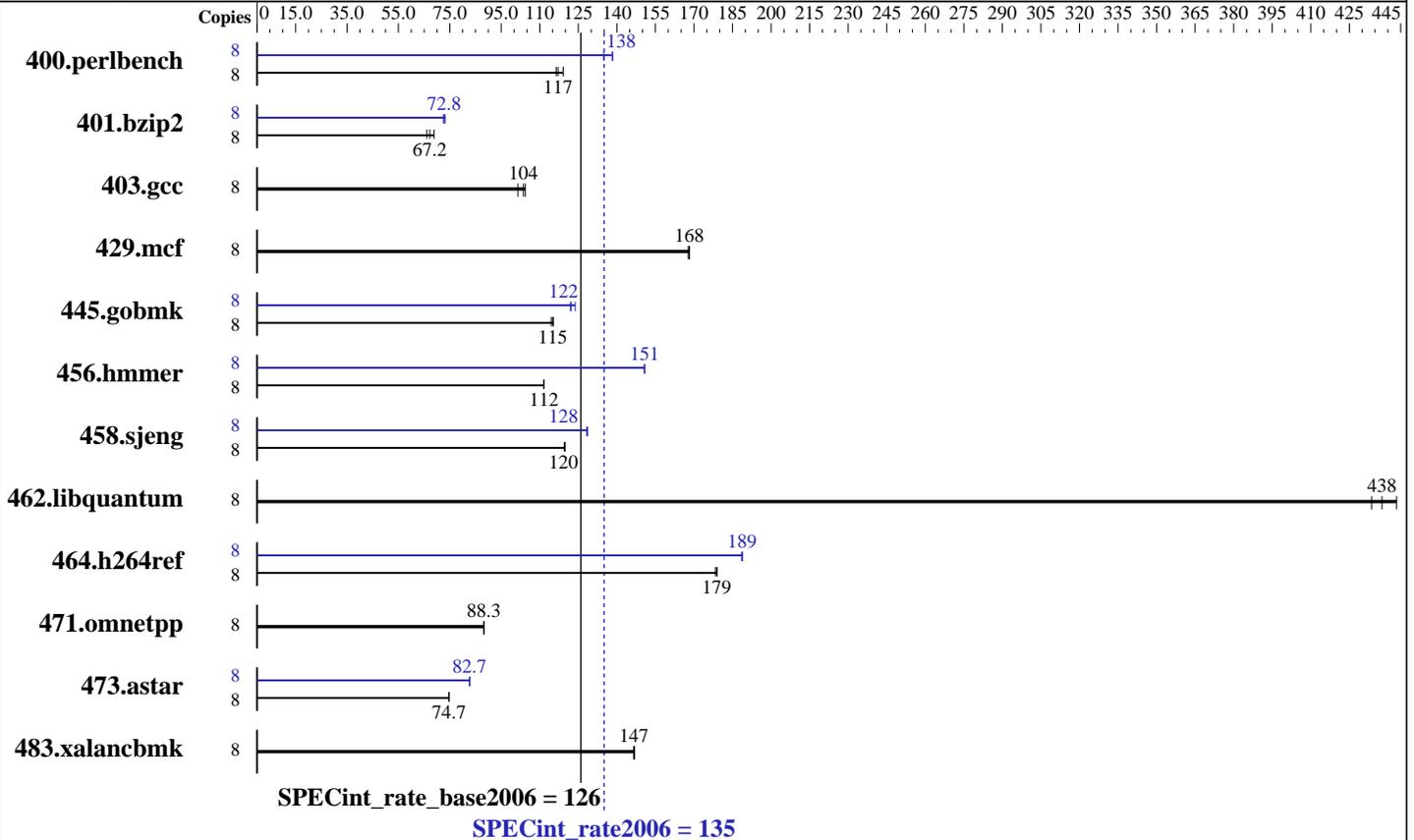
Itautec

Servidor Itautec MX223 (Intel Xeon E5504)

SPECint_rate2006 = 135

SPECint_rate_base2006 = 126

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**668**	**117**	672	116	656	119	8	579	135	565	138	**565**	**138**
401.bzip2	8	**1148**	**67.2**	1121	68.8	1168	66.1	8	**1060**	**72.8**	1054	73.2	1064	72.5
403.gcc	8	617	104	635	101	**621**	**104**	8	617	104	635	101	**621**	**104**
429.mcf	8	**434**	**168**	435	168	434	168	8	**434**	**168**	435	168	434	168
445.gobmk	8	728	115	734	114	**729**	**115**	8	678	124	688	122	**686**	**122**
456.hmmer	8	669	111	669	112	**669**	**112**	8	496	151	495	151	**495**	**151**
458.sjeng	8	809	120	808	120	**809**	**120**	8	753	129	754	128	**754**	**128**
462.libquantum	8	374	443	382	434	**379**	**438**	8	374	443	382	434	**379**	**438**
464.h264ref	8	993	178	989	179	**990**	**179**	8	939	189	937	189	**938**	**189**
471.omnetpp	8	567	88.2	**566**	**88.3**	566	88.4	8	567	88.2	**566**	**88.3**	566	88.4
473.astar	8	753	74.6	752	74.7	**752**	**74.7**	8	**679**	**82.7**	680	82.6	678	82.8
483.xalancbmk	8	376	147	377	147	**376**	**147**	8	376	147	377	147	**376**	**147**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
    numactl was used to bind copies to the cores

General Notes

    This result was measured on the Servidor Itautec MX203.
    The Servidor Itautec MX223 and the Servidor Itautec MX203 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
      icc

C++ benchmarks:
      icpc

Base Portability Flags

       400.perlbench: -DSPEC_CPU_LINUX_IA32
      462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

Servidor Itautec MX223 (Intel Xeon E5504)

SPECint_rate2006 = 135

SPECint_rate_base2006 = 126

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/081/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/081/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/081/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/081/bin/intel64/icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

SPEC CINT2006 Result

Itautec

Servidor Itautec MX223 (Intel Xeon E5504)

SPECint_rate2006 = 135

SPECint_rate_base2006 = 126

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -ansi-alias -opt-prefetch`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
`-ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
`-ansi-alias -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32`
`-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap64`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

Servidor Itautec MX223 (Intel Xeon E5504)

$\text{SPECint\_rate2006} = \quad 135$

$\text{SPECint\_rate\_base2006} = \quad 126$

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 02:35:32 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 July 2009.