SPEC® CINT2006 Result

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GIGA-BYTE Technology Co. Ltd.

(Test Sponsor: Intel Corporation)

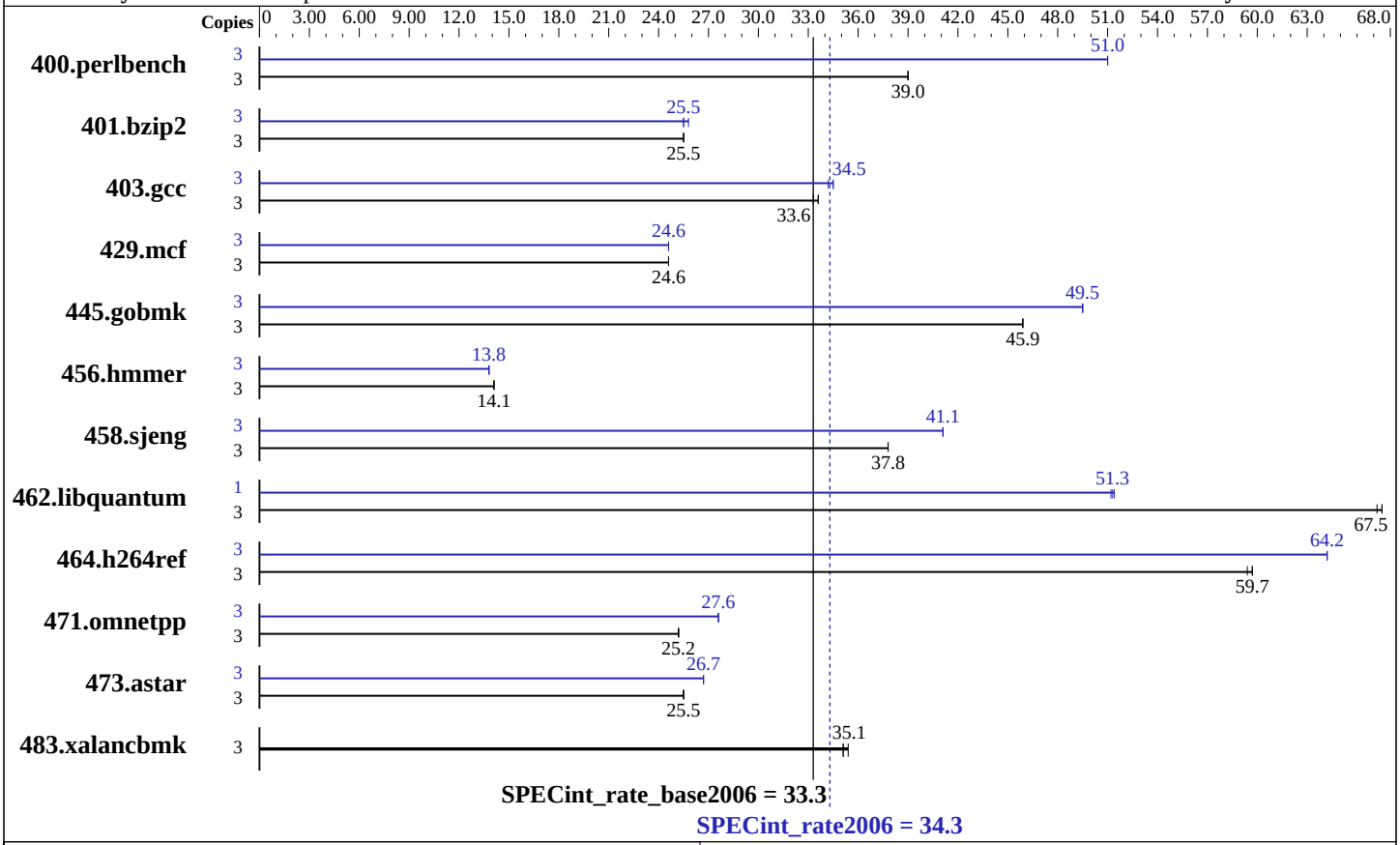
Gigabyte MA78GM-S2H Motherboard (AMD Phenom X3 8750)

SPECint®_rate2006 = 34.3

SPECint_rate_base2006 = 33.3

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jan-2009
Hardware Availability: Apr-2008
Software Availability: Nov-2008



Hardware

CPU Name:	AMD Phenom X3 8750
CPU Characteristics:	
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	3 cores, 1 chip, 3 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	2 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem:	Seagate 320 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	3	750	39.0	751	39.0	**750**	**39.0**	3	573	51.0	575	51.0	**575**	**51.0**
401.bzip2	3	1133	25.5	**1131**	**25.5**	1130	25.5	3	1129	25.8	**1130**	**25.5**	1139	25.5
403.gcc	3	724	33.3	**719**	**33.6**	718	33.6	3	703	34.2	**701**	**34.5**	699	34.5
429.mcf	3	**1116**	**24.6**	1117	24.6	1115	24.6	3	1114	24.6	1116	24.6	**1115**	**24.6**
445.gobmk	3	684	45.9	**684**	**45.9**	684	45.9	3	635	49.5	636	49.5	**635**	**49.5**
456.hmmer	3	1982	14.1	**1971**	**14.1**	1969	14.1	3	2008	13.8	**2011**	**13.8**	2015	13.8
458.sjeng	3	961	37.8	**961**	**37.8**	961	37.8	3	**883**	**41.1**	883	41.1	883	41.1
462.libquantum	3	**923**	**67.5**	923	67.5	925	67.2	1	405	51.2	404	51.4	**404**	**51.3**
464.h264ref	3	**1115**	**59.7**	1115	59.4	1115	59.7	3	**1035**	**64.2**	1036	64.2	1035	64.2
471.omnetpp	3	742	25.2	**742**	**25.2**	742	25.2	3	678	27.6	679	27.6	**679**	**27.6**
473.astar	3	824	25.5	825	25.5	**824**	**25.5**	3	789	26.7	**789**	**26.7**	789	26.7
483.xalancbmk	3	**589**	**35.1**	592	35.1	585	35.4	3	**589**	**35.1**	592	35.1	585	35.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
submit disabled for 462.libquantum peak
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
`/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

C++ benchmarks:
`/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc9 -Qc99`

C++ benchmarks:
`icl -Qvc9`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `/arch:SSE2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias /F512000000

403.gcc: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

429.mcf: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: /arch:SSE2 -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: /arch:SSE2 -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml`

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For other inquiries, please contact webmaster@spec.org.

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