SPEC® CINT2006 Result

Bull SAS

bullx B500
(Intel Xeon X5570, 2.93 GHz)

SPECint®_rate2006 =　251

SPECint_rate_base2006 =　234

CPU2006 license: 20

Test sponsor: Bull SAS
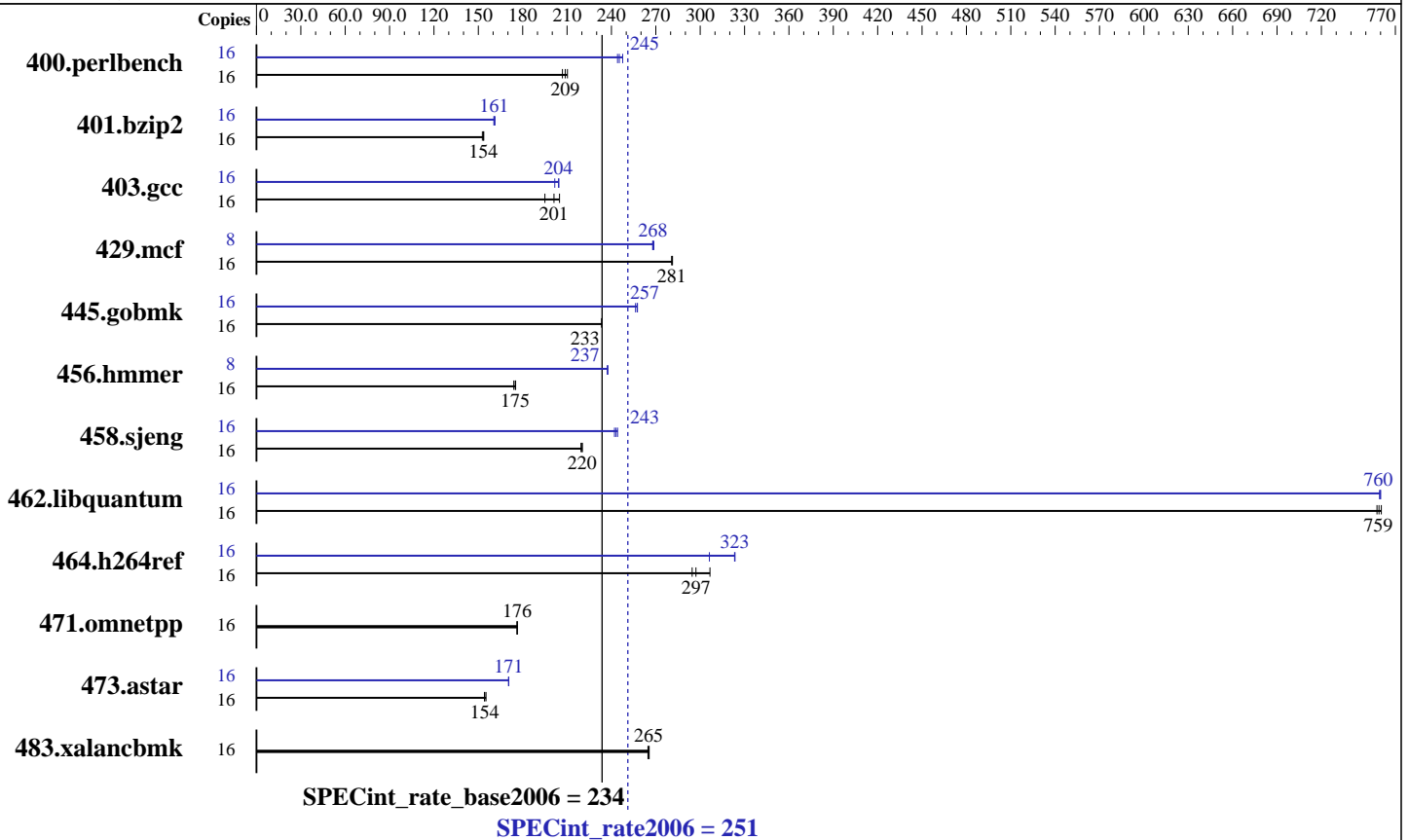
Tested by: Bull SAS

Test date: Jun-2009

Hardware Availability: Aug-2009

Software Availability: Feb-2009

Copies

Benchmark	Copies	Value
400.perlbench	16	245
400.perlbench	16	209
401.bzip2	16	161
401.bzip2	16	154
403.gcc	16	204
403.gcc	16	201
429.mcf	8	268
429.mcf	16	281
445.gobmk	16	257
445.gobmk	16	233
456.hmmer	8	237
456.hmmer	16	175
458.sjeng	16	243
458.sjeng	16	220
462.libquantum	16	760
462.libquantum	16	759
464.h264ref	16	323
464.h264ref	16	297
471.omnetpp	16	176
473.astar	16	171
473.astar	16	154
483.xalancbmk	16	265

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Hardware

CPU Name: Intel Xeon X5570

CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz

CPU MHz: 2933

FPU: Integrated

CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 8 MB I+D on chip per chip

Other Cache: None

Memory: 24 GB (6 x 4 GB PC3-10600R, CL9, ECC)

Disk Subsystem: 1x32 GB SATAII, SSD

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp

Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080

Auto Parallel: No

File System: ReiserFS

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	756	207	744	210	**749**	**209**	16	631	248	**637**	**245**	640	244
401.bzip2	16	1011	153	1005	154	**1005**	**154**	16	**960**	**161**	962	161	957	161
403.gcc	16	629	205	**640**	**201**	660	195	16	630	204	639	202	**631**	**204**
429.mcf	16	**520**	**281**	519	281	520	281	8	272	269	272	268	**272**	**268**
445.gobmk	16	**719**	**233**	718	234	719	233	16	652	258	**652**	**257**	655	256
456.hmmer	16	**853**	**175**	858	174	852	175	8	314	237	**314**	**237**	315	237
458.sjeng	16	882	219	**879**	**220**	879	220	16	793	244	800	242	**796**	**243**
462.libquantum	16	**437**	**759**	438	758	436	760	16	436	760	**436**	**760**	437	759
464.h264ref	16	1155	307	1202	295	**1192**	**297**	16	1094	324	**1095**	**323**	1156	306
471.omnetpp	16	**567**	**176**	567	176	568	176	16	**567**	**176**	567	176	568	176
473.astar	16	724	155	729	154	**728**	**154**	16	**659**	**171**	658	171	659	170
483.xalancbmk	16	416	266	**416**	**265**	417	265	16	416	266	**416**	**265**	417	265

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
     BIOS setting:
     NUMA configuration : Enabled
```

Base Compiler Invocation

```
C benchmarks:
     icc
```

```
C++ benchmarks:
     icpc
```

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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
    -opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

    401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

    456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

    458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):
    icpc

    473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

        458.sjeng: `-DSPEC_CPU_LP64`
    462.libquantum: `-DSPEC_CPU_LINUX`
         473.astar: `-DSPEC_CPU_LP64`
    483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
                `-prof-use`(pass 2) `-ansi-alias` `-opt-prefetch`

        401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
                `-prof-use`(pass 2) `-opt-prefetch` `-ansi-alias` `-auto-ilp32`

          403.gcc: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-inline-calloc`
                `-opt-malloc-options=3`

          429.mcf: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
                `-prof-use`(pass 2) `-opt-prefetch`

        445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2) `-O2`
                `-ipo` `-no-prec-div` `-ansi-alias`

        456.hmmer: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-unroll2`
                `-ansi-alias` `-auto-ilp32`

        458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
                `-prof-use`(pass 2) `-unroll4` `-auto-ilp32`

    462.libquantum: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static`
                `-opt-malloc-options=3` `-opt-prefetch`

      464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
                `-prof-use`(pass 2) `-unroll2` `-ansi-alias`

C++ benchmarks:

      471.omnetpp: basepeak = yes

         473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
                `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
                `-ansi-alias` `-opt-ra-region-strategy=routine` `-auto-ilp32`
                `-Wl,-z,muldefs` `-L/spec/cpu2006.1.1/lib` `-lsmartheap64`

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.01.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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