SPEC® CINT2006 Result

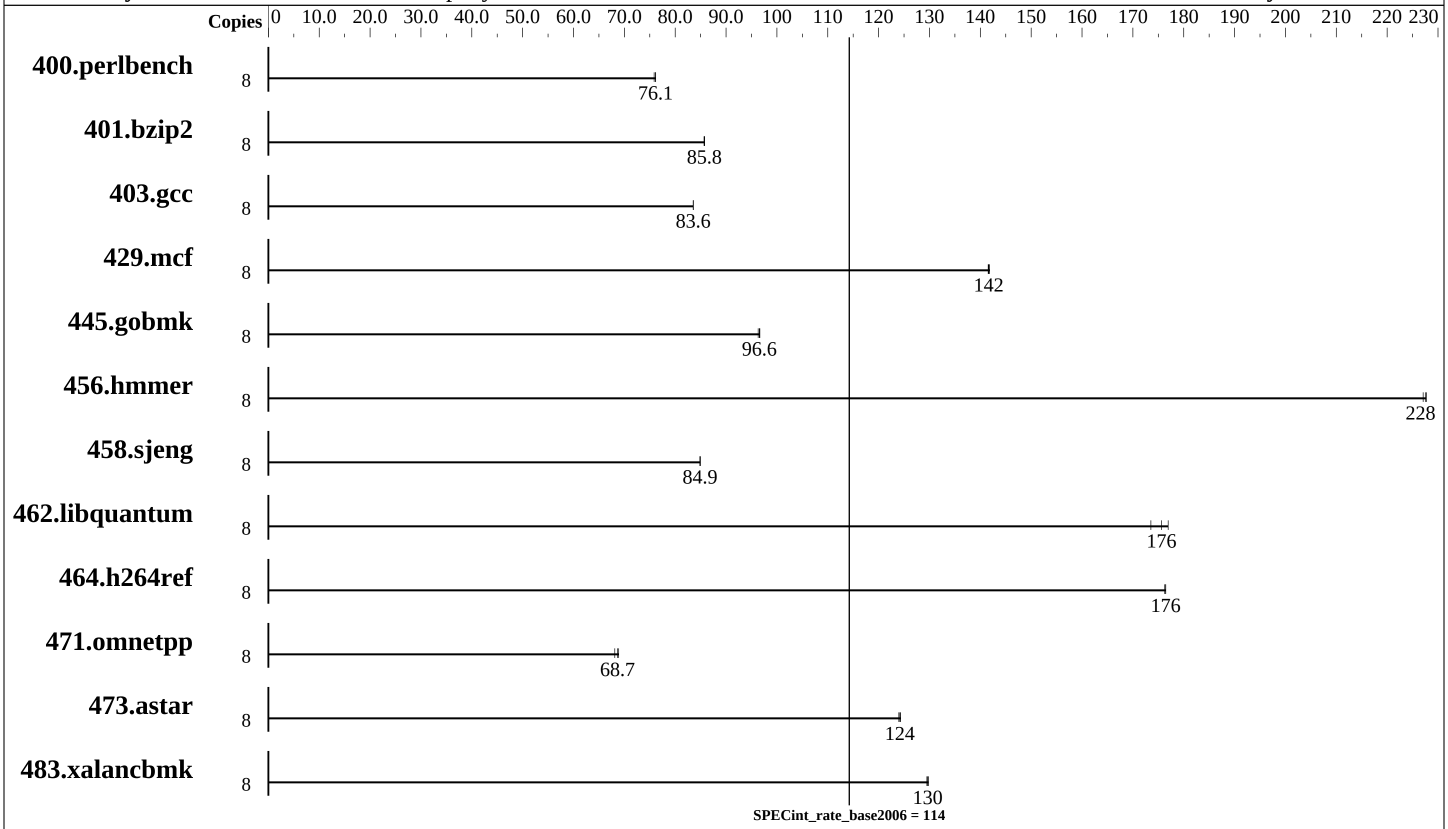
Hewlett-Packard Company

HP Integrity BL870c (1.6GHz/24MB Dual-Core Intel Itanium 2)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 114

CPU2006 license:	03	**Test date:**	May-2009
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Nov-2007
Tested by:	Hewlett-Packard Company	**Software Availability:**	Mar-2009



Hardware

CPU Name:	Dual-Core Intel Itanium 9150N
CPU Characteristics:	1.6GHz/24MB, 533MHz FSB
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip
CPU(s) orderable:	1-4 chips
Primary Cache:	16 KB I + 16 KB D on chip per core
Secondary Cache:	1 MB I + 256 KB D on chip per core
L3 Cache:	12 MB I+D on chip per core
Other Cache:	None
Memory:	96 GB (24x4GB PC-4200 DIMMs)
Disk Subsystem:	2x73 GB 15K RPM SAS
Other Hardware:	None

Software

Operating System:	HPUX11i-DC-OE B.11.31.0903
Compiler:	HP C/aC++ Developer's Bundle C.11.31.04.2
Auto Parallel:	No
File System:	vxfs
System State:	Multi-user
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MallocNextGen B.11.31.0903.02

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	1026	76.2	**1027**	**76.1**	1030	75.9							
401.bzip2	8	900	85.8	**900**	**85.8**	901	85.7							
403.gcc	8	771	83.5	771	83.6	**771**	**83.6**							
429.mcf	8	515	142	**515**	**142**	514	142							
445.gobmk	8	**869**	**96.6**	868	96.6	871	96.3							
456.hmmer	8	329	227	**328**	**228**	328	228							
458.sjeng	8	**1140**	**84.9**	1141	84.8	1139	85.0							
462.libquantum	8	**944**	**176**	955	174	937	177							
464.h264ref	8	**1003**	**176**	1004	176	1003	176							
471.omnetpp	8	**728**	**68.7**	726	68.9	734	68.1							
473.astar	8	**452**	**124**	453	124	452	124							
483.xalancbmk	8	**426**	**130**	425	130	426	129							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
The system had the March 2009 HP-UX 11i v3 Data Center
Operating Environment and March 2009 compilers installed.

The following kernel tunables were set, in addition to the
defaults set by the Base Operating Environment:

filecache_max=25%
filecache_min=25%
maxdsiz=3221225472
fcache_fb_policy=1
base_pagesize=64
pagezero_daemon_enabled=0
vxfs_ifree_timelag=-1
```

Platform Notes

```
Hardware Threading was disabled
The following config file entry was used to bind
processes to cores using the HP-UX "mpsched" utility:
submit = let "MYCPU=\$SPECCOPYNUM*2" ;mpsched -c \$MYCPU $command
```

Base Compiler Invocation

C benchmarks:
`/opt/ansic/bin/cc -AC99`

C++ benchmarks:
`/opt/aCC/bin/aCC -Aa`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_HPUX_IA64`
403.gcc: `-DSPEC_CPU_HPUX`
462.libquantum: `-DSPEC_CPU_HPUX`
483.xalancbmk: `-DSPEC_CPU_HPUX_IA64`

Base Optimization Flags

C benchmarks:
`+Ofaster +Otype_safety=ansi -Wl,-aarchive_shared -Wl,+pd,64M -Wl,+pi,64K -Wl,-N`

C++ benchmarks:
`+Ofaster +Otype_safety=ansi -Wl,-aarchive_shared -Wl,+pd,64M -Wl,+pi,64K -Wl,-N -lmallocng`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itanium-HPUX-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itanium-HPUX-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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