



# SPEC<sup>®</sup> CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECint<sup>®</sup>\_rate2006 = 141

PRIMERGY BX920 S1, Intel Xeon E5506, 2.13 GHz

SPECint\_rate\_base2006 = 132

CPU2006 license: 19

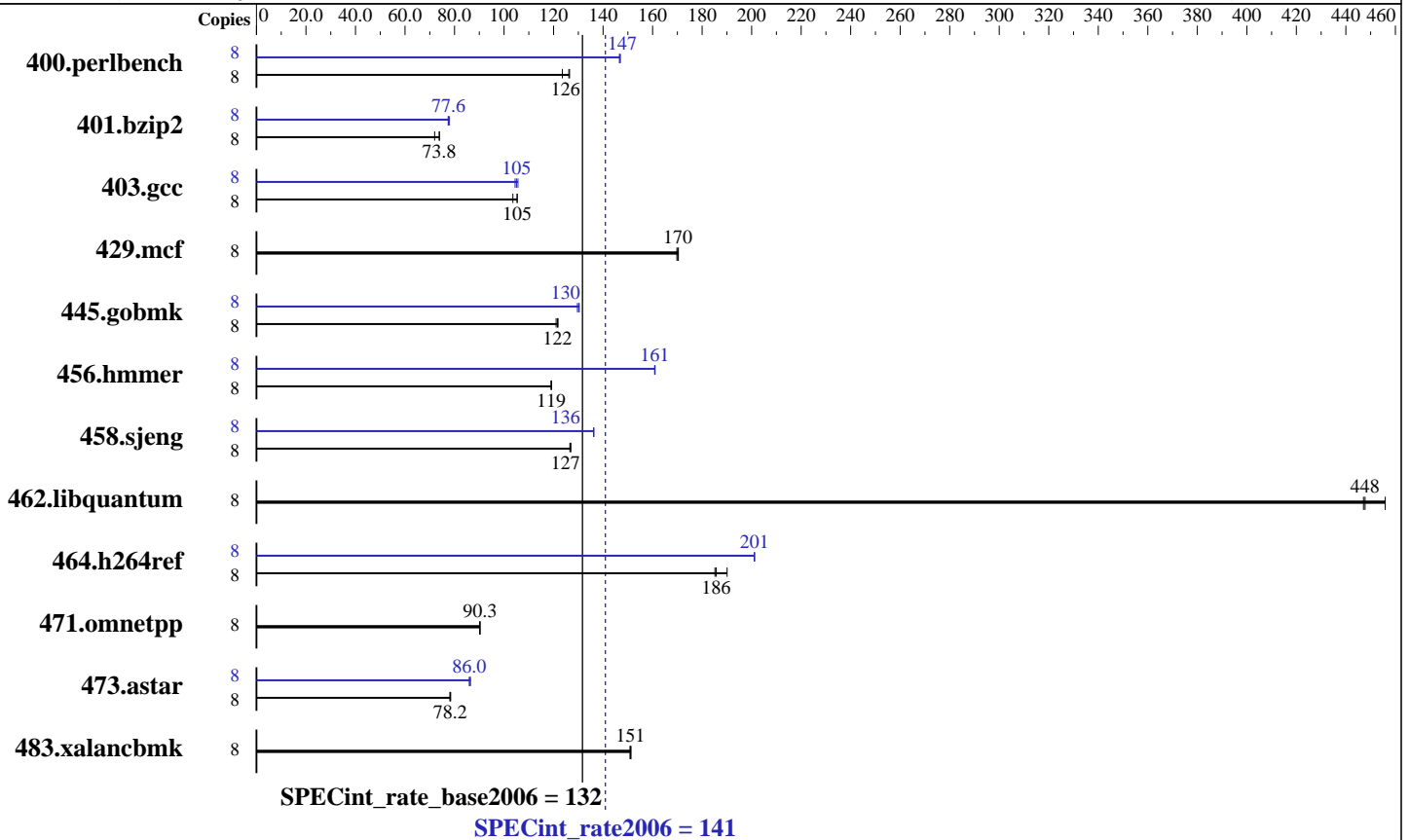
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2009

Hardware Availability: May-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5506  
 CPU Characteristics:  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 36 GB (9x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SAS, 73 GB, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECint\_rate2006 = 141

PRIMERGY BX920 S1, Intel Xeon E5506, 2.13 GHz

SPECint\_rate\_base2006 = 132

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2009

Hardware Availability: May-2009

Software Availability: Feb-2009

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	633	124	<b>620</b>	<b>126</b>	618	126	8	532	147	533	147	<b>532</b>	<b>147</b>
401.bzip2	8	<b>1046</b>	<b>73.8</b>	1045	73.9	1073	72.0	8	997	77.4	990	78.0	<b>995</b>	<b>77.6</b>
403.gcc	8	612	105	<b>612</b>	<b>105</b>	622	104	8	617	104	610	106	<b>613</b>	<b>105</b>
429.mcf	8	<b>429</b>	<b>170</b>	430	170	428	170	8	<b>429</b>	<b>170</b>	430	170	428	170
445.gobmk	8	693	121	<b>690</b>	<b>122</b>	689	122	8	<b>645</b>	<b>130</b>	644	130	647	130
456.hammer	8	<b>627</b>	<b>119</b>	626	119	627	119	8	464	161	<b>464</b>	<b>161</b>	464	161
458.sjeng	8	<b>763</b>	<b>127</b>	763	127	765	127	8	711	136	<b>711</b>	<b>136</b>	711	136
462.libquantum	8	371	447	<b>370</b>	<b>448</b>	364	456	8	371	447	<b>370</b>	<b>448</b>	364	456
464.h264ref	8	931	190	956	185	<b>953</b>	<b>186</b>	8	880	201	<b>880</b>	<b>201</b>	880	201
471.omnetpp	8	<b>554</b>	<b>90.3</b>	554	90.2	554	90.3	8	<b>554</b>	<b>90.3</b>	554	90.2	554	90.3
473.astar	8	718	78.2	<b>718</b>	<b>78.2</b>	716	78.4	8	653	86.0	<b>653</b>	<b>86.0</b>	650	86.4
483.xalancbmk	8	365	151	366	151	<b>365</b>	<b>151</b>	8	365	151	366	151	<b>365</b>	<b>151</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 800 MHz.

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint\_rate2006 = 141

PRIMERGY BX920 S1, Intel Xeon E5506, 2.13 GHz

SPECint\_rate\_base2006 = 132

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu

Test date: Feb-2009  
Hardware Availability: May-2009  
Software Availability: Feb-2009

## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint\_rate2006 = 141

PRIMERGY BX920 S1, Intel Xeon E5506, 2.13 GHz

SPECint\_rate\_base2006 = 132

CPU2006 license: 19

Test date: Feb-2009

Test sponsor: Fujitsu

Hardware Availability: May-2009

Tested by: Fujitsu

Software Availability: Feb-2009

## Peak Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3  
429.mcf: basepeak = yes  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: basepeak = yes  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64  
483.xalancbmk: basepeak = yes



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint\_rate2006 = 141

PRIMERGY BX920 S1, Intel Xeon E5506, 2.13 GHz

SPECint\_rate\_base2006 = 132

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2009

Hardware Availability: May-2009

Software Availability: Feb-2009

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Tue Jul 22 23:50:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 12 May 2009.