



SPEC® CFP2006 Result

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NEC Corporation

Express5800/R120a-2
(Intel Xeon X5570)

SPECfp®2006 = 39.2

SPECfp_base2006 = 36.8

CPU2006 license: 9006

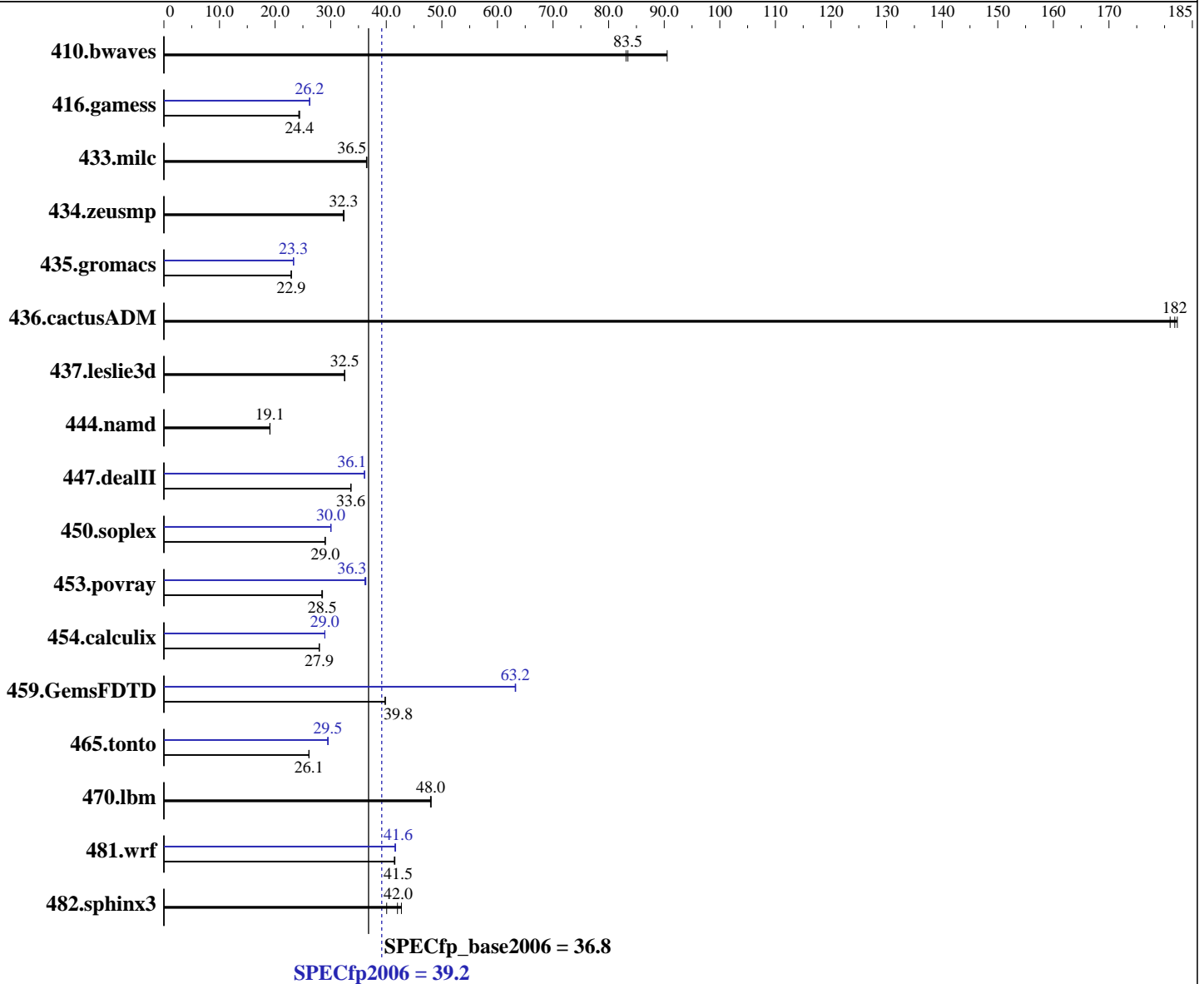
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Apr-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081, l_cprof_p_11.0.081
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 X 4 GB PC3-8500R, 2 rank, CL7, ECC)
Disk Subsystem: 1x146.5 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	150	90.5	<u>163</u>	<u>83.5</u>	163	83.1	150	90.5	<u>163</u>	<u>83.5</u>	163	83.1
416.gamess	803	24.4	<u>803</u>	<u>24.4</u>	807	24.3	748	26.2	746	26.2	<u>747</u>	<u>26.2</u>
433.milc	251	36.5	252	36.4	<u>252</u>	<u>36.5</u>	251	36.5	252	36.4	<u>252</u>	<u>36.5</u>
434.zeusmp	281	32.4	<u>282</u>	<u>32.3</u>	282	32.3	281	32.4	<u>282</u>	<u>32.3</u>	282	32.3
435.gromacs	311	22.9	<u>312</u>	<u>22.9</u>	312	22.9	<u>306</u>	<u>23.3</u>	307	23.3	306	23.3
436.cactusADM	<u>65.7</u>	<u>182</u>	66.0	181	65.6	182	<u>65.7</u>	<u>182</u>	66.0	181	65.6	182
437.leslie3d	289	32.6	289	32.5	<u>289</u>	<u>32.5</u>	289	32.6	289	32.5	<u>289</u>	<u>32.5</u>
444.namd	420	19.1	<u>420</u>	<u>19.1</u>	421	19.1	420	19.1	<u>420</u>	<u>19.1</u>	421	19.1
447.dealII	340	33.7	340	33.6	<u>340</u>	<u>33.6</u>	317	36.1	<u>317</u>	<u>36.1</u>	317	36.1
450.soplex	288	29.0	<u>287</u>	<u>29.0</u>	287	29.0	277	30.1	<u>278</u>	<u>30.0</u>	278	30.0
453.povray	186	28.5	187	28.4	<u>187</u>	<u>28.5</u>	147	36.2	146	36.3	<u>147</u>	<u>36.3</u>
454.calculix	<u>295</u>	<u>27.9</u>	295	28.0	295	27.9	285	29.0	<u>285</u>	<u>29.0</u>	285	28.9
459.GemsFDTD	<u>267</u>	<u>39.8</u>	267	39.8	266	39.8	168	63.2	168	63.3	<u>168</u>	<u>63.2</u>
465.tonto	377	26.1	377	26.1	<u>377</u>	<u>26.1</u>	333	29.6	334	29.5	<u>334</u>	<u>29.5</u>
470.lbm	286	48.1	<u>286</u>	<u>48.0</u>	287	48.0	286	48.1	<u>286</u>	<u>48.0</u>	287	48.0
481.wrf	<u>269</u>	<u>41.5</u>	269	41.4	269	41.6	268	41.7	269	41.6	<u>268</u>	<u>41.6</u>
482.sphinx3	<u>464</u>	<u>42.0</u>	486	40.1	456	42.7	<u>464</u>	<u>42.0</u>	486	40.1	456	42.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

Platform Notes

BIOS setting:
NUMA configuration : Enabled



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General Notes

The NEC Express5800/R120a-1(Intel Xeon X5570),
the NEC Express5800/R120a-2(Intel Xeon X5570),
the Bull NovaScale R440 E2 (Intel Xeon X5570, 2.93 GHz) and
the Bull NovaScale R460 E2 (Intel Xeon X5570, 2.93 GHz) models are electronically equivalent.
The results have been measured on a NEC Express5800/R120a-1(Intel Xeon X5570) model.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revG.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revG.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revD.xml>

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