SPEC® CINT2006 Result

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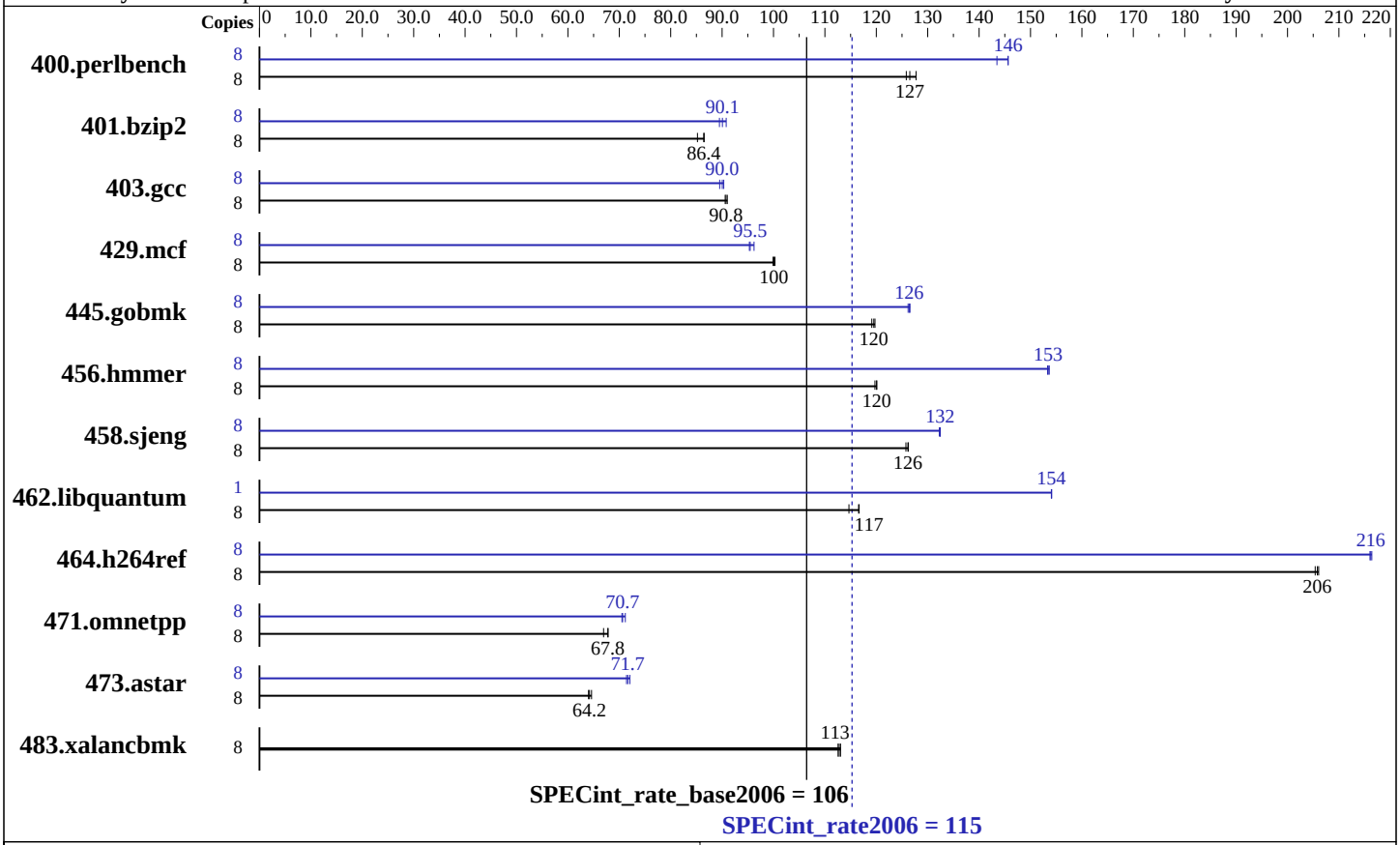
Supermicro
Motherboard X7DVL-I

SPECint®_rate2006 = 115

SPECint_rate_base2006 = 106

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2009
Hardware Availability: Nov-2007
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	Quad Core, 2.33 GHz, 1333 MHz System Bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	12 GB (6 x 2 GB DDR2-667 FBD, CL5)
Disk Subsystem:	1 x 750 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.37_f594963d-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	612	128	**618**	**127**	621	126	8	545	143	537	146	**537**	**146**
401.bzip2	8	906	85.2	**893**	**86.4**	892	86.5	8	863	89.5	**857**	**90.1**	850	90.8
403.gcc	8	**709**	**90.8**	708	91.0	711	90.6	8	**715**	**90.0**	713	90.3	719	89.5
429.mcf	8	730	99.9	727	100	**729**	**100**	8	758	96.2	**764**	**95.5**	766	95.3
445.gobmk	8	**702**	**120**	704	119	701	120	8	663	127	**664**	**126**	665	126
456.hmmer	8	623	120	622	120	**622**	**120**	8	487	153	**486**	**153**	486	154
458.sjeng	8	767	126	**767**	**126**	770	126	8	731	132	**731**	**132**	732	132
462.libquantum	8	1445	115	**1422**	**117**	1421	117	1	135	154	134	154	**134**	**154**
464.h264ref	8	862	205	859	206	**860**	**206**	8	819	216	818	216	**819**	**216**
471.omnetpp	8	747	66.9	737	67.8	**738**	**67.8**	8	703	71.2	**708**	**70.7**	709	70.5
473.astar	8	877	64.0	869	64.6	**875**	**64.2**	8	**784**	**71.7**	786	71.4	779	72.1
483.xalancbmk	8	490	113	**489**	**113**	488	113	8	490	113	**489**	**113**	488	113

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores except
 for 462.libquantum peak
```

Operating System Notes

```
'ulimit -S unlimited' was used to set the stacksize to unlimited prior to run
```

General Notes

```
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
Tested system is built in open environment,
to ensure system stability, a 500W (minimum) ATX power supply
[8-pin (+12V) and 24-pin are required]
Product description can be obtained at
http://www.supermicro.com/products/motherboard/Xeon1333/5000V/X7DVL-i.cfm
```

Base Compiler Invocation

C benchmarks:

```
icc
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

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Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.00.html`
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.00.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml`

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