SPEC® CINT2006 Result

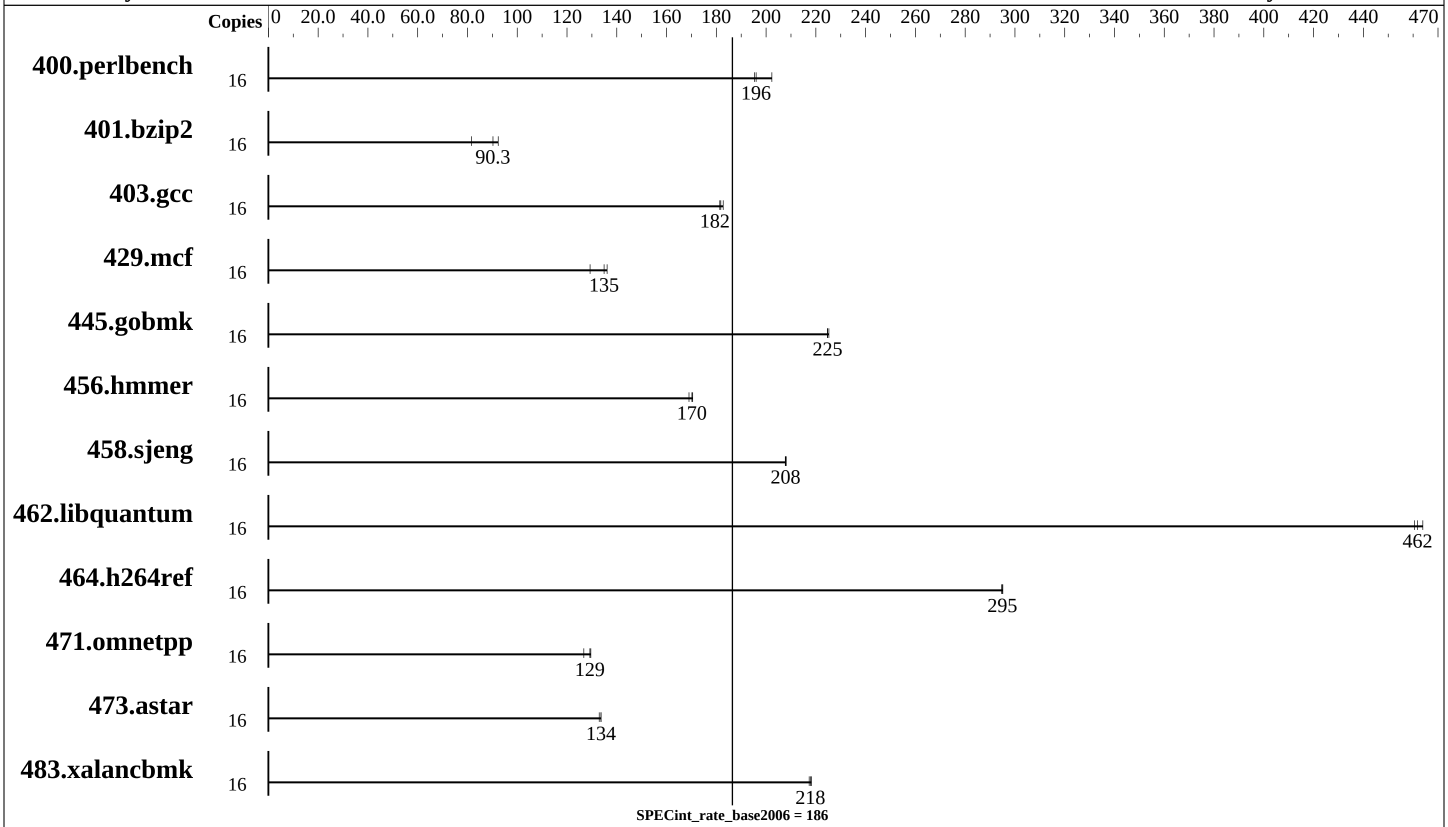
Itautec

Servidor Itautec LX213 (Intel Xeon X5560)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 186

CPU2006 license:	9001	**Test date:**	Apr-2009
Test sponsor:	Itautec	**Hardware Availability:**	Mar-2009
Tested by:	Itautec	**Software Availability:**	Feb-2009



Hardware

CPU Name:	Intel Xeon X5560
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz, 6.4 GT/s QPI
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	12 GB (6 x 2GB DDR3-1066, CL 9, ECC)
Disk Subsystem:	1 x 160 GB SATA-2, 7200RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.34-smp for x86_64
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	773	202	800	195	**798**	**196**							
401.bzip2	16	1672	92.4	1892	81.6	**1711**	**90.3**							
403.gcc	16	**708**	**182**	705	183	710	181							
429.mcf	16	1129	129	1072	136	**1082**	**135**							
445.gobmk	16	745	225	747	225	**747**	**225**							
456.hmmer	16	875	171	883	169	**877**	**170**							
458.sjeng	16	932	208	**931**	**208**	930	208							
462.libquantum	16	720	461	715	464	**718**	**462**							
464.h264ref	16	1200	295	**1200**	**295**	1202	295							
471.omnetpp	16	789	127	**774**	**129**	772	130							
473.astar	16	845	133	840	134	**840**	**134**							
483.xalancbmk	16	**507**	**218**	506	218	508	217							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

General Notes

```
This result was measured on the Servidor Itautec LX203.
The Servidor Itautec LX213 and the Servidor Itautec LX203 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

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Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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