SPEC® CINT2006 Result

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Fujitsu

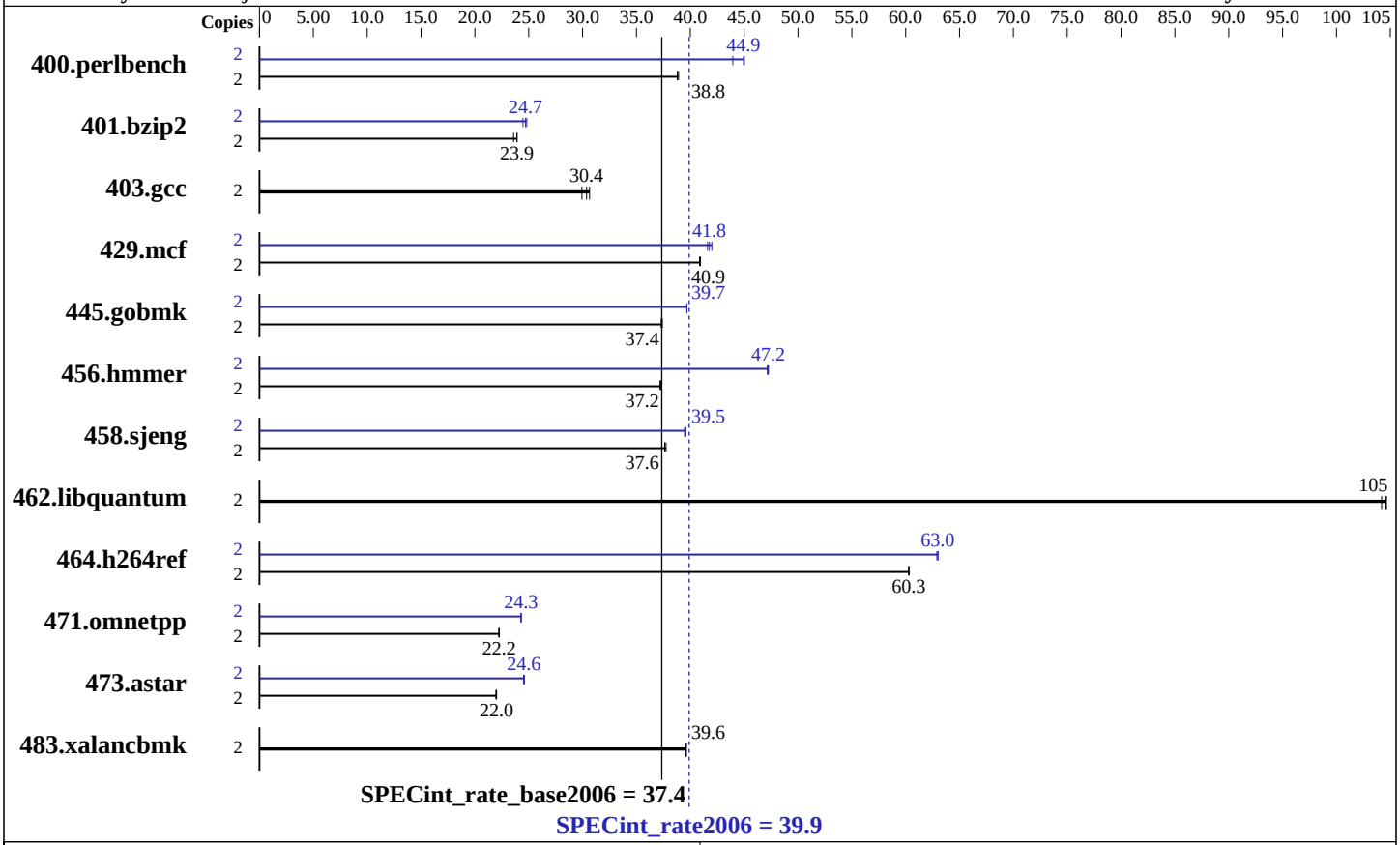
PRIMERGY TX150 S6, Intel Core 2 Duo E7400, 2.80 GHz

SPECint®_rate2006 = 39.9

SPECint_rate_base2006 = 37.4

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Mar-2009
Hardware Availability: Apr-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core 2 Duo E7400
CPU Characteristics:	1067 MHz system bus
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	3 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)
Disk Subsystem:	1x SATA, 250 GB, 7200 rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**504**	**38.8**	504	38.8	502	38.9	2	445	43.9	**435**	**44.9**	434	45.0
401.bzip2	2	818	23.6	**807**	**23.9**	807	23.9	2	778	24.8	**781**	**24.7**	789	24.5
403.gcc	2	525	30.6	**530**	**30.4**	538	29.9	2	525	30.6	**530**	**30.4**	538	29.9
429.mcf	2	**446**	**40.9**	446	40.9	446	40.9	2	438	41.6	434	42.0	**437**	**41.8**
445.gobmk	2	**561**	**37.4**	562	37.3	561	37.4	2	**529**	**39.7**	529	39.7	529	39.7
456.hmmer	2	501	37.2	**501**	**37.2**	501	37.2	2	395	47.3	**395**	**47.2**	396	47.2
458.sjeng	2	643	37.6	641	37.8	**643**	**37.6**	2	**612**	**39.5**	611	39.6	613	39.5
462.libquantum	2	398	104	396	105	**396**	**105**	2	398	104	396	105	**396**	**105**
464.h264ref	2	734	60.3	**734**	**60.3**	734	60.3	2	704	62.9	**703**	**63.0**	703	63.0
471.omnetpp	2	**562**	**22.2**	562	22.2	562	22.2	2	514	24.3	515	24.3	**515**	**24.3**
473.astar	2	640	21.9	638	22.0	**638**	**22.0**	2	573	24.5	571	24.6	**571**	**24.6**
483.xalancbmk	2	348	39.6	**348**	**39.6**	349	39.6	2	348	39.6	**348**	**39.6**	349	39.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset has been used to bind processes to cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
Adjacent Sector Prefetch = Disable
```

General Notes

```
For information about Fujitsu please visit:
http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:

```
icc
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml`

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For other inquiries, please contact webmaster@spec.org.

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