SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

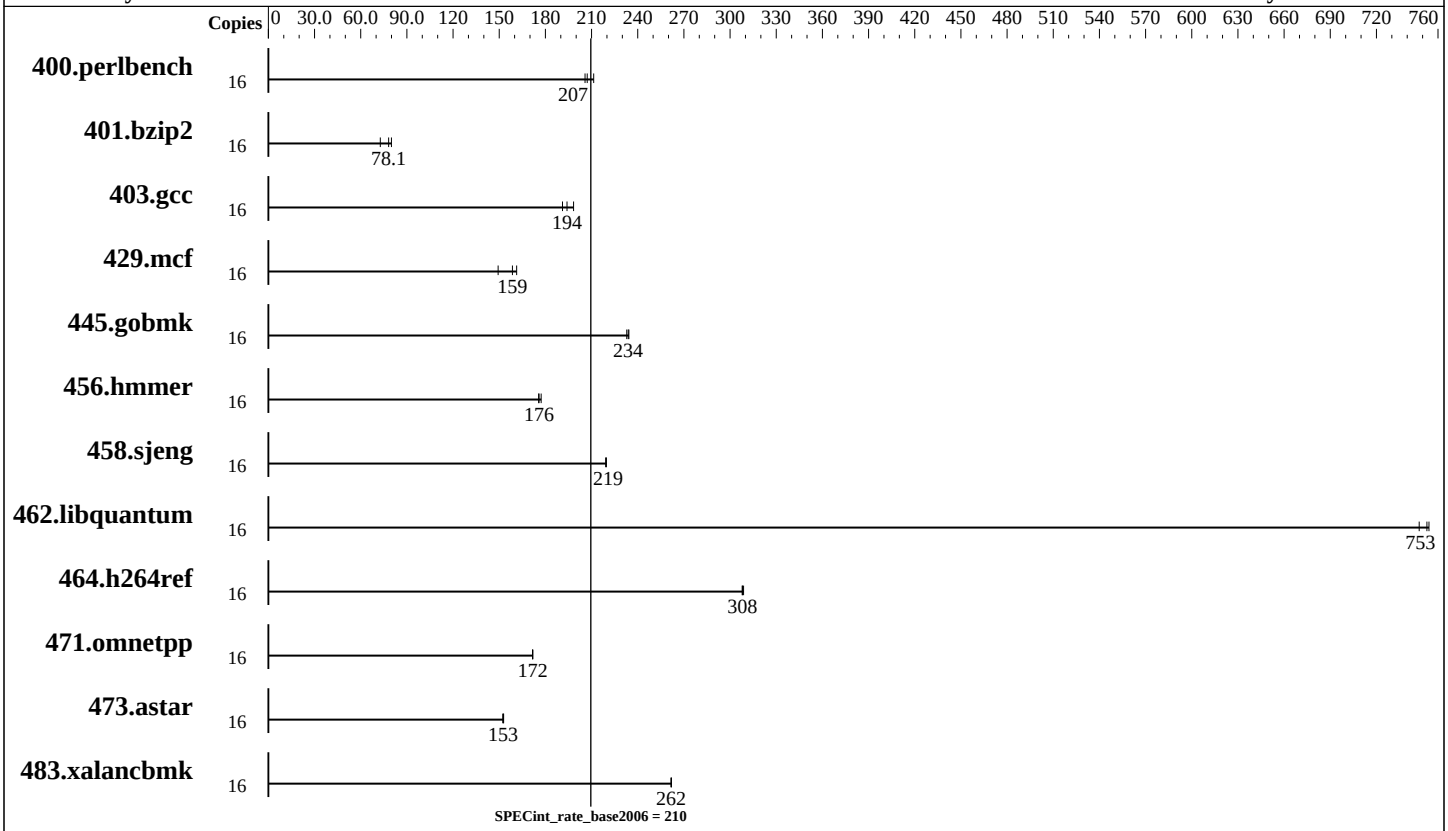
Servidor Itautec MX223 (Intel Xeon X5560)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 210

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Apr-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon X5560
CPU Characteristics:	Intel Turbo Boost Technology up to 3.2 GHz
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4GB DDR3-1333, CL 9, ECC)
Disk Subsystem:	1 x 160 GB SATA-2, 7200RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.34-smp for x86_64
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

Servidor Itautec MX223 (Intel Xeon X5560)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 210

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Apr-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	740	211	**755**	**207**	760	206							
401.bzip2	16	**1976**	**78.1**	2123	72.7	1931	80.0							
403.gcc	16	649	198	**664**	**194**	674	191							
429.mcf	16	904	161	**920**	**159**	977	149							
445.gobmk	16	**718**	**234**	721	233	717	234							
456.hmmer	16	850	176	842	177	**849**	**176**							
458.sjeng	16	**882**	**219**	883	219	882	220							
462.libquantum	16	443	748	**440**	**753**	440	754							
464.h264ref	16	**1149**	**308**	1150	308	1147	309							
471.omnetpp	16	582	172	583	172	**582**	**172**							
473.astar	16	738	152	**736**	**153**	735	153							
483.xalancbmk	16	**422**	**262**	422	262	422	262							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
numactl was used to bind copies to the cores
```

General Notes

```
This result was measured on the Servidor Itautec MX223.
The Servidor Itautec MX223 and the Servidor Itautec MX203 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 02:00:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 April 2009.