# SPEC® CINT2006 Result

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## Fujitsu

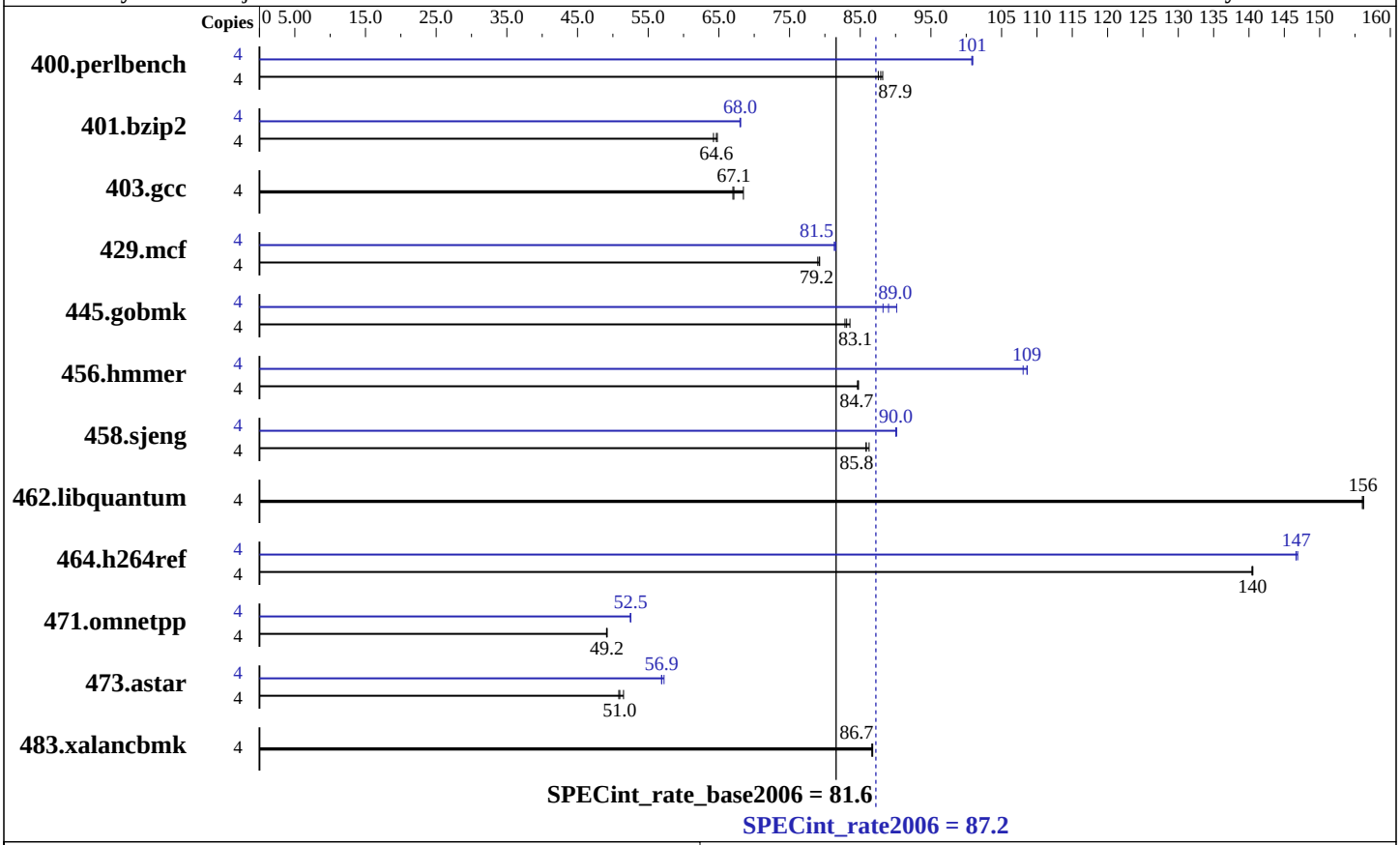
PRIMERGY TX150 S6, Intel Xeon X3380, 3.16 GHz

SPECint®_rate2006 = 87.2

SPECint_rate_base2006 = 81.6

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Mar-2009
**Hardware Availability:** Apr-2009
**Software Availability:** Nov-2008




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X3380 |
| CPU Characteristics: | 1333 MHz system bus |
| CPU MHz: | 3167 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 12 MB I+D on chip per chip, 6 MB shared / 2 cores |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC) |
| Disk Subsystem: | 1 x SATA, 250 GB, 7200 rpm |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp |
| Compiler: | Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Multi-User Run Level 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | **445** | **87.9** | 446 | 87.6 | 443 | 88.2 | 4 | **388** | **101** | 387 | 101 | 388 | 101 |
| 401.bzip2 | 4 | 596 | 64.8 | 601 | 64.2 | **597** | **64.6** | 4 | 567 | 68.1 | 568 | 68.0 | **567** | **68.0** |
| 403.gcc | 4 | 470 | 68.5 | **480** | **67.1** | 481 | 67.0 | 4 | 470 | 68.5 | **480** | **67.1** | 481 | 67.0 |
| 429.mcf | 4 | 460 | 79.3 | **461** | **79.2** | 462 | 79.0 | 4 | **448** | **81.5** | 449 | 81.3 | 447 | 81.5 |
| 445.gobmk | 4 | 502 | 83.5 | 507 | 82.8 | **505** | **83.1** | 4 | 465 | 90.2 | **471** | **89.0** | 476 | 88.2 |
| 456.hmmer | 4 | 440 | 84.8 | 441 | 84.6 | **441** | **84.7** | 4 | 345 | 108 | **344** | **109** | 344 | 109 |
| 458.sjeng | 4 | **564** | **85.8** | 561 | 86.3 | 564 | 85.8 | 4 | 537 | 90.1 | 538 | 90.0 | **538** | **90.0** |
| 462.libquantum | 4 | **531** | **156** | 531 | 156 | 531 | 156 | 4 | **531** | **156** | 531 | 156 | 531 | 156 |
| 464.h264ref | 4 | 630 | 141 | **630** | **140** | 630 | 140 | 4 | **603** | **147** | 603 | 147 | 604 | 147 |
| 471.omnetpp | 4 | 509 | 49.1 | 508 | 49.2 | **509** | **49.2** | 4 | 476 | 52.5 | **476** | **52.5** | 476 | 52.5 |
| 473.astar | 4 | 552 | 50.8 | **551** | **51.0** | 545 | 51.5 | 4 | 491 | 57.2 | 494 | 56.9 | **493** | **56.9** |
| 483.xalancbmk | 4 | 318 | 86.8 | **318** | **86.7** | 318 | 86.7 | 4 | 318 | 86.8 | **318** | **86.7** | 318 | 86.7 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 taskset has been used to bind processes to cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

## Platform Notes

```
BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable
```

## General Notes

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For information about Fujitsu please visit: http://www.fujitsu.com
```

## Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:
icpc

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml


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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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