SPEC® CINT2006 Result

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Fujitsu

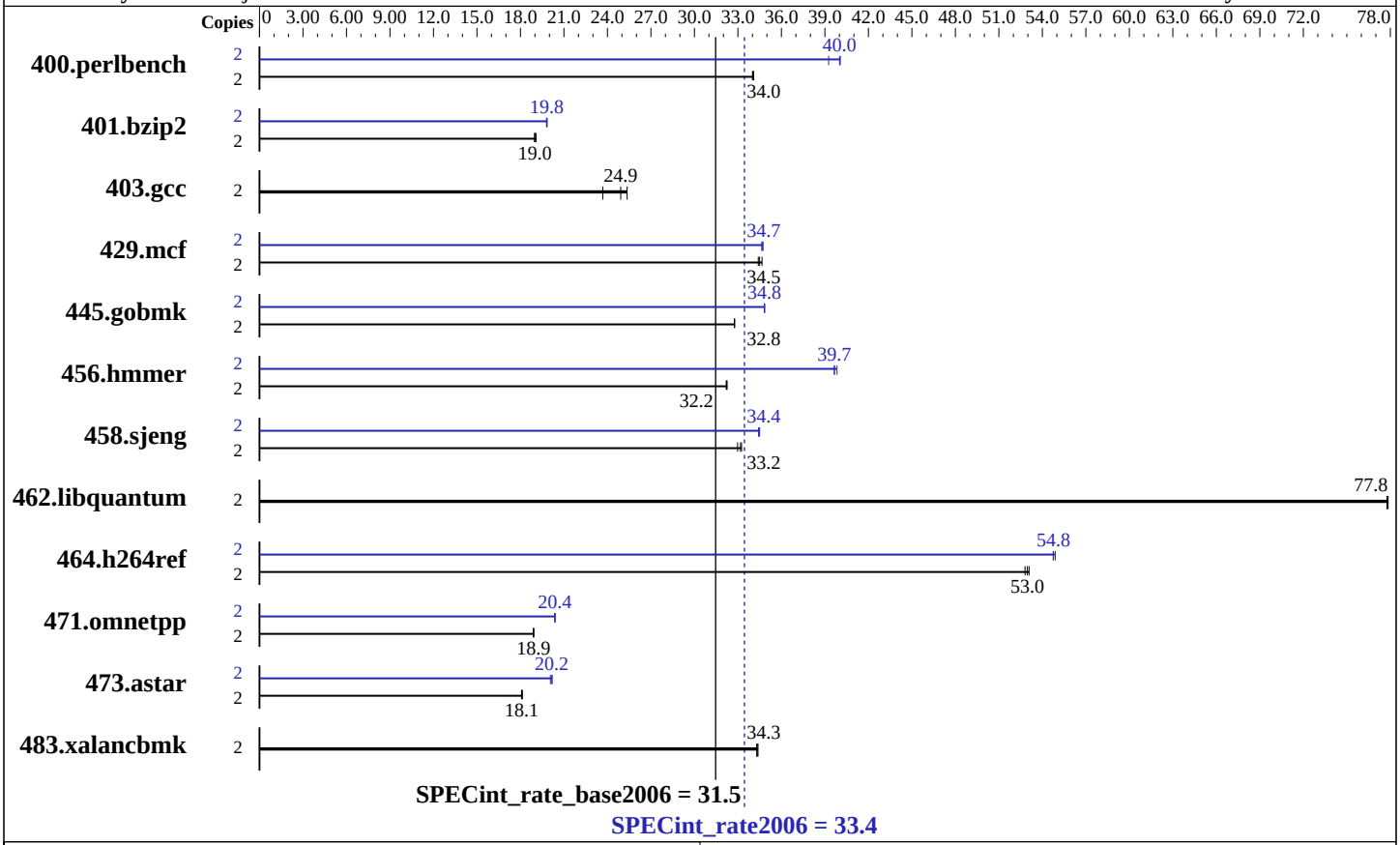
PRIMERGY TX150 S6, Intel Pentium E5200, 2.50 GHz

SPECint®_rate2006 = 33.4

SPECint_rate_base2006 = 31.5

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Feb-2009
Hardware Availability: Apr-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Pentium E5200
CPU Characteristics:	800 MHz system bus
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)
Disk Subsystem:	1 x SATA, 250 GB, 7200 rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	575	34.0	573	34.1	**574**	**34.0**	2	498	39.3	**488**	**40.0**	488	40.1
401.bzip2	2	**1016**	**19.0**	1017	19.0	1012	19.1	2	975	19.8	**974**	**19.8**	973	19.8
403.gcc	2	680	23.7	**646**	**24.9**	635	25.4	2	680	23.7	**646**	**24.9**	635	25.4
429.mcf	2	**529**	**34.5**	526	34.7	530	34.4	2	525	34.7	527	34.6	**526**	**34.7**
445.gobmk	2	640	32.8	**640**	**32.8**	640	32.8	2	**602**	**34.8**	602	34.8	602	34.8
456.hmmer	2	579	32.2	579	32.3	**579**	**32.2**	2	471	39.7	468	39.8	**471**	**39.7**
458.sjeng	2	728	33.3	734	33.0	**729**	**33.2**	2	701	34.5	703	34.4	**703**	**34.4**
462.libquantum	2	533	77.8	532	77.8	**533**	**77.8**	2	533	77.8	532	77.8	**533**	**77.8**
464.h264ref	2	834	53.1	**836**	**53.0**	838	52.8	2	809	54.7	**808**	**54.8**	806	54.9
471.omnetpp	2	662	18.9	661	18.9	**661**	**18.9**	2	613	20.4	613	20.4	**613**	**20.4**
473.astar	2	**776**	**18.1**	774	18.1	777	18.1	2	696	20.2	700	20.1	**696**	**20.2**
483.xalancbmk	2	401	34.4	**402**	**34.3**	402	34.3	2	401	34.4	**402**	**34.3**	402	34.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset has been used to bind processes to cores
```

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

General Notes

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For information about Fujitsu please visit: http://www.fujitsu.com
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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.03.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.03.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 00:48:03 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 April 2009.