SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX200 S5, Intel Xeon L5506, 2.13 GHz

$SPECint®_rate2006 = $ 141

$SPECint_rate_base2006 = $ 132

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Copies		
400.perlbench	8	147
	8	126
401.bzip2	8	78.1
	8	70.7
403.gcc	8	105
	8	105
429.mcf	8	172
445.gobmk	8	131
	8	122
456.hmmer	8	161
	8	119
458.sjeng	8	137
	8	127
462.libquantum	8	450
464.h264ref	8	197
	8	186
471.omnetpp	8	92.0
473.astar	8	86.6
	8	78.7
483.xalancbmk	8	151

SPECint_rate_base2006 = 132

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Hardware

CPU Name:	Intel Xeon L5506
CPU Characteristics:	
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-8500R, 2 rank, CL7-7-7, ECC)
Disk Subsystem:	1 x SATA, 250 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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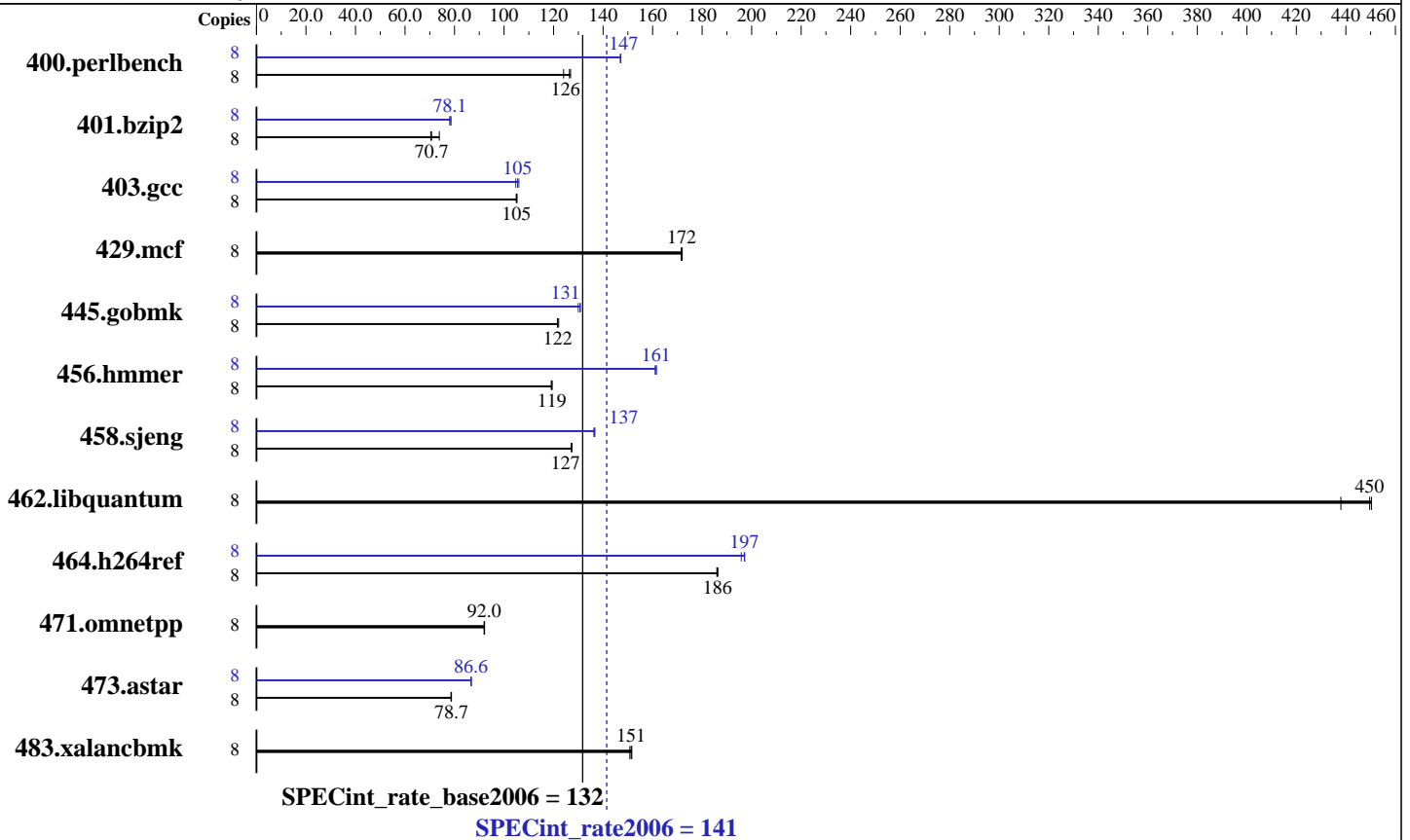
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Test date: Mar-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	630	124	616	127	**618**	**126**	8	**531**	**147**	531	147	532	147
401.bzip2	8	1045	73.8	**1092**	**70.7**	1097	70.4	8	988	78.1	982	78.6	**988**	**78.1**
403.gcc	8	612	105	614	105	**613**	**105**	8	615	105	608	106	**611**	**105**
429.mcf	8	425	172	424	172	**425**	**172**	8	425	172	424	172	**425**	**172**
445.gobmk	8	688	122	**690**	**122**	690	122	8	646	130	**642**	**131**	642	131
456.hmmer	8	627	119	625	119	**625**	**119**	8	462	161	464	161	**462**	**161**
458.sjeng	8	761	127	**761**	**127**	760	127	8	708	137	710	136	**709**	**137**
462.libquantum	8	378	438	368	450	**369**	**450**	8	378	438	368	450	**369**	**450**
464.h264ref	8	950	186	**951**	**186**	952	186	8	898	197	**898**	**197**	904	196
471.omnetpp	8	**543**	**92.0**	543	92.1	544	92.0	8	**543**	**92.0**	543	92.1	544	92.0
473.astar	8	**713**	**78.7**	715	78.5	713	78.7	8	**648**	**86.6**	648	86.6	647	86.8
483.xalancbmk	8	366	151	**365**	**151**	364	152	8	366	151	**365**	**151**	364	152

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

> 401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

> 456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

> 458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
```
icpc
```

> 473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

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Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -ansi-alias -opt-prefetch

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

         403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
                  -opt-malloc-options=3

         429.mcf: basepeak = yes

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                  -ipo -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                  -ansi-alias -auto-ilp32

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll4 -auto-ilp32

  462.libquantum: basepeak = yes

      464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
     471.omnetpp: basepeak = yes

       473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
                  -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

   483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
         403.gcc: -Dalloca=_alloca
```

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Test sponsor: Fujitsu	**Hardware Availability:** Apr-2009
Tested by: Fujitsu	**Software Availability:** Feb-2009

The flags file that was used to format this result can be browsed at

`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html`

You can also download the XML flags source by saving the following link:

`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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